

Marketing Bulletin

- DATE: Tuesday, February 01, 2000
- **TO:** Affected Customers
- FROM: Marketing
- **RE:** EC18 Series Termination

To all concerned parties,

This bulletin is to notify all customers of the discontinuation of the EC18 series Ecliptek oscillator effective Tuesday, February 01, 2000.

In compliance with our End of Life (EOL) policy, this notice will serve as advanced notice of product termination. New orders will not be accepted after Tuesday, February 01, 2000, with delivery to be conclude by Tuesday, February 01, 2000.

If there are any questions pertaining to this bulletin, please contact your Ecliptek sales representative. Thank you again for your cooperation.

Ecliptek Marketing

Para Para	STANDARD SPECIF	
Frequency Range:		number TEN08-016-102 for a list of qualified frequencies.)
Frequency Tolerance/Stability:	(All Values Inclusive of Temperature, Volta	ge, and Load)
00	±100ppm Max	0.0101111
45	±50ppm Max.	ORIGINAL
25	±25ppm Max. (0°C to +70°C only)	IF IN RED
Operating Temperature Range	0°C to +70°C	
	-40°C to +85°C	
Storage Temperature Range	-55°C to +125°C	
Aging (@ 25°C)	±5ppm/year Maximum	
Supply Voltage (VDD)	3.3Vdc ±10%	
Input Current (IDD)	≤ 100.000MHz 40mA Maximum, > 100.00	0MHz 50mA Maximum
Output Voltage Logic High (VOH)	VDD-0.4Vdc Min.	
Output Voltage Logic Low (VoL)	0.4Vdc Max	
Duty Cycle (@ 25°C)	50% ±10% (@ 50% of waveform)	
T	50% ±5% (@ 50% of waveform)	
Rise/Fall Time	3nSec Max. (10% to 90% of waveform)	
Load Drive Capability	15pF HCMOS Load Maximum	
Pin 1 Connection	and the second se	
Blank	No Connect	
TS	Tri-State (High Impedence)	
Fri-State Input Voltage (VIH & VIL)	+0.8Vdd Min. to Enable Output, +0.2Vdd I	fax. to Disable Output (High Impedence). No Connect to Enable Ou
Standby Current	10µA Maximum (Disabled Output = High I	npedance)
Start Up Time	10 mSec Maximum	
Jitter (Absolute)	±100pSec Maximum	
Jitter (One Sigma)	±25pSec Maximum	
		5011411041
	ENVIRONMENTAL & M	
Shock:	Conditions and Criteria Listed in TQC41-8	33-007 ; 50cm
Vibration:	Conditions and Criteria Listed in TQC41-8	33-008
Seal Integrity:	Conditions and Criteria Listed in TQC41-8	33-003 (Internal Crystal Only)
Solderability:	Conditions and Criteria Listed in TQC41-8	33-004 ; 95% coverage
Marking Permenancy:	Conditions and Criteria Listed in TQC41-8	
TS = Duty Cy Blank = T = 50% Operating Blank = 0°C ET = -40°C	50% ±10% ±5% remperature Range to 70°C to +85°C	6.7 ±0.2
Frequency Toleran 00 = ±100ppm Maxin 45 = ±50ppm Maxim 25 = ±25ppm Maxim MARKING G	num ium ium	PIN CONNECTION 1 No Connect or Tri-State 2 Ground/Case Ground
(Line #1) ECLIPTEK		3 Output ALL DIMENSIONS 4 Supply Voltage IN MILLIMETERS
(Line #2) XX.XXXM Frequency i	n MHz	SPECIFICATION CONTROL DRAWING
(Line #3) XX Y ZZ Week of Last Digit o Ecliptek Manuf	Year	Title 3.3Vdc J-Leaded Plastic Surface Mount Oscillato
NOTE: Marking shall conform to conditions listed in TQC41-001-000.		A 0.21-00
PACKAGING OPTIONS Blank = Bulk (CPA72-001-000)		SET MADE TOP 3
TR = Tape & Reel (CPA70-411-000)		and the second sec