

2STW1693

High power PNP epitaxial planar bipolar transistor

Features

- High breakdown voltage V_{CEO} = -80V
- Complementary to 2STW4466
- Typical f_t = 20MHz
- Fully characterized at 125 °C

Applications

Audio power amplifier

Description

The device is a PNP transistor manufactured in low voltage planar technology using base island layout. The resulting transistor shows good gain linearity coupled with low V_{CESAT} behaviour.

Recommended for 40W to 70W high fidelity audio frequency amplifier output stage.

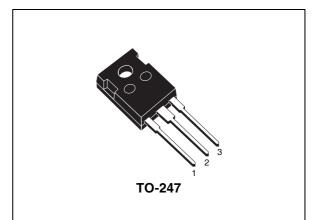


Figure 1. Internal schematic diagram

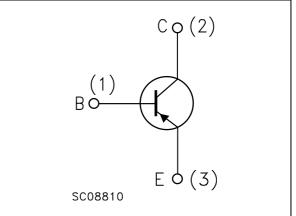


Table	1.	Device	summary
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Order code	Marking	Package	Packaging
2STW1693	2STW1693	TO-247	Tube

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1 Electrical ratings

Symbol	Parameter	Value	
V _{CBO}	Collector-emitter voltage (I _E = 0)	-100	V
V _{CEO}	Collector-emitter voltage (I _B = 0)	-80	V
V_{EBO}	Collector-base voltage $(I_C = 0)$	-6	V
۱ _C	Collector current	-6	А
I _{CM}	Collector peak current (t _P < 5ms)	-12	А
P _{TOT}	Total dissipation at $T_c = 25^{\circ}C$	60	W
T _{stg}	Storage temperature	-65 to 150	°C
Τ _J	Max. operating junction temperature	150	°C

Table 2. Absolute maximum rating

Table 3. Thermal data

Symbol	Parameter	Value	Unit
R _{thj-amb}	Thermal resistance junction-amb max	2.08	°C/W



2 Electrical characteristics

(T_{CASE} = 25°C; unless otherwise specified)

Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit
I _{CBO}	Collector cut-off current $(I_E = 0)$	V _{CB} = -80V			-0.1	μA
I _{EBO}	Emitter cut-off current $(I_{\rm C} = 0)$	V _{EB} = -6V			-0.1	μA
V _{(BR)EBO}	Collector-emitter breakdown voltage (I _C = 0)	I _E = -1mA	-6			V
V _{(BR)CBO}	Collector-emitter breakdown voltage (I _E = 0)	I _C = -100μA	-80			V
V _{(BR)CEO}	Collector-emitter breakdown voltage (I _B = 0)	I _C = -50mA	-80			V
V _{CE(sat)} ⁽¹⁾	Collector-emitter saturation voltage	$I_{C} = -2A \qquad I_{B} = -200 \text{mA}$ $I_{C} = -6A \qquad I_{B} = -600 \text{mA}$			-0.6 -1.5	V V
V _{BE} ⁽¹⁾	Base-emitter voltage	$V_{CE} = -5V$ $I_C = -5A$			-1.5	V
h _{FE}	DC current gain	$I_{\rm C} = -2A$ $V_{\rm CE} = -4V$	50		120	
f _T	Transition frequency	I _C = -0.5A V _{CE} = -12V		20		MHz
C _{CBO}	Collector-base capacitance	$I_E = 0$ $V_{CB} = -10V$ f = 1MHz		80		pF
t _{on}	Turn-on time	I _C = -3A V _{CC} = -30V		0.18		ns
t _{stg}	Storage time	$I_{B1} = -I_{B2} = -0.3A$		0.6		ns
t _{off}	Fall time			0.09		ns

Table 4. Electrical characteristics

1. Pulsed duration = 300 μ s, duty cycle \leq 1.5%



2.1 Electrical characteristics (curves)

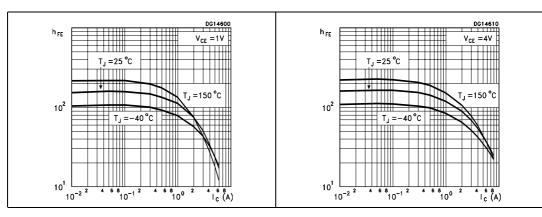


Figure 2. DC current gain

Figure 3. DC current gain

Figure 4. Collector-emitter saturation I voltage

° T_ =25 د T

10°

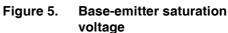
T_J =150 °C

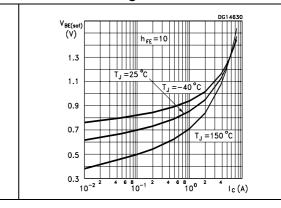
 $T_{J} = -40$

Ic(A)

 $h_{FE} = 10$

⁶ ⁸ 10⁻¹







V_{CE(sol)} (V)

100

10

10⁻²

10⁻²

Figure 7. Resistive load switching time

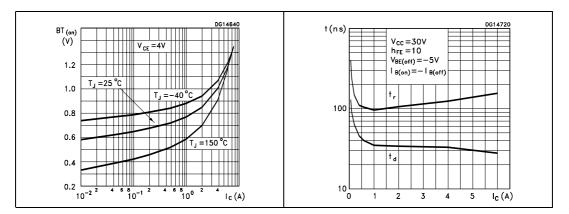
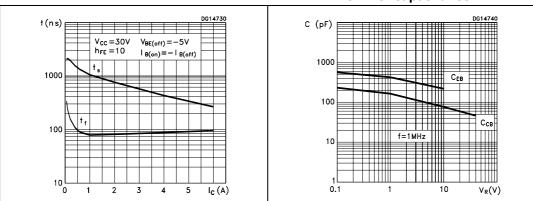


Figure 8. Switching time resistive load Figure 9. Collector-base and collectoremitter capacitance





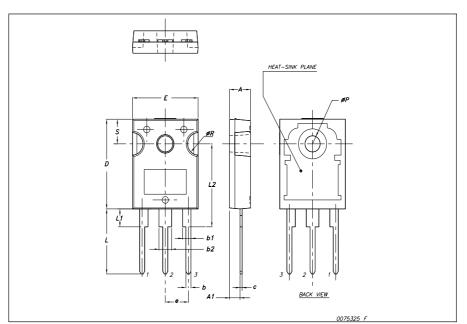
3 Package mechanical data

In order to meet environmental requirements, ST offers these devices in ECOPACK® packages. These packages have a Lead-free second level interconnect. The category of second level interconnect is marked on the package and on the inner box label, in compliance with JEDEC Standard JESD97. The maximum ratings related to soldering conditions are also marked on the inner box label. ECOPACK is an ST trademark. ECOPACK specifications are available at: www.st.com



Dim.	mm.		
	Min.	Тур	Max.
A	4.85		5.15
A1	2.20		2.60
b	1.0		1.40
b1	2.0		2.40
b2	3.0		3.40
С	0.40		0.80
D	19.85		20.15
E	15.45		15.75
е		5.45	
L	14.20		14.80
L1	3.70		4.30
L2		18.50	
øР	3.55		3.65
øR	4.50		5.50
S		5.50	

TO-247 Mechanical data





4 Revision history

 Table 5.
 Document revision history

Date	Revision	Changes
10-Oct-2007	1	Initial release



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