

S25xx Model
8 Pin Dip, 2.5V, LVCMOS



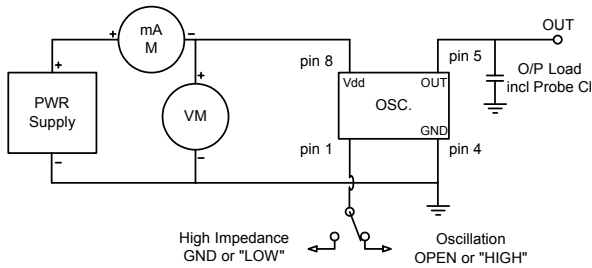
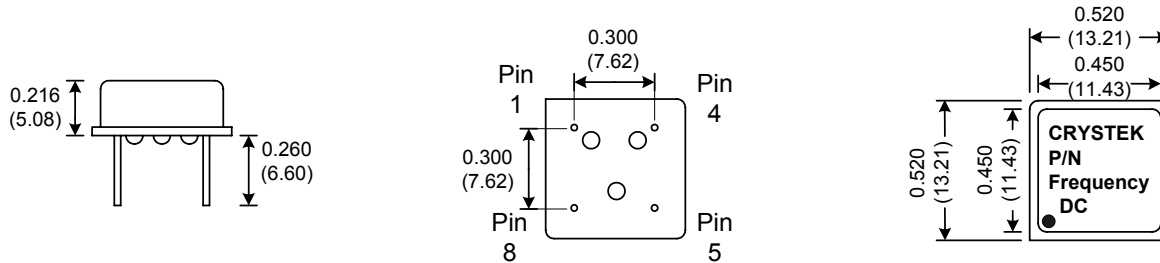
Clock Oscillator



Frequency Range: 1.544MHz to 70MHz
Frequency Stability: ±20ppm to ±100ppm
Temperature Range:
 Operating: -10°C to 70°C
 (Option) -40°C to 85°C
Storage: -55°C to 120°C
Input Voltage: 2.5V ± 0.125V
Input Current: 35mA Max
Output: LVCMOS
 Symmetry: 45/55% Max @ 50% Vdd
 Rise/Fall Time: 10ns Max @ 20% to 80%
 Logic: "0" = 10% Vdd Max
 "1" = 90% Vdd Min
 Load: 15pF
Jitter RMS: 5ps Typ, 10ps Max

Designed to meet today's requirements for low voltage applications. The S25xx Series uses only fundamental and 3rd overtone crystal designs for excellent jitter performance.

Aging: <3ppm 1st/yr, 1ppm every year thereafter



Crystek Part Number Guide		
Example: S2592-44.736		
Example: SE2592-44.736		
Temperature		Frequency Stability
-10/ 70°C	-40/ 85°C	
S2590	SE2590	+/- 100ppm
S2592	SE2592	+/- 50ppm
S2591	SE2591	+/- 25ppm
S2598	NA	+/- 20ppm

Tri-State Function	
Function pin 1	Output pin
Open	Active
"1" level 2.2V Min	Active
"0" level 0.4V Max	High Z

Specifications subject to change without notice.

TD-021012 Rev. C