

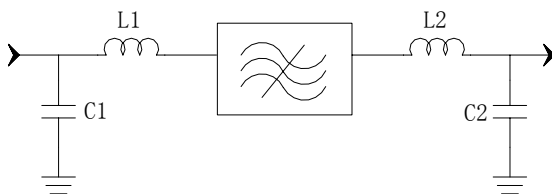
Specifications

Parameter	Unit	Minimum	Typical	Maximum
Center Frequency	MHz	168.92	169	169.08
Insertion Loss	dB	-	28.4	29
1 dB Bandwidth	MHz	3.8	3.84	-
3 dB Bandwidth	MHz	-	3.98	-
30 dB Bandwidth	MHz	-	4.45	4.5
40 dB Bandwidth	MHz	-	4.53	5
Passband Variation	dB	-	0.4	0.9
Group Delay Variation($f_0 \pm 1.8\text{MHz}$)	nsec	-	100	300
Absolute Delay	usec	-	4.5	-
Ultimate Rejection (outside $f_0 \pm 3\text{MHz}$)	dB	45	49	-
Material Temperature coefficient	KHz/°C	0.169		
Ambient Temperature	°C	25		
Package Size	DIP3512(35.0x12.8x4.7mm3)			

Notes:

1. All specifications are based on the test circuit shown
2. In production, devices will be tested at room temperature to a guardbanded specification to ensure electrical compliance over temperature
3. Electrical margin has been built into the design to account for the variations due to temperature drift and manufacturing tolerances
4. This is the optimum impedance in order to achieve the performance show

Matching Configuration



L1=39nH L2=33nH
C1=C2=39pF
Source/Load Impedance=50 ohm

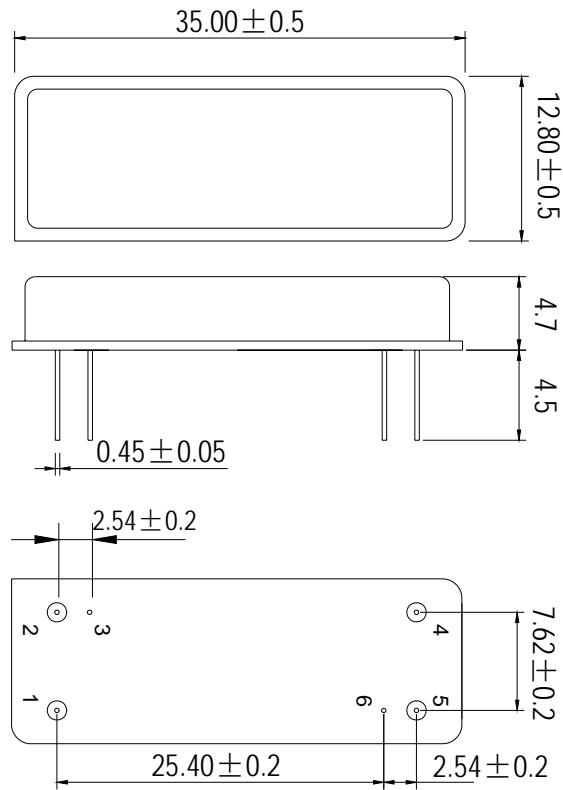
Notes - Component values may change depending on board layout.



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Package Dimension



Input:1
Output:5

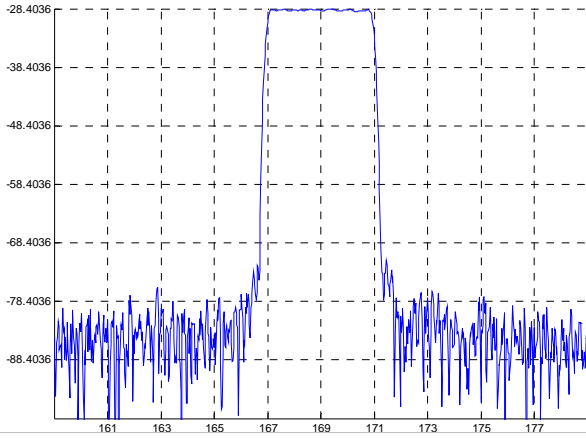


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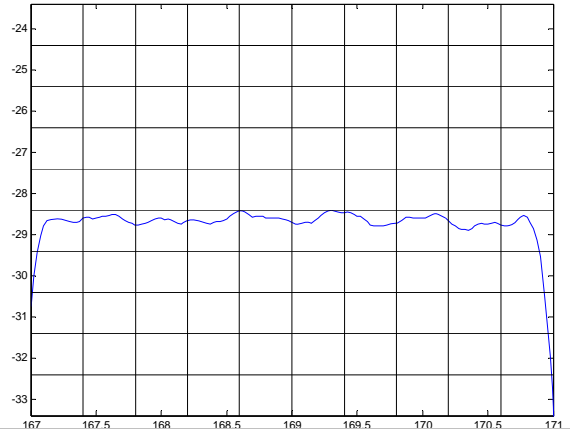
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Typical Performance

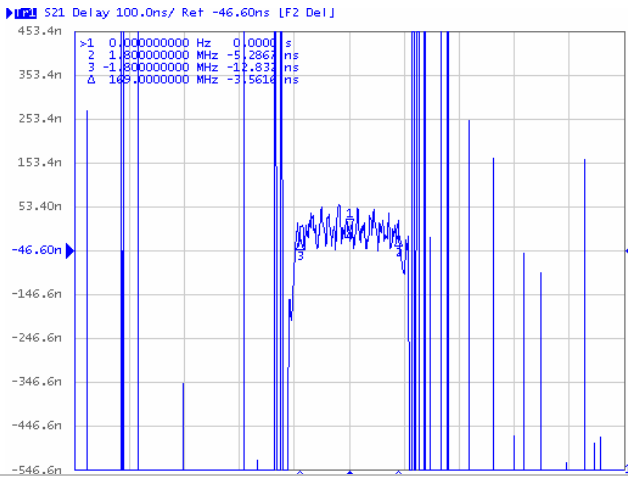
Frequency Respond



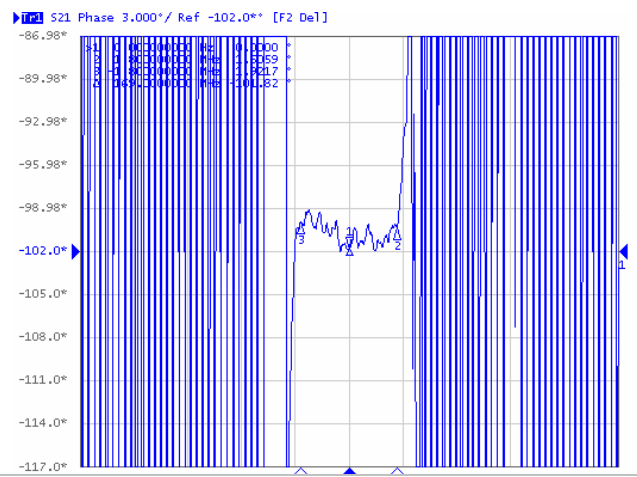
Passband Respond



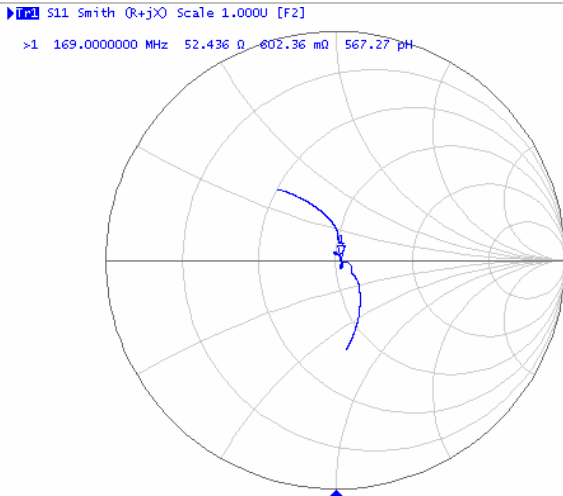
Group Delay Variation($f_0 \pm 1.8\text{MHz}$)



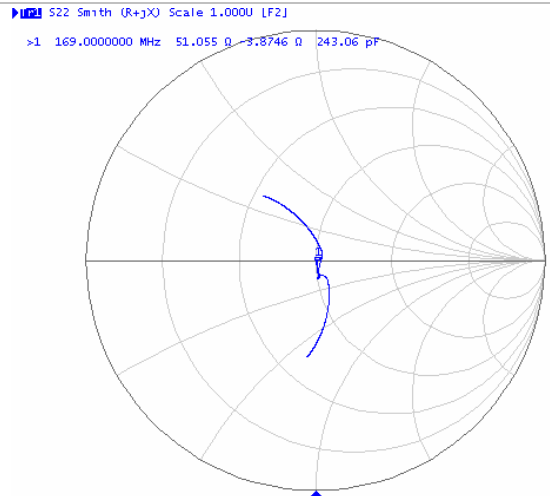
Phase Linearity($f_0 \pm 1.8\text{MHz}$)



Smith Chart S11



Smith Chart S22



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