

Vishay High Power Products

Ultrafast Rectifier, 2 x 8 A FRED Pt[™]



- Ultrafast recovery time
- · Low forward voltage drop
- Low leakage current
- 175 °C operating junction temperature
- Lead (Pb)-free ("PbF" suffix)
- Designed and qualified for industrial level

DESCRIPTION/APPLICATIONS

FRED Pt[™] series are the state of the art ultrafast recovery rectifiers specifically designed with optimized performance of forward voltage drop and ultrafast recovery time.

The planar structure and the platinum doped life time control, guarantee the best overall performance, ruggedness and reliability characteristics.

These devices are intended for use in the output rectification stage of SMPS, UPS, dc-to-dc converters as well as freewheeling diode in low voltage inverters and chopper motor drives.

Their extremely optimized stored charge and low recovery current minimize the switching losses and reduce over dissipation in the switching element and snubbers.

ABSOLUTE MAXIMUM R	ATINGS				
PARAMETER		SYMBOL TEST CONDITIONS		VALUES	UNITS
Peak repetitive reverse voltage		V _{RRM}		400	V
Average restified forward aurrent	per leg			8	
Average rectified forward current	total device	I _{F(AV)}	T_{C} = 155 °C, rated V_{R}	16	•
Non-repetitive peak surge current		I _{FSM}	T _C = 25 °C	100	A
Peak repetitive forward current		I _{FRM}	T_{C} = 155 °C, rated V_{R} , square wave, 20 kHz	16	
Operating junction and storage temperatures		T _J , T _{Stg}		- 65 to 175	°C

ELECTRICAL SPECIFICATIONS PER LEG ($T_J = 25 \text{ °C}$ unless otherwise specified)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Breakdown voltage, blocking voltage	V _{BR} , V _R	I _R = 100 μA	400	-	-	
Forward voltage	V _F	I _F = 8 A	-	1.19	1.3	V
		I _F = 8 A, T _J = 150 °C	-	0.94	1.0	
Reverse leakage current I _R		$V_R = V_R$ rated	-	0.2	10	
	IR	$T_J = 150 \text{ °C}, V_R = V_R \text{ rated}$	-	20	500	μA
Junction capacitance	CT	V _R = 400 V - 14		-	pF	
Series inductance	L _S	Measured lead to lead 5 mm from package body	-	8.0	-	nH

* Pb containing terminations are not RoHS compliant, exemptions may apply





TO-220AB

PRODUCT SUMMARY				
t _{rr}	60 ns			
I _{F(AV)}	2 x 8 A			
V _R	400 V			

Anode 👌

Base common

cathode

2 Q

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2 Common cathode

O Anode

16CTU04PbF

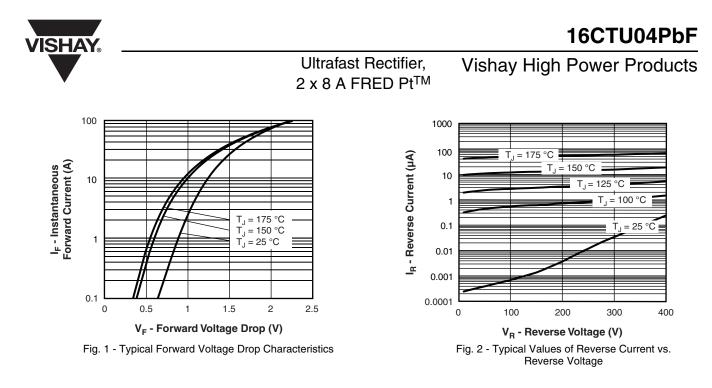
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DYNAMIC RECOVERY CHARACTERISTICS PER LEG (T _J = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNITS
		$I_F = 1.0 \text{ A}, \text{ d}I_F/\text{d}t = 50 \text{ A}/\mu\text{A}, V_R = 30 \text{ V}$		-	35	60	
Reverse recovery time t _{rr}	t _{rr}	T _J = 25 °C		-	43	-	ns
		T _J = 125 °C	I _F = 8 A dI _F /dt = 200 A/μs V _R = 200 V	-	67	-	
Peak recovery current	I _{RRM}	T _J = 25 °C		-	2.8	-	^
		T _J = 125 °C		-	6.3	-	A
Reverse recovery charge	Q _{rr}	T _J = 25 °C		-	60	-	nC
		T _J = 125 °C		-	210	-	nc

THERMAL - MECHANICAL SPECIFICATIONS							
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS	
Maximum junction and storage temperature range	T _J , T _{Stg}		- 65	-	175	°C	
Thermal resistance, junction to case	R _{thJC}		-	1.8	2		
Thermal resistance, junction to ambient	R _{thJA}	Typical socket mount	-	-	50	°C/W	
Thermal resistance, case to heatsink	R _{thCS}	Mounting surface, flat, smooth and greased	-	0.5	-		
Weight			-	2.0	-	g	
weight			-	0.07	-	oz.	
Mounting torque			6.0 (5.0)	-	12 (10)	kgf ⋅ cm (lbf ⋅ in)	
Marking device		Case style TO-220AB	16CTU04				



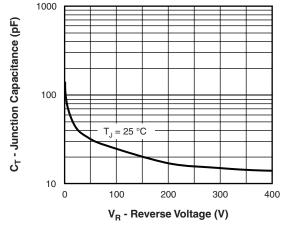


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

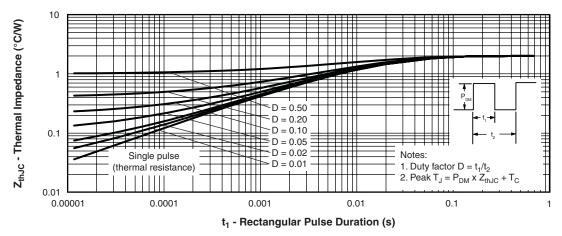


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics

16CTU04PbF

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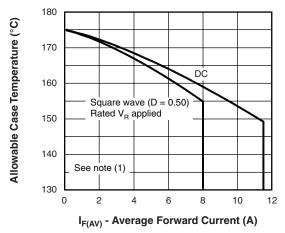
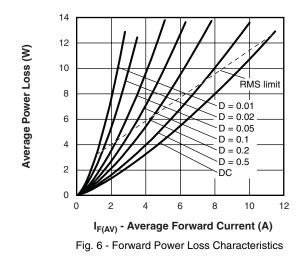


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current



Note

- ⁽¹⁾ Formula used: $T_C = T_J (Pd + Pd_{REV}) \times R_{thJC}$;
- $\begin{array}{l} \mbox{Pd} = \mbox{Forward power loss} = \mbox{I}_{F(AV)} \times \mbox{V}_{FM} \mbox{ at } (\mbox{I}_{F(AV)}/\mbox{D}) \mbox{ (see fig. 6);} \\ \mbox{Pd}_{REV} = \mbox{Inverse power loss} = \mbox{V}_{R1} \times \mbox{I}_{R} \mbox{ (1 D); } \mbox{I}_{R} \mbox{ at } \mbox{V}_{R1} = \mbox{Rated} \mbox{V}_{R} \end{array}$

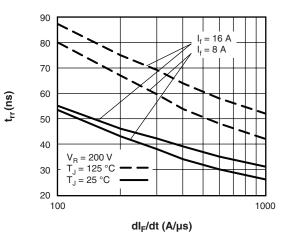
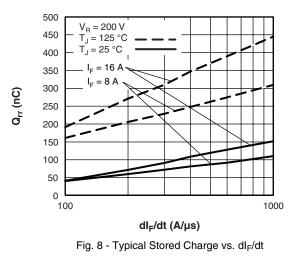


Fig. 7 - Typical Reverse Recovery Time vs. dl_F/dt





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$V_{R} = 200 V$ $L = 70 \mu H$ $U_{R} = 200 V$ $U_{R} =$

Fig. 9 - Reverse Recovery Parameter Test Circuit

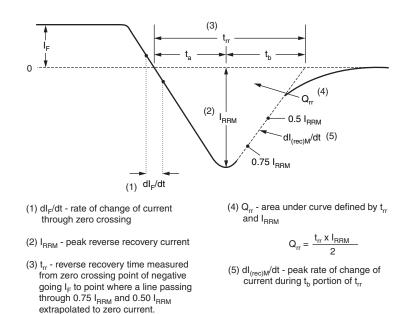


Fig. 10 - Reverse Recovery Waveform and Definitions

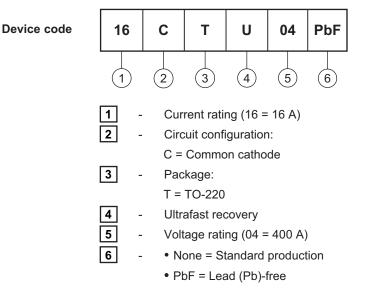
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ORDERING INFORMATION TABLE



Tube standard pack quantity: 50 pieces

LINKS TO RELATED DOCUMENTS				
Dimensions http://www.vishay.com/doc?95222				
Part marking information	http://www.vishay.com/doc?95225			



Vishay

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