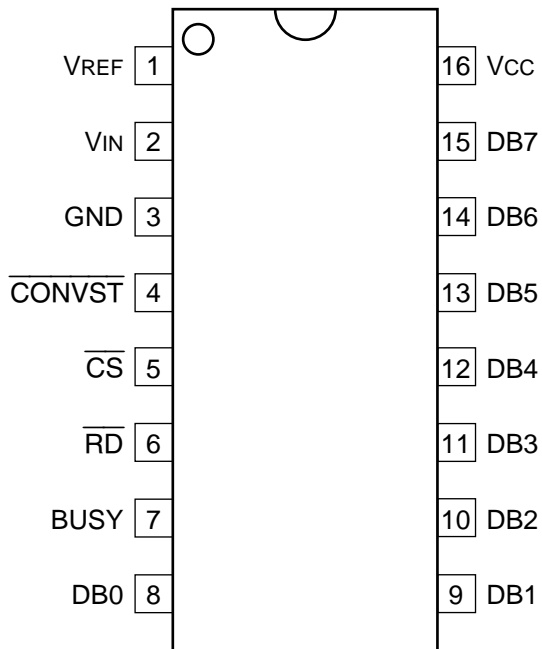


8-BIT SAMPLING AD CONVERTER

—TOP VIEW—

**INPUTS** $\overline{\text{CONVST}}$: CONVERT START $\overline{\text{CS}}$: CHIP SELECT $\overline{\text{RD}}$: READ

VIN : ANALOG SIGNAL

VREF : REFERENCE

OUTPUTS

BUSY : ADC BUSY

DB0 - DB7 : DATA BIT

