

# RJL5020DPK

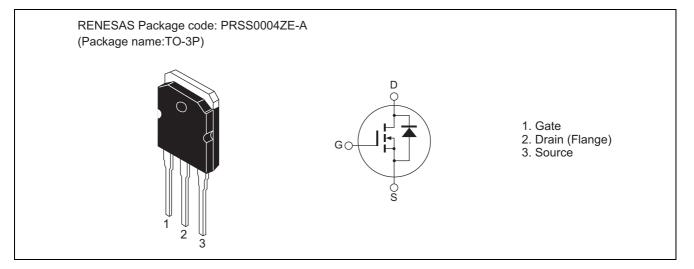
Silicon N Channel MOS FET High Speed Power Switching

> REJ03G1733-0400 Rev.4.00 Aug 29, 2008

# Features

- Built-in fast recovery diode
- Low on-resistance
- Low leakage current
- High speed switching

# Outline



# **Absolute Maximum Ratings**

			$(Ta = 25^{\circ}C)$
Item	Symbol	Ratings	Unit
Drain to source voltage	V <sub>DSS</sub>	500	V
Gate to source voltage	V <sub>GSS</sub>	±30	V
Drain current	ID	38	А
Drain peak current	Note1 D (pulse)	114	А
Body-drain diode reverse drain current	I <sub>DR</sub>	38	А
Body-drain diode reverse drain peak current	Note1 I <sub>DR (pulse)</sub>	114	А
Avalanche current	I <sub>AP</sub> <sup>Note3</sup>	12.5	А
Avalanche energy	E <sub>AR</sub> <sup>Note3</sup>	8.6	mJ
Channel dissipation	Pch <sup>Note2</sup>	200	W
Channel to case thermal impedance	0ch-c	0.625	°C/W
Channel temperature	Tch	150	°C
Storage temperature	Tstg	-55 to +150	°C

Notes: 1. PW  $\leq$  10  $\mu s,$  duty cycle  $\leq$  1%

- 2. Value at Tc =  $25^{\circ}$ C
- 3. STch =  $25^{\circ}$ C, Tch  $\leq 150^{\circ}$ C

# **Electrical Characteristics**

						$(Ta = 25^{\circ}C)$
Item	Symbol	Min	Тур	Max	Unit	Test conditions
Drain to source breakdown voltage	V <sub>(BR)DSS</sub>	500		_	V	$I_D = 10 \text{ mA}, V_{GS} = 0$
Zero gate voltage drain current	I <sub>DSS</sub>	—	—	10	μΑ	$V_{DS} = 500 \text{ V}, \text{ V}_{GS} = 0$
Gate to source leak current	I <sub>GSS</sub>	_		±0.1	μΑ	$V_{GS} = \pm 30 \text{ V}, V_{DS} = 0$
Gate to source cutoff voltage	V <sub>GS(off)</sub>	1.5		4.0	V	$V_{DS} = 10 \text{ V}, I_D = 1 \text{ mA}$
Static drain to source on state resistance	R <sub>DS(on)</sub>	_	0.105	0.135	Ω	$I_D = 19 \text{ A}, V_{GS} = 10 \text{ V}^{Note4}$
Input capacitance	Ciss	_	4750	_	pF	V <sub>DS</sub> = 25 V
Output capacitance	Coss	_	520	_	pF	V <sub>GS</sub> = 0 f = 1 MHz
Reverse transfer capacitance	Crss		61		pF	
Turn-on delay time	t <sub>d(on)</sub>	_	45	_	ns	I <sub>D</sub> = 19 A
Rise time	tr	_	90	_	ns	$V_{GS} = 10 V$ $R_L = 13.2 \Omega$ $Rg = 10 \Omega$
Turn-off delay time	t <sub>d(off)</sub>	_	215	_	ns	
Fall time	t <sub>f</sub>	_	154	_	ns	
Total gate charge	Qg	—	140	_	nC	V <sub>DD</sub> = 400 V
Gate to source charge	Qgs	_	19	_	nC	V <sub>GS</sub> = 10 V I <sub>D</sub> = 38 A
Gate to drain charge	Qgd	_	57	_	nC	
Body-drain diode forward voltage	V <sub>DF</sub>	_	0.94	1.60	V	$I_F = 38 \text{ A}, V_{GS} = 0^{Note4}$
Body-drain diode reverse recovery time	t <sub>rr</sub>	_	170	_	ns	$I_F = 38 \text{ A}, V_{GS} = 0$ $di_F/dt = 100 \text{ A}/\mu\text{s}$

Notes: 4. Pulse test

# Package Dimensions

Package Name TO-3P	JEITA Package Code SC-65	RENESAS Code	Previous Code	MASS[Typ.]	
10-3P	SC-65	PRSS0004ZE-A 15.6 ± 0.3	TO-3P/TO-3PV	5.0g 4.8 ± 0.2 1.5 2.8 0.6 ± 0.2	Unit: mm
	<u>5.45 ± 0</u>		<u>.0</u> <u>.0</u> <u>.5.45 ± 0.5</u>		

# **Ordering Information**

Part No.	Quantity	Shipping Container
RJL5020DPK-00-T0	360 pcs	Box (Tube)

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