

S78DL33L

unit : mm

Low Drop output Voltage Regulator

Descriptions

• Three Terminal Positive Low Dropout Voltage Regulator

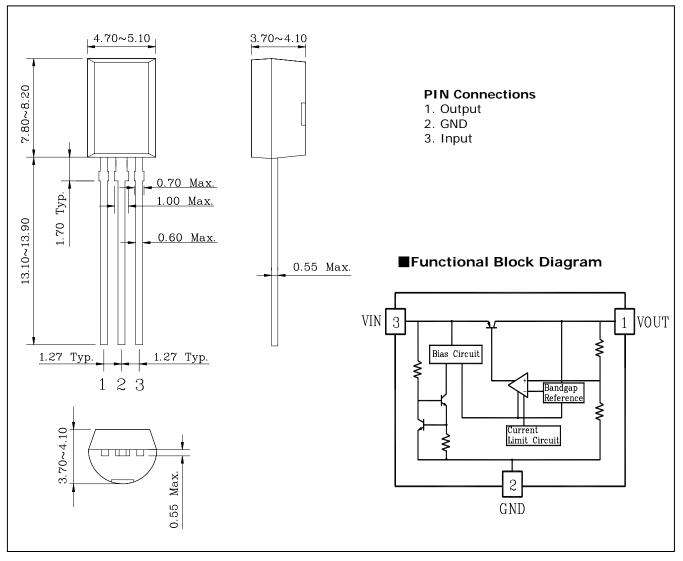
Features

- Low Standby Current Consumption (500 μ Typ.)
- Maximum Output Current (180 Max.)
- Less I/O voltage Difference (250 mV Max.)

Ordering Information

Type NO.	Marking	Package Code		
S78DL33L	S78DL33L	TO-92L		

Outline Dimensions



1

S78DL33L

Maximum ratings

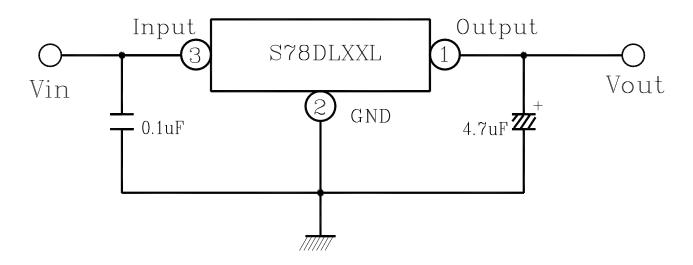
Maximum ratings 1			
Characteristic	Symbol	Ratings	Unit
Operating Input voltage	V _{IN}	16	V
Power Dissipation	P _D	1	W
Operating Temperature Range	T _{OPR}	-40~+85	°C
Junction Temperature	Tj	150	°C
Storage Temperature Range	T _{stg}	-55~150	°C
Lead Temperature Time	T _{sol}	260 (10 Sec)	٥C

Electrical Characteristics

($V_{IN} = 4.3V, I_{OUT} = 100uA, T_j = 25^{\circ}C$)

Characteristic	Symbol	Test Condition	Min.	Тур.	Max.	Unit
Output voltage	V _{OUT}	$V_{IN} = 4.3V$, $I_{OUT} = 100uA$	3.168	3.3	3.432	V
Voltage Regulation	∆ V _{OUT} (1)	$V_{IN} = 4.3V \sim 10V, I_{OUT} = 100uA$	-	2	15	mV
Load Regulation	∆ V _{OUT} (2)	$V_{IN}{=}4.3V$, $I_{OUT}{=}1{\sim}100mA$	-	7	28	mV
Dropout Voltage	V	I _{OUT} =50mA	-	110	230	mV
	V _{DROP}	I _{OUT} =100mA	-	150	300	
Ripple Rejection Ratio	RR	f=100Hz, I _{OUT} =100uA	-	75	-	dB
Ground pin Current		$V_{\rm IN}{=}4.3V$, $I_{\rm OUT}{=}100uA$		200	400	uA
	I _{GND}	V_{IN} =4.3V , I_{OUT} =50mA		0.9	1.8	mA
		$V_{\rm IN}{=}4.3V$, $I_{\rm OUT}{=}100mA$		2.1	4	mA

Test circuit



Electrical Characteristic Curves

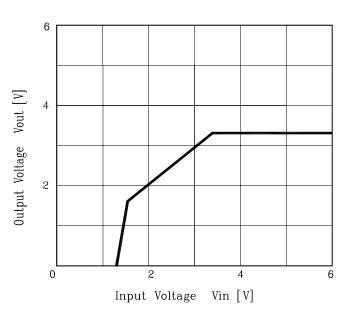
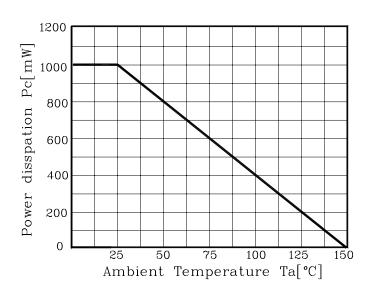


Fig. 1. Vin - Vout





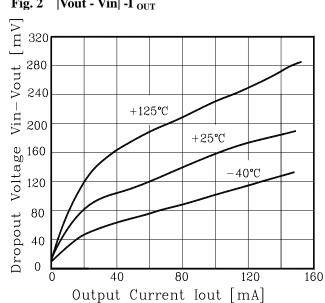
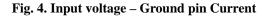
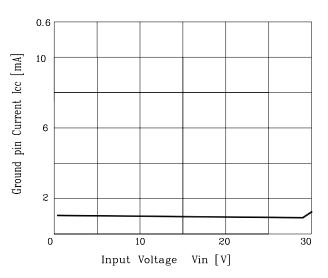


Fig. 2 |Vout - Vin| -I _{OUT}





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