

#### Linear Hall Effect Sensor IC in CMOS technology

# Release Notes: Revision bars indicate significant changes to the previous edition.

### Features:

The linear CMOS Hall Sensor is used for precise measurements of the magnetic flux. The differential output voltage is proportional to the magnetic flux density at a right angle to the sensitive area. Due to chopper compensation, low magnetic offset and offset drift is achieved. It can be used as a current sensor or can detect any mechanical movement. Very accurate angle measurements or distance measurements can be done. The sensor is very robust and can be used in an electrically and mechanically hostile environment.

- low magnetic offset
- extremely sensitive
- 4.8 to 12 Volt operation
- wide temperature range  $T_A = -40$  to +150  $^{\circ}C$
- over-voltage protection
- differential output
- accurate absolute measurements of DC and low frequency magnetic flux densities
- on-chip temperature compensation
- low 1/f-noise

#### Specifications

#### Marking Code

Туре	Temperature Range				
	Α	E	С		
HAL400S	400A	400E	400C		

#### **Operating Junction Temperature Range**

**A:**  $T_J = -40 \degree C$  to +170  $\degree C$  **E:**  $T_J = -40 \degree C$  to +100  $\degree C$ **C:**  $T_J = 0 \degree C$  to +100  $\degree C$ 

#### **Designation of Hall Sensors**





#### Example: HAL400S-E

- $\rightarrow$  Type: 400
- $\rightarrow$  Package: SOT-89A
- $\rightarrow$  Temperature Range: T\_J = -40 °C to +100 °C

#### Solderability

- Package SOT-89A: according to IEC68-2-58



Fig. 1: Pin configuration

#### **Functional Description**



Fig. 2: Block diagram of the HAL 400 (top view)

The Linear Hall Sensor measures accurate constant and low frequency magnetic flux densities. The differential output voltage is proportional to the magnetic flux density passing vertically through the sensitive area of the chip. The common mode voltage (average of the voltages on pin 2 and pin 3) of the differential output amplifier is a constant 2.2 V.

The differential output voltage consists of two components due to the switching offset compensation technique. The average of the differential output voltage represents the magnetic flux density. This component is overlaid by an differential AC signal at a typical frequency of 147 kHz. The AC signal represents the internal offset voltages of amplifiers and hall plates, that are influenced by mechanical stress and temperature cycling. External filtering or integrating measurement can be done to eliminate the AC component of the signal. So the influence of mechanical stress and temperature cycling is suppressed. No adjustment of magnetic offset is needed.

The sensitivity is stabilized over a wide range of temperature and supply voltage due to internal voltage regulation and circuits for temperature compensation.

### Offset Compensation (see Fig. 3)

The Hall Offset Voltage is the residual voltage measured in absence of a magnetic field (zero-field residual voltage). This voltage is caused by mechanical stress and can be modeled by a displacement of the connections for voltage measurement and/or current supply.

Compensation of this kind of offset is done by cyclic commutating the connections for current flow and voltage measurement.

- First cycle:

The hall supply current flows between the points 4 and 2. In the absence of a magnetic field V<sub>13</sub> is the Hall Offset Voltage (+V<sub>Offs</sub>). In case of a magnetic field, V<sub>13</sub> is the sum of the Hall voltage (V<sub>H</sub>) and V<sub>Offs</sub>. V<sub>13</sub> = V<sub>H +</sub> V<sub>Offs</sub>

- Second cycle:

The hall supply current flows between the points 1 and 3. In the absence of a magnetic field V<sub>24</sub> is the Hall Offset Voltage with negativ polarity ( $-V_{Offs}$ ). In case of a magnetic field, V<sub>24</sub> is the difference of the Hall voltage (V<sub>H</sub>) and V<sub>Offs</sub>. V<sub>24</sub> = V<sub>H</sub> - V<sub>Offs</sub>

The output shows in the first cycle the sum of the Hall voltage and the offset, in the second the difference of both. The difference of the mean values of V<sub>OUT1</sub> and V<sub>OUT2</sub> (V<sub>OUTDIF</sub>) is equivalent to V<sub>Hall</sub>.



a) Offset Voltage

Fig. 3: Hall Offset Compensation



**b)** Switched Current Supply



#### **Outline Dimensions**



#### Fig. 4: Plastic Package SOT-89A

Weight approximately 0.04 g Dimensions in mm

#### **Absolute Maximum Ratings**

Symbol	Parameter	Pin No.	Min.	Max.	Unit
V <sub>DD</sub>	Supply Voltage	1	–15	12	V
I <sub>DDZ</sub>	Supply Current through Protection Device	1	-400 <sup>1)</sup>	400 <sup>1)</sup>	mA
I <sub>OUT</sub>	Output Current	2, 3	-5	5	mA
I <sub>OUTZ</sub>	Output Current through Protection Device	1	-300 <sup>1)</sup>	300 <sup>1)</sup>	mA
T <sub>S</sub>	Storage Temperature Range		-65	150	°C
TJ	Junction Temperature Range		-40 -40	150 170 <sup>2)</sup>	°C ℃
<sup>1)</sup> t $\leq$ 2 ms <sup>2)</sup> t < 1000 h					

Stresses beyond those listed in the "Absolute Maximum Ratings" may cause permanent damage to the device. This is a stress rating only. Functional operation of the device at these or any other conditions beyond those indicated in the "Recommended Operating Conditions/Characteristics" of this specification is not implied. Exposure to absolute maximum ratings conditions for extended periods may affect device reliability.

#### **Recommended Operating Conditions**

Symbol	Parameter	Pin No.	Min.	Max.	Unit	Remarks
I <sub>OUT</sub>	Continuous Output Current	2, 3	-2.25	2.25	mA	T <sub>J</sub> = 25 °C
I <sub>OUT</sub>	Continuous Output Current	2, 3	-1	1	mA	T <sub>J</sub> = 170 °C
CL	Load Capacitance	2, 3	-	1	nF	



Fig. 5: Recommended Operating Supply Voltage

#### **Extended Operational Range**

Within the extended operating range, the ICs operate as mentioned in the functional description. The functionality has been tested on samples, whereby the characteristics may lie outside the specified limits.

Symbol	Parameter	Pin No.	Min.	Max.	Unit	Remarks
V <sub>DD</sub>	Supply Voltage	1	4.3	12	V	
I <sub>OUT</sub>	Output Current	2,3	-3	3	mA	T <sub>J</sub> = -40 °C to +170 °C

**Electrical and Magnetic Characteristics** see Fig. 5 for T<sub>A</sub> and V<sub>DD</sub> as not otherwise specified; Typical characteristics for T<sub>J</sub> = 25 °C, –75 mT < B < 75 mT and V<sub>DD</sub> = 6.8 V

Symbol	Parameter	Pin No.	Min.	Тур.	Max.	Unit	Test Conditions
I <sub>DD</sub>	Supply Current	1	11.8	14.5	17.1	mA	T <sub>J</sub> = 25 °C; I <sub>OUT1,2</sub> = 0 mA
I <sub>DD</sub>	Supply Current under Recom- mended Operating Conditions	1	9.3	14.5	18.5	mA	I <sub>OUT1,2</sub> = 0 mA
V <sub>CM</sub>	Common Mode Output Voltage	2, 3	2.1	2.2	2.3	V	I <sub>OUT1,2</sub> = 0 mA V <sub>CM</sub> = (V <sub>OUT1</sub> + V <sub>OUT2</sub> ) / 2
CMRR	Common Mode Rejection Ratio	2, 3	-2	0	2	mV/V	I <sub>OUT1,2</sub> = 0 mA CMRR is limited by the influ- ence of power dissipation
$S_B = \Delta V_{OUTDIF} / \Delta B$	Differential Magnetic Sensitivity	2–3	37	42.5	49.5	mV/mT	$B = \pm 60 \text{ mT} \text{ ; } T_J = 25 \text{ °C}$ $V_{OUTDIF} = V_{OUT1} - V_{OUT2}$
S <sub>B</sub>	Differential Magnetic Sensitivity under Recommended Operating Conditions	2–3	33	42.5	49.5	mV/mT	$B = \pm 60 \text{ mT}$ V <sub>OUTDIF</sub> = V <sub>OUT1</sub> - V <sub>OUT2</sub>
B <sub>offset</sub>	Magnetic Offset	2–3	-1.0	-0.2	1.0	mT	B = 0 mT, I <sub>OUT1,2</sub> = 0 mA T <sub>J</sub> = 25 °C
B <sub>offset</sub>	Magnetic Offset over Tempera- ture	2–3	-1.25	-0.2	1.25	mT	B = 0 mT, I <sub>OUT1,2</sub> = 0 mA
$\Delta B_{OFFSET}/\Delta T$	Magnetic Offset Change due to $T_{A}$		-15	0	15	μΤ/Κ	B = 0 mT, I <sub>OUT1,2</sub> = 0 mA
BW	Bandwidth (–3 dB)	2–3	_	10	-	kHz	1)
NL <sub>dif</sub>	Non Linearity of Differential Output	2–3	_	0.2	1	%	B = ±40 mT, B = ±60 mT
NL <sub>single</sub>	Non Linearity of Single Ended Output	2, 3	-	2	3	%	
f <sub>CH</sub>	Chopper Frequency	2, 3	114	147	166	kHz	T <sub>J</sub> = 25 °C
f <sub>CH</sub>	Chopper Frequency over Temp.	2, 3	90	147	166	kHz	
V <sub>OUTACpp</sub>	Peak-to-Peak AC Output Voltage	2, 3	0	0.32	0.8	V	
n <sub>meff</sub>	Magnetic RMS Differential Broadband Noise	2–3	_	10	-	μΤ	BW = 10 Hz to 10 kHz
f <sub>Cflicker</sub>	Corner Frequency of 1/f Noise	2–3	-	10		Hz	B = 0 mT
f <sub>Cflicker</sub>	Corner Frequency of 1/f Noise	2–3	-	100		Hz	B = 65 mT
R <sub>OUT</sub>	Output Resistance	2, 3	0	30	50	Ω	$I_{OUT1,2} \le 2.5 \text{ mA}; T_{J} = 25 \degree C$ $V_{DD} = 6.8 \text{ V}$
R <sub>OUT</sub>	Output Resistance over Temperature	2, 3	0	30	150	Ω	$I_{OUT1,2} \le 2.5 \text{ mA}$
R <sub>thJSB</sub> case	Thermal Resistance Junction to Substrate Backside		_	150	200	K/W	Fiberglass Substrate 30 mm x 10 mm x 1.5 mm pad size see Fig. 6









































### **Application Circuits**

The normal integrating characteristics of a voltmeter is sufficient for signal filtering.



Fig. 7: Flux density measurement with voltmeter



Display the difference between channel 1 and channel

2 to show the Hall voltage. Capacitors 4.7 nF and 330 pF for electromagnetic immunity are recommended.





Do not connect OUT1 or OUT2 to Ground.

Fig. 9: Differential HAL400 output to single-ended output R = 10 k $\Omega$ , C = 7.5 nF,  $\Delta$ R for offset adjustment, BW<sub>-3dB</sub> = 1.3 kHz



Fig. 10: Differential HAL400 output to single-ended output (referenced to ground), filter – BW<sub>-3dB</sub> = 14.7 kHz

#### HAL400 Documentation History

1. Preliminary data sheet: "HAL400 Linear Hall Effect Sensor IC", March 29, 1994, 6251-346-1PD. First release of the preliminary data sheet.

2. Preliminary data sheet: "HAL400 Linear Hall Effect Sensor IC", Aug. 1, 1995, 6251-346-2PD. Second release of the preliminary data sheet. Major changes:

- Marking code

3. Preliminary data sheet: "HAL400 Linear Hall Effect Sensor IC", Jan. 23, 1997, 6251-346-3PD. Third release of the preliminary data sheet. Major changes:

- Electrical and Magnetic Characteristics
- diagram: Typical output voltages versus magnetic flux density

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