HAT2132H

Silicon N Channel MOS FET
High Speed Power Switching

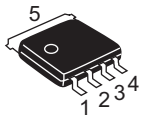
REJ03G0177-0300
Rev.3.00
Dec 07, 2006

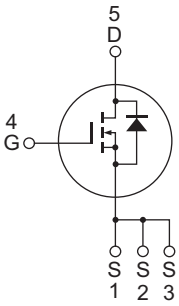
Features

- Low drive current.
- Low on-resistance
- Low profile

Outline

RENESAS Package code: PTZZ0005DA-A
(Package name: LFPAK)





1, 2, 3	Source
4	Gate
5	Drain

Absolute Maximum Ratings

(Ta = 25°C)

Item	Symbol	Ratings	Unit
Drain to source voltage	V_{DSS}	200	V
Gate to source voltage	V_{GSS}	±30	V
Drain current	I_D	6	A
Drain peak current	$I_{D(pulse)}$ Note1	24	A
Body-drain diode reverse drain current	I_{DR}	6	A
Body-drain diode reverse drain peak current	$I_{DR(pulse)}$ Note1	24	A
Avalanche current	I_{AP} Note3	6	A
Avalanche energy	E_{AR} Note3	2.4	mJ
Channel dissipation	Pch Note2	20	W
Channel to case thermal impedance	θch-c	6.25	°C/W
Channel temperature	Tch	150	°C
Storage temperature	Tstg	–55 to +150	°C

Notes: 1. PW ≤ 10 µs, duty cycle ≤ 1%
2. Value at Tc = 25°C
3. STch = 25°C, Tch ≤ 150°C

Electrical Characteristics

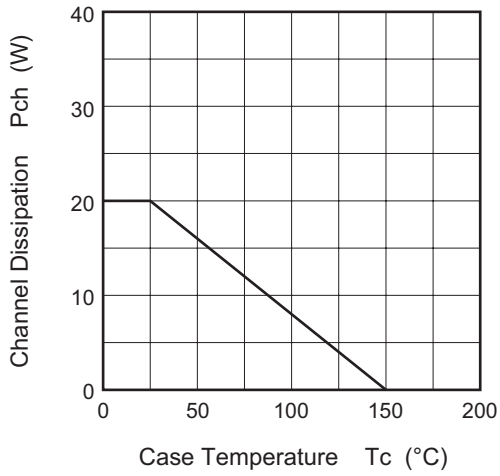
(Ta = 25°C)

Item	Symbol	Min	Typ	Max	Unit	Test conditions
Drain to source breakdown voltage	$V_{(BR)DSS}$	200	—	—	V	I_D = 10 mA, V_{GS} = 0
Zero gate voltage drain current	I_{DSS}	—	—	1	µA	V_{DS} = 200 V, V_{GS} = 0
Gate to source leak current	I_{GSS}	—	—	±0.1	µA	V_{GS} = ±30 V, V_{DS} = 0
Gate to source cutoff voltage	$V_{GS(off)}$	3.0	—	4.0	V	V_{DS} = 10 V, I_D = 1 mA
Forward transfer admittance	$\|y_{fs}\|$	2.7	4.7	—	S	I_D = 3 A, V_{DS} = 10 V Note4
Static drain to source on state resistance	$R_{DS(on)}$	—	0.36	0.45	Ω	I_D = 3 A, V_{GS} = 10 V Note4
Input capacitance	Ciss	—	450	—	pF	V_{DS} = 25 V V_{GS} = 0 f = 1 MHz
Output capacitance	Coss	—	65	—	pF	
Reverse transfer capacitance	Crss	—	13	—	pF	
Turn-on delay time	$t_{d(on)}$	—	19	—	ns	I_D = 3 A V_{GS} = 10 V R_L = 33.3 Ω Rg = 10 Ω
Rise time	t_r	—	26	—	ns	
Turn-off delay time	$t_{d(off)}$	—	48	—	ns	
Fall time	t_f	—	9	—	ns	
Total gate charge	Qg	—	12.5	—	nC	V_{DD} = 160 V V_{GS} = 10 V I_D = 6 A
Gate to source charge	Qgs	—	2.5	—	nC	
Gate to drain charge	Qgd	—	6	—	nC	
Body-drain diode forward voltage	V_{DF}	—	0.85	1.30	V	I_F = 6 A, V_{GS} = 0 Note4
Body-drain diode reverse recovery time	t_{rr}	—	95	—	ns	I_F = 6 A, V_{GS} = 0 di_F/dt = 100 A/µs

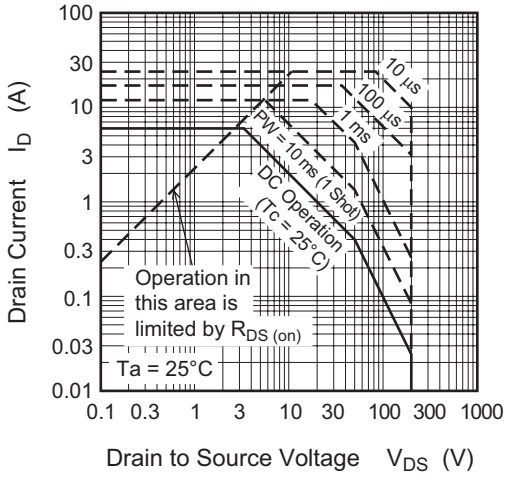
Notes: 4. Pulse test

Main Characteristics

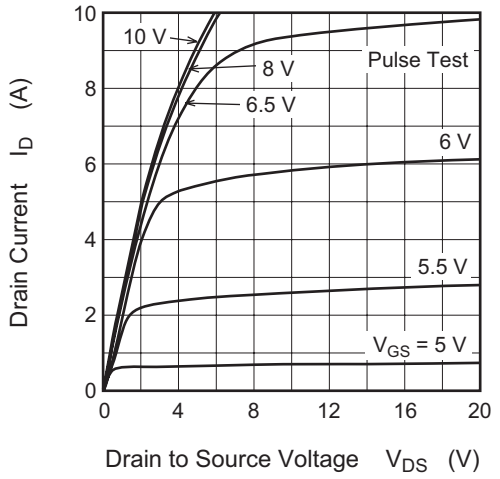
Power vs. Temperature Derating



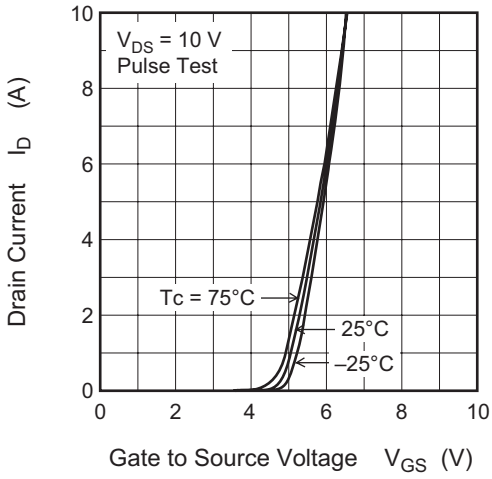
Maximum Safe Operation Area



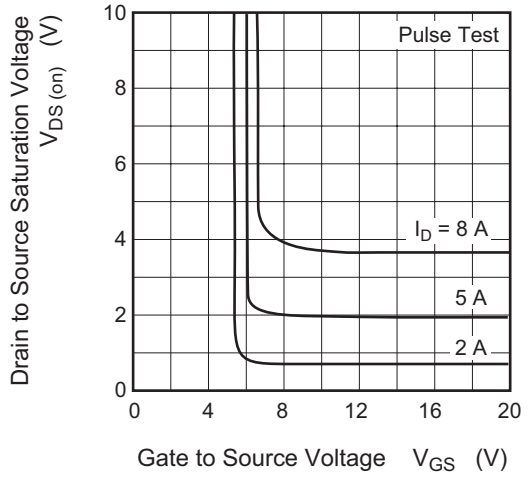
Typical Output Characteristics



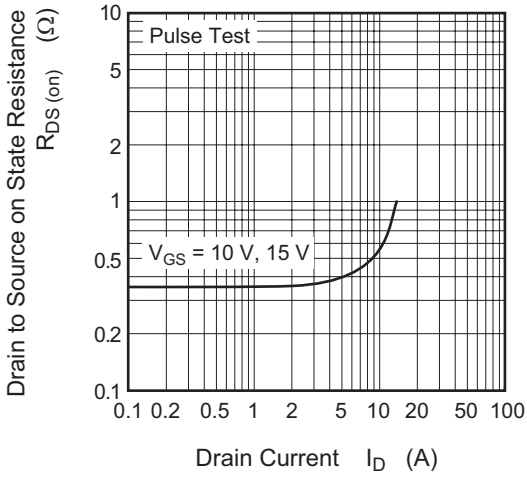
Typical Transfer Characteristics



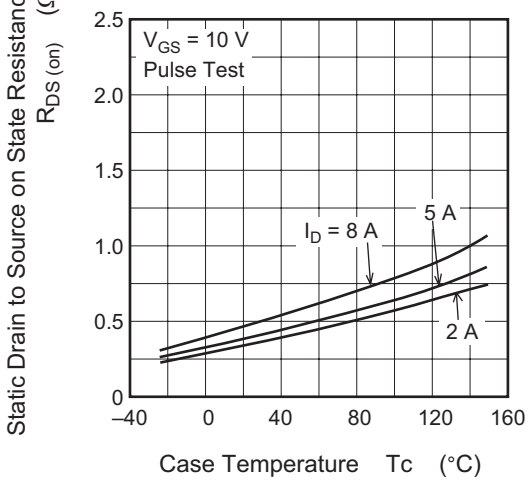
Drain to Source Saturation Voltage vs. Gate to Source Voltage



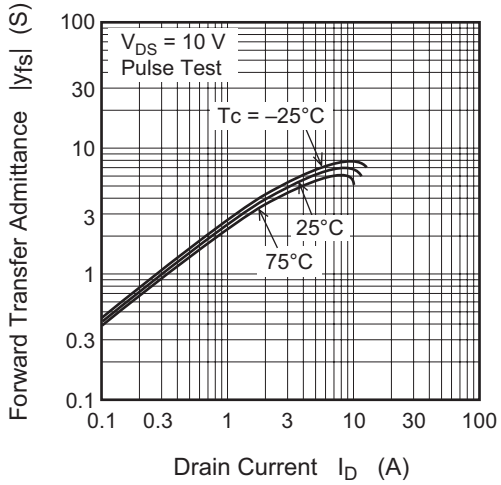
Static Drain to Source on State Resistance vs. Drain Current



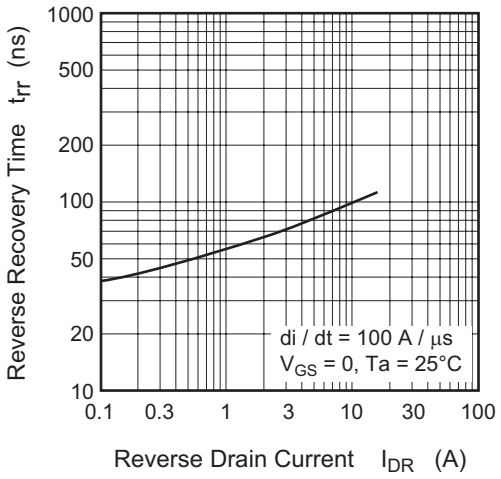
Static Drain to Source on State Resistance vs. Temperature



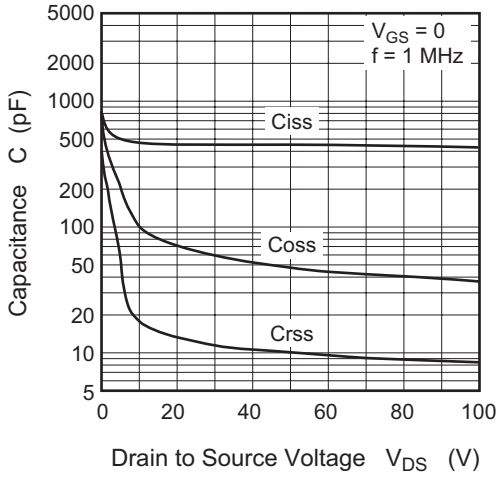
Forward Transfer Admittance vs. Drain Current



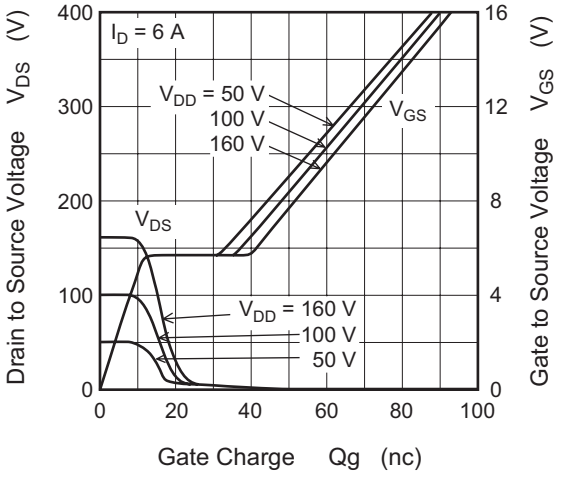
Body-Drain Diode Reverse Recovery Time



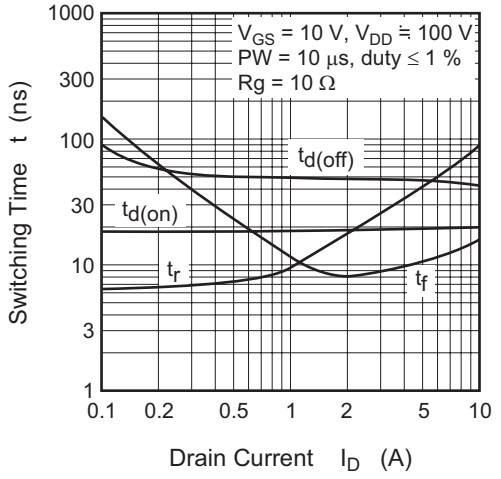
Typical Capacitance vs. Drain to Source Voltage



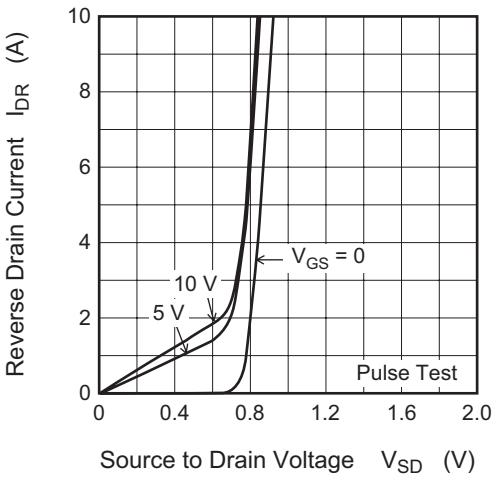
Dynamic Input Characteristics



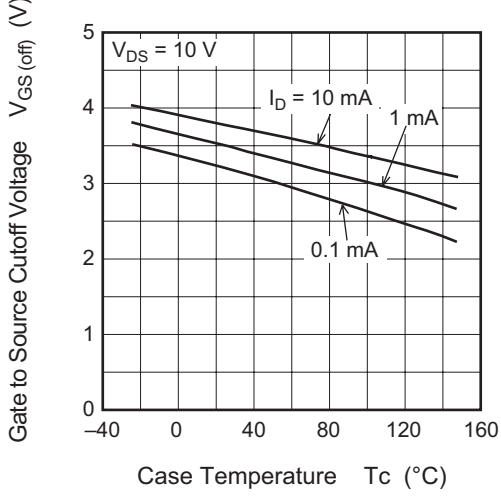
Switching Characteristics



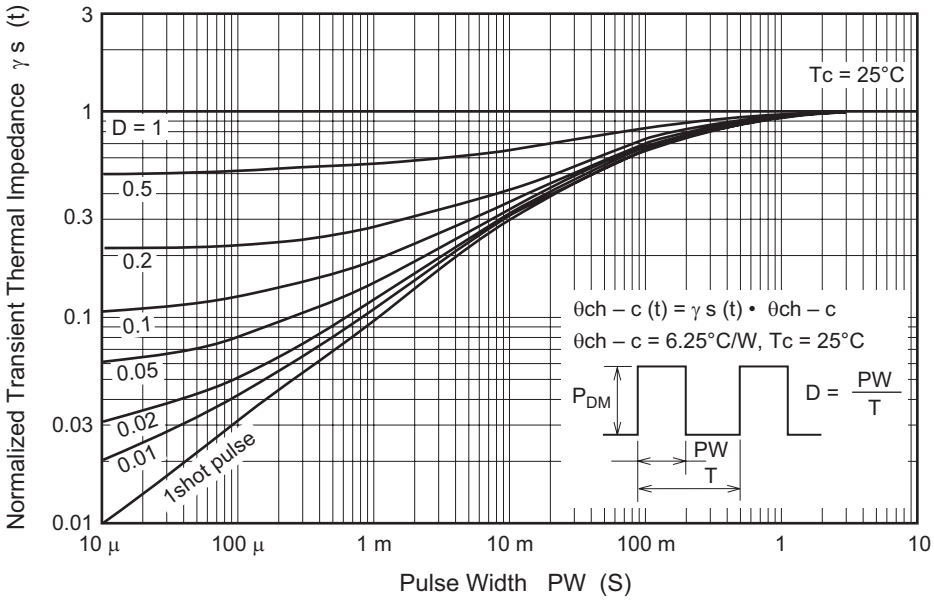
Reverse Drain Current vs. Source to Drain Voltage



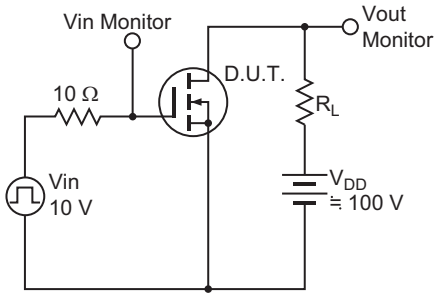
Gate to Source Cutoff Voltage vs. Case Temperature



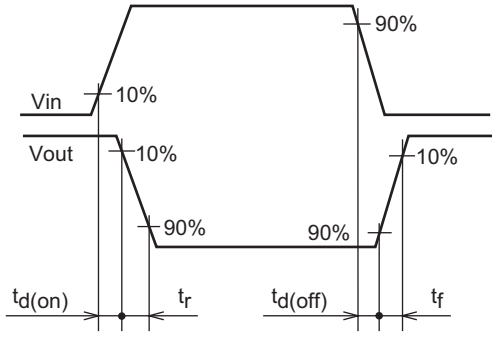
Normalized Transient Thermal Impedance vs. Pulse Width



Switching Time Test Circuit



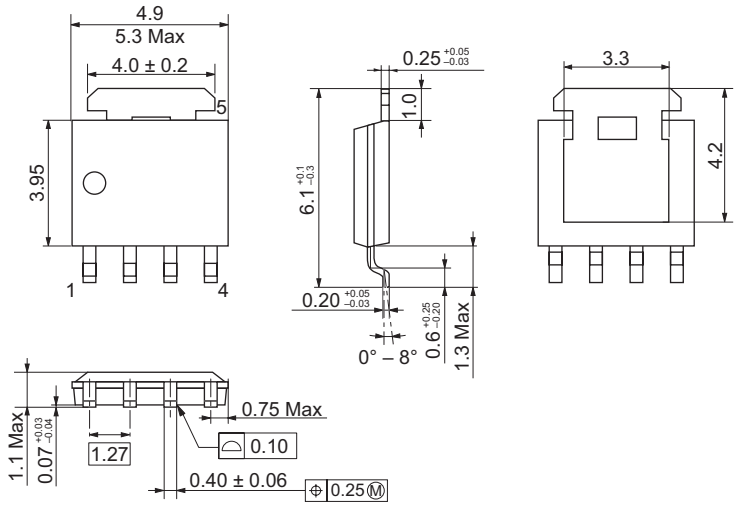
Switching Time Waveform



Package Dimensions

Package Name	JEITA Package Code	RENESAS Code	Previous Code	MASS[Typ.]
LFPAK	SC-100	PTZZ0005DA-A	LFPAKV	0.080g

Unit: mm



(Ni/Pd/Au plating)

Ordering Information

Part No.	Quantity	Shipping Container
HAT2132H-EL-E	2500 pcs	Taping

Note: For some grades, production may be terminated. Please contact the Renesas sales office to check the state of production before ordering the product.

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http://www.renesas.com

Refer to "**http://www.renesas.com/en/network**" for the latest and detailed information.

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