

HAT2226R

Silicon N Channel MOS FET High Speed Power Switching

REJ03G1466-0100 Rev.1.00 Jul 18, 2006

Features

- Low on-resistance
- Low drive current
- High density mounting

Outline

Absolute Maximum Ratings

 $(Ta = 25^{\circ}C)$

| Item | Symbol | Ratings | Unit | |
|---|-------------------------------|-------------|------|--|
| Drain to source voltage | V_{DSS} | 600 | V | |
| Gate to source voltage | V _{GSS} | ±30 | V | |
| Drain current | I _D | 0.1 | Α | |
| Drain peak current | I _{D (pulse)} Note1 | 0.4 | Α | |
| Body-drain diode reverse drain current | I _{DR} | 0.1 | Α | |
| Body-drain diode reverse drain peak current | I _{DR (pulse)} Note1 | 0.4 | Α | |
| Channel dissipation | Pch Note2 | 1.5 | W | |
| Channel temperature | Tch | 150 | °C | |
| Storage temperature | Tstg | -55 to +150 | °C | |

Notes: 1. PW \leq 10 μ s, duty cycle \leq 1%

2. When using the glass epoxy board (FR4 40 x 40 x 1.6 mm), PW \leq 10 s

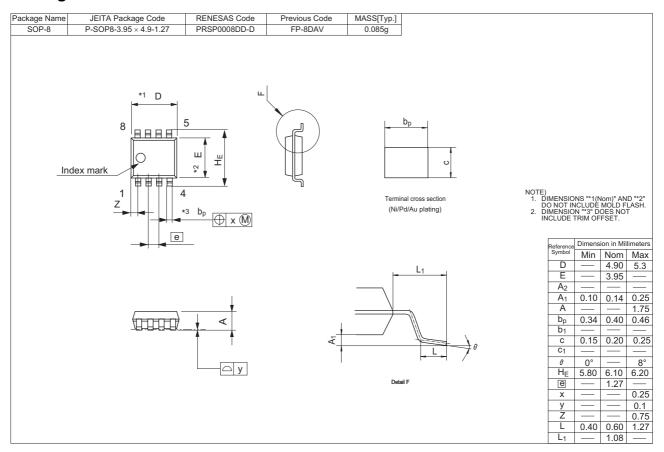
Electrical Characteristics

 $(Ta = 25^{\circ}C)$

| Item | Symbol | Min | Тур | Max | Unit | Test conditions | |
|--|----------------------|-----|------|------|------|---|--|
| Drain to source breakdown voltage | V _{(BR)DSS} | 600 | _ | _ | V | $I_D = 10 \text{ mA}, V_{GS} = 0$ | |
| Zero gate voltage drain current | I _{DSS} | _ | _ | 1 | μΑ | V _{DS} = 600 V, V _{GS} = 0 | |
| Gate to source leak current | I _{GSS} | _ | _ | ±0.1 | μΑ | $V_{GS} = \pm 30 \text{ V}, V_{DS} = 0$ | |
| Gate to source cutoff voltage | $V_{GS(off)}$ | 3.0 | _ | 5.0 | V | $V_{DS} = 10 \text{ V}, I_{D} = 1 \text{ mA}$ | |
| Static drain to source on state resistance | R _{DS(on)} | _ | 35 | 52 | Ω | $I_D = 50 \text{ mA}, V_{GS} = 10 \text{ V}^{\text{Note3}}$ | |
| Input capacitance | Ciss | _ | 25 | _ | pF | V _{DS} = 25 V | |
| Output capacitance | Coss | | 4 | | pF | $V_{GS} = 0$ | |
| Reverse transfer capacitance | Crss | _ | 0.4 | _ | pF | f = 1 MHz | |
| Turn-on delay time | t _{d(on)} | _ | 34 | _ | ns | I _D = 50 mA | |
| Rise time | t _r | _ | 16 | _ | ns | V _{GS} = 10 V | |
| Turn-off delay time | t _{d(off)} | _ | 57 | _ | ns | $R_L = 6000 \Omega$ | |
| Fall time | t _f | _ | 320 | _ | ns | $Rg = 10 \Omega$ | |
| Total gate charge | Qg | _ | 3.1 | _ | nC | V _{DD} = 480 V | |
| Gate to source charge | Qgs | _ | 0.5 | _ | nC | V _{GS} = 10 V | |
| Gate to drain charge | Qgd | _ | 2.3 | _ | nC | $I_D = 100 \text{ mA}$ | |
| Body-drain diode forward voltage | V_{DF} | _ | 0.79 | 1.35 | V | $I_F = 100 \text{ mA}, V_{GS} = 0^{\text{Note3}}$ | |
| Body-drain diode reverse recovery time | t _{rr} | _ | 175 | _ | ns | $I_F = 100$ mA, $V_{GS} = 0$ $di_F/dt = 100$ A/ μ s | |

Notes: 3. Pulse test

Package Dimensions



Ordering Information

| Part Name | Quantity | Shipping Container |
|---------------|----------|--------------------|
| HAT2226R-EL-E | 2500 pcs | Taping |

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