

HD74HC564, HD74HC574

Octal D-type Flip-Flops (with 3-state outputs)

REJ03D0630-0200 (Previous ADE-205-510) Rev.2.00 Mar 30, 2006

Description

These devices are positive edge triggered flip-flops. The difference between HD74HC564 and HD74HC574 is only that the former has inverting outputs and the latter has noninvertering outputs.

Data at the D inputs, meeting the set-up and hold time requirements, are transferred to the Q or \overline{Q} outputs on positive going transitions of the clock (CK) input. When a high logic level is applied to the output control (OC) input, all outputs go to a high impedance state, regardless of what signals are present at the other inputs and the state of the storage elements.

Features

• High Speed Operation: t_{pd} (Clock to Output) = 13 ns typ ($C_L = 50 \text{ pF}$)

• High Output Current: Fanout of 15 LSTTL Loads

• Wide Operating Voltage: $V_{CC} = 2 \text{ to } 6 \text{ V}$

• Low Input Current: 1 μA max

• Low Quiescent Supply Current: I_{CC} (static) = 4 μ A max (Ta = 25°C)

• Ordering Information

Part Name	Package Type	Package Code (Previous Code)	Package Abbreviation	Taping Abbreviation (Quantity)
HD74HC564P HD74HC574P	DILP-20 pin	PRDP0020AC-B (DP-20NEV)	Р	_
HD74HC564FPEL HD74HC574FPEL	SOP-20 pin (JEITA)	PRSP0020DD-B (FP-20DAV)	FP	EL (2,000 pcs/reel)
HD74HC564RPEL	SOP-20 pin (JEDEC)	PRSP0020DC-A (FP-20DBV)	RP	EL (1,000 pcs/reel)

Note: Please consult the sales office for the above package availability.

Function Table

	Inputs	Outputs			
Output Control	Clock	Data	HD74HC564	HD74HC574	
L		Н	L	Н	
L	\int	L	Н	L	
L	L	X	Q_0	Q_0	
Н	X	X	Z	Z	

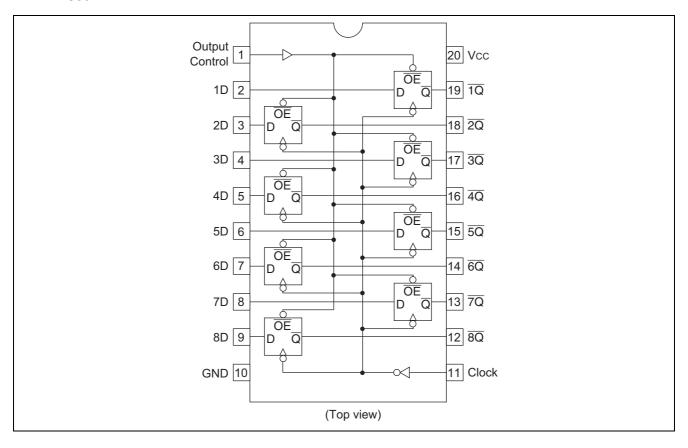
Q₀: level of Q before the indicated Steady-sate input conditions were established.

 \overline{Q}_0 : complement of Q_0 or level of \overline{Q} before the indicated Steady-state input Conditions were established.

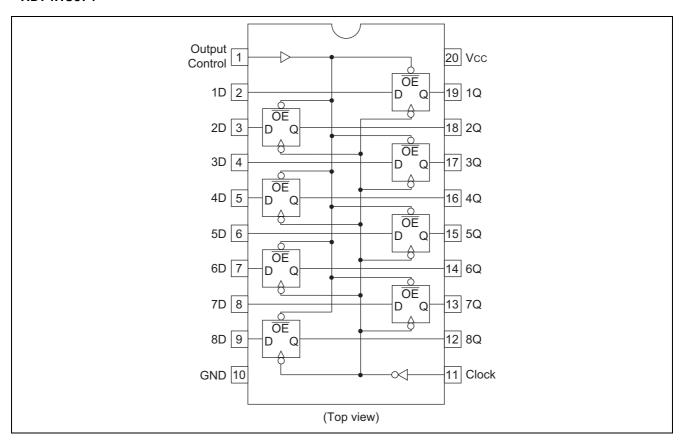


Pin Arrangement

HD74HC564

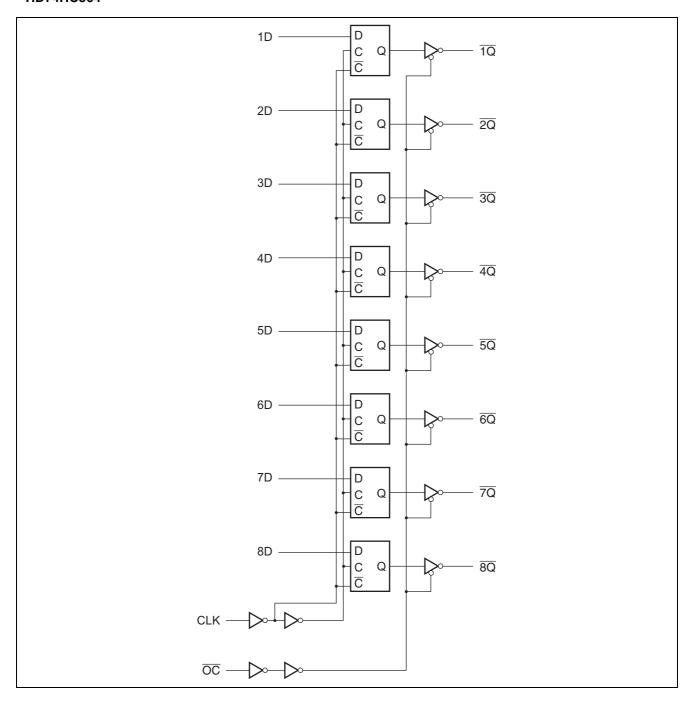


HD74HC574

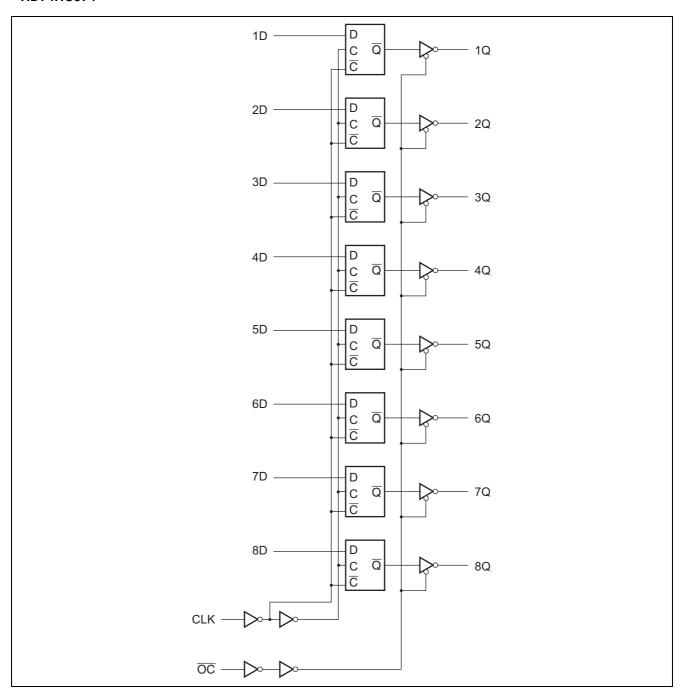


Logic Diagram

HD74HC564



HD74HC574



Absolute Maximum Ratings

Item	Symbol	Ratings	Unit
Supply voltage range	V _{CC}	-0.5 to 7.0	V
Input / Output voltage	V _{IN} , V _{OUT}	-0.5 to V _{CC} +0.5	V
Input / Output diode current	I _{IK} , I _{OK}	±20	mA
Output current	I ₀	±35	mA
V _{CC} , GND current	I _{CC} or I _{GND}	±75	mA
Power dissipation	P _T	500	mW
Storage temperature	Tstg	-65 to +150	°C

Note: The absolute maximum ratings are values, which must not individually be exceeded, and furthermore, no two of which may be realized at the same time.

Recommended Operating Conditions

Item	Symbol	Ratings	Unit	Conditions
Supply voltage	V _{CC}	2 to 6	V	
Input / Output voltage	V _{IN} , V _{OUT}	0 to V _{CC}	V	
Operating temperature	Та	-40 to 85	°C	
		0 to 1000		V _{CC} = 2.0 V
Input rise / fall time ^{*1}	t_r , t_f	0 to 500	ns	$V_{CC} = 4.5 \text{ V}$
		0 to 400		V _{CC} = 6.0 V

Note: 1. This item guarantees maximum limit when one input switches.

Waveform: Refer to test circuit of switching characteristics.

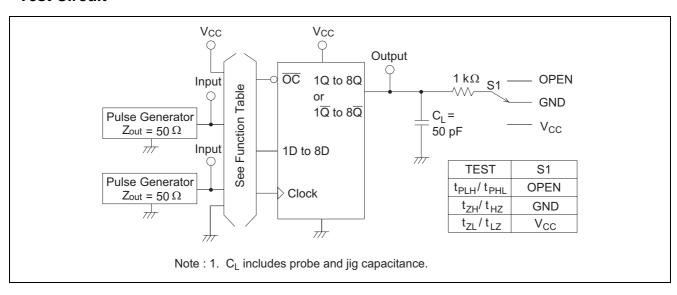
Electrical Characteristics

			Ta = 25°C		Ta = -40	= -40 to+85°C				
Item	Symbol	V _{cc} (V)	Min	Тур	Max	Min	Max	Unit	Test Conditions	
Input voltage	V _{IH}	2.0	1.5	_	_	1.5	_	V		
		4.5	3.15	_	_	3.15	_			
		6.0	4.2	1	_	4.2	_			
	V_{IL}	2.0	l	1	0.5		0.5	V		
		4.5	1	1	1.35	_	1.35			
		6.0	l	1	1.8		1.8			
Output voltage	V_{OH}	2.0	1.9	2.0	_	1.9	_	V	$Vin = V_{IH} or V_{IL}$	$I_{OH} = -20 \mu A$
		4.5	4.4	4.5	_	4.4	_			
		6.0	5.9	6.0	_	5.9	_			
		4.5	4.18	1	_	4.13	_			$I_{OH} = -6 \text{ mA}$
		6.0	5.68	_	_	5.63	_			$I_{OH} = -7.8 \text{ mA}$
	V_{OL}	2.0	-	0.0	0.1	_	0.1	V	$Vin = V_{IH} or V_{IL}$	$I_{OL} = 20 \mu A$
		4.5	-	0.0	0.1	_	0.1			
		6.0	-	0.0	0.1	_	0.1			
		4.5	-	_	0.26	_	0.33			$I_{OL} = 6 \text{ mA}$
		6.0	-	_	0.26	_	0.33			$I_{OL} = 7.8 \text{ mA}$
Off-state output	l _{OZ}	6.0	_	_	±0.5	_	±5.0	μΑ	$Vin = V_{IH} or V_{IL}$	
current									Vout = V _{CC} or GND	
Input current	lin	6.0	_	_	±0.1	_	±1.0	μΑ	$Vin = V_{CC}$ or GND	
Quiescent supply current	I _{CC}	6.0	_	_	4.0	_	40	μΑ	Vin = V_{CC} or GND, lout = $0 \mu A$	

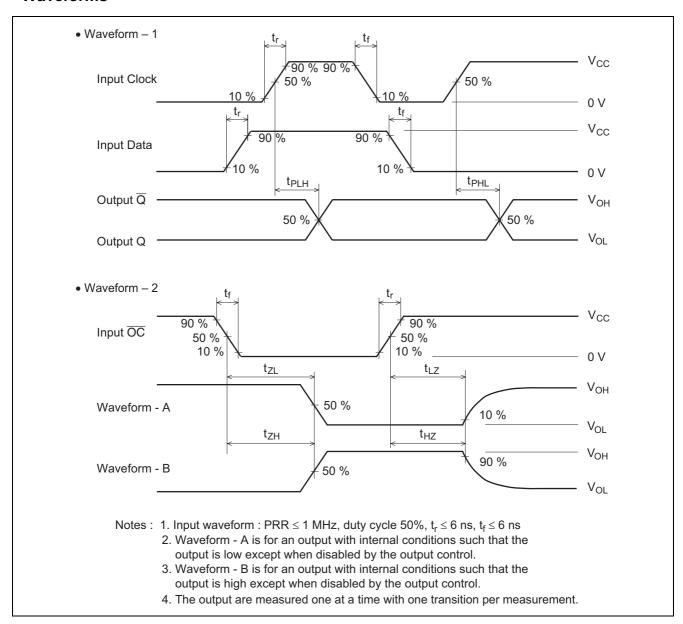
Switching Characteristics ($C_L = 50 \text{ pF}$, Input $t_r = t_f = 6 \text{ ns}$)

	Ta = 25°C Ta = -40 to +85		to +85°C						
Item	Symbol	V _{cc} (V)	Min	Тур	Max	Min	Max	Unit	Test Conditions
Maximum clock	f _{max}	2.0	_	_	6	_	5	MHz	
frequency		4.5	_	_	30	_	24		
		6.0	_	_	35	_	28		
Propagation delay	t _{PLH}	2.0	_	_	155	_	195	ns	Clock to output
time	t _{PHL}	4.5	_	13	31	_	39		
		6.0	_	_	26	_	33		
Output enable	t _{ZH}	2.0	_	_	150	_	190	ns	
time	t_{ZL}	4.5	_	13	30	_	38		
		6.0	_	_	26	_	33		
Output disable	t _{HZ}	2.0	_	_	150	_	190	ns	
time	t_{LZ}	4.5	_	15	30	_	38		
		6.0	_	_	26	_	33		
Setup time	t _{su}	2.0	_	_	100	_	125	ns	
		4.5	_	1	20	_	25		
		6.0	_	_	17	_	21		
Hold time	t _h	2.0	5	_	_	5	_	ns	
		4.5	5	0	_	5	_		
		6.0	5	_	_	5	_		
Pulse width	t _w	2.0	80	_	_	100	_	ns	
		4.5	16	4	_	20	_		
		6.0	14	_	_	17	_		
Output rise/fall	t _{TLH}	2.0	_	_	60	_	75	ns	
time	t_{THL}	4.5	_	4	12	_	15		
		6.0	_	_	10	_	13		
Input capacitance	Cin	_	_	5	10	_	10	pF	

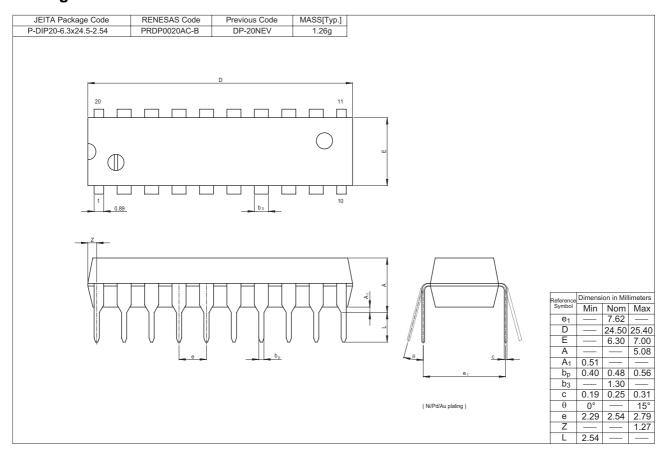
Test Circuit

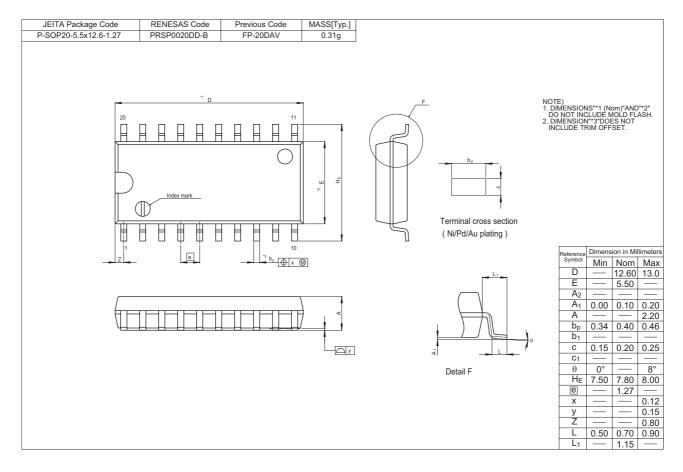


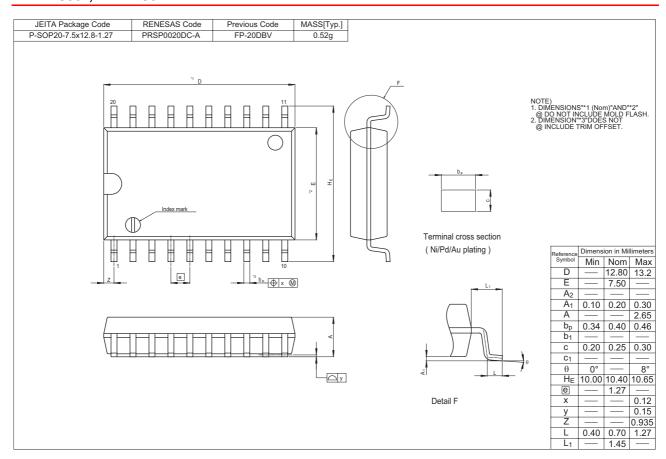
Waveforms



Package Dimensions







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