

HRC0203C

Silicon Schottky Barrier Diode for Rectifying

REJ03G0619-0300
(Previous: ADE-208-1518B)
Rev.3.00
May 20, 2005

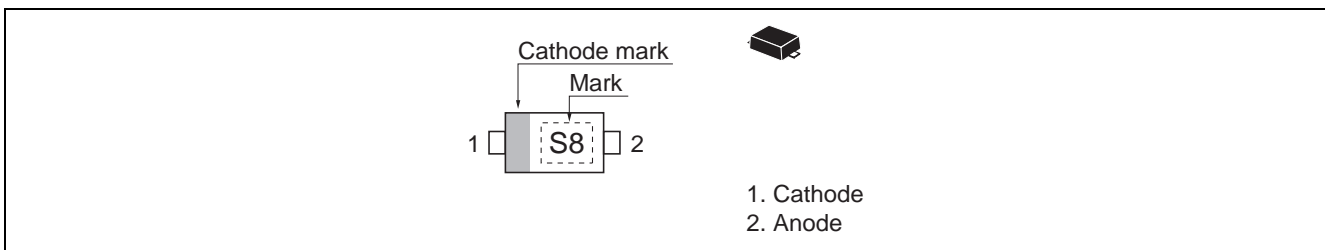
Features

- Low forward voltage drop and suitable for high efficiency rectifying.
- Ultra small Flat Lead Package (UFP) is suitable for surface mount design.

Ordering Information

Type No.	Laser Mark	Package Name	Package Code (Previous Code)
HRC0203C	S8	UFP	PWSF0002ZA-A (UFP)

Pin Arrangement



Absolute Maximum Ratings

(Ta = 25°C)

Item	Symbol	Value	Unit
Repetitive peak reverse voltage	V_{RRM}^{*1}	30	V
Average rectified current	I_O^{*1}	200	mA
Non-Repetitive peak forward surge current	I_{FSM}^{*2}	2	A
Junction temperature	T_j	125	°C
Storage temperature	T_{stg}	-55 to +125	°C

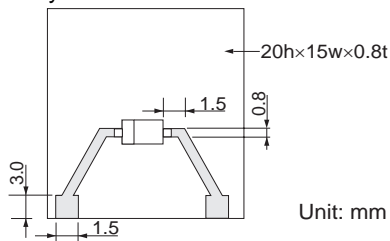
Notes: 1. See from Fig.3 to Fig.5, with polyimide board.
2. 10 ms sine wave 1 pulse.

Electrical Characteristics

(Ta = 25°C)

Item	Symbol	Min	Typ	Max	Unit	Test Condition
Forward voltage	V_{F1}	—	—	0.25	V	$I_F = 5 \text{ mA}$
	V_{F2}	—	—	0.45		$I_F = 200 \text{ mA}$
Reverse current	I_R	—	—	30	μA	$V_R = 10 \text{ V}$
Thermal resistance	$R_{th(j-a)}$	—	550	—	°C/W	Polyimide board ^{*1}

Note: 1. Polyimide board



Main Characteristic

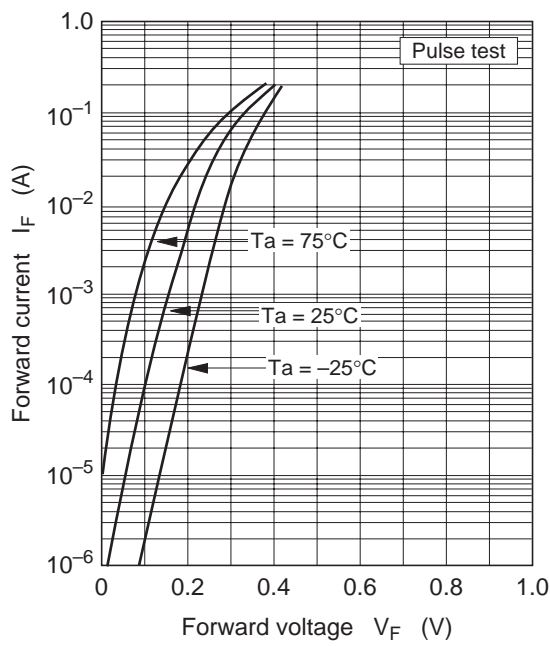


Fig.1 Forward current vs. Forward voltage

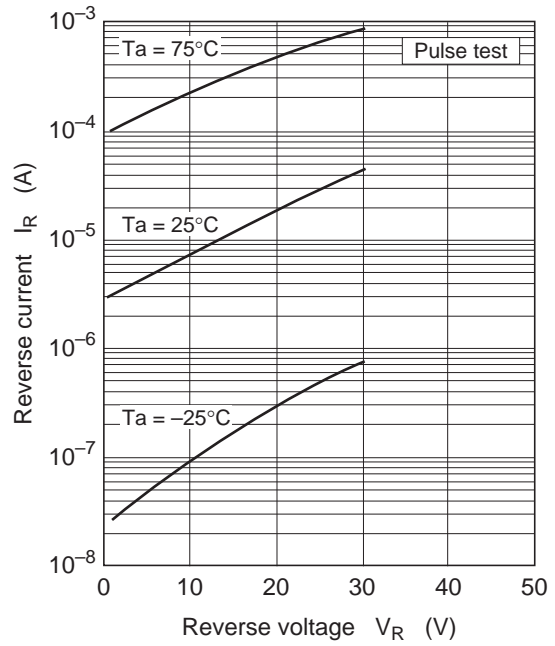


Fig.2 Reverse current vs. Reverse voltage

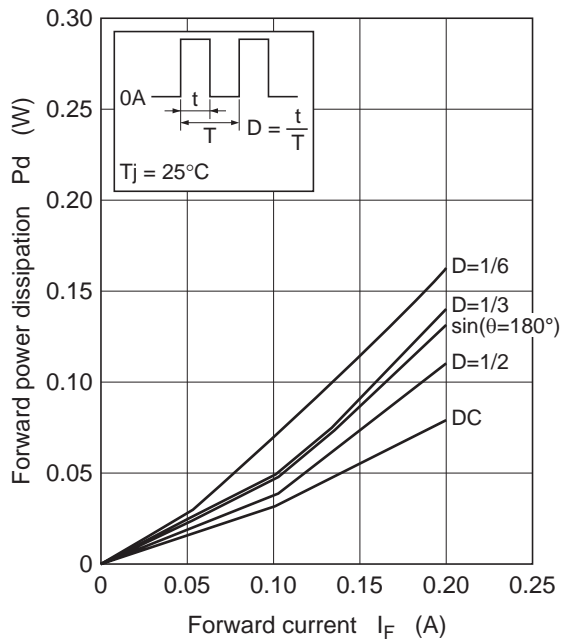


Fig3. Forward power dissipation vs. Forward current

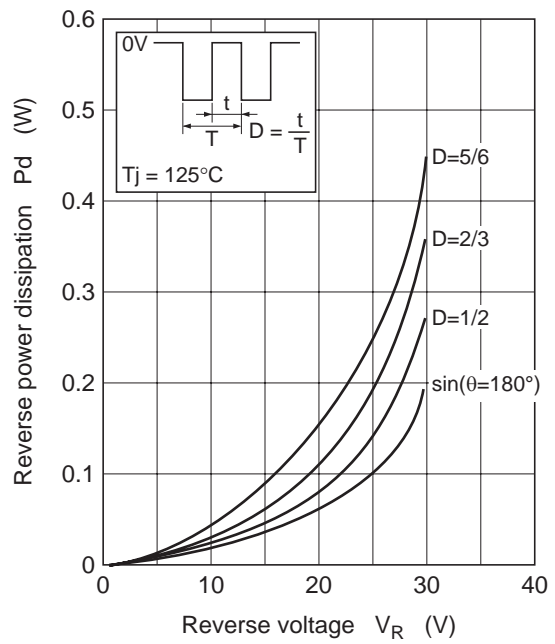


Fig4. Reverse power dissipation vs. Reverse voltage

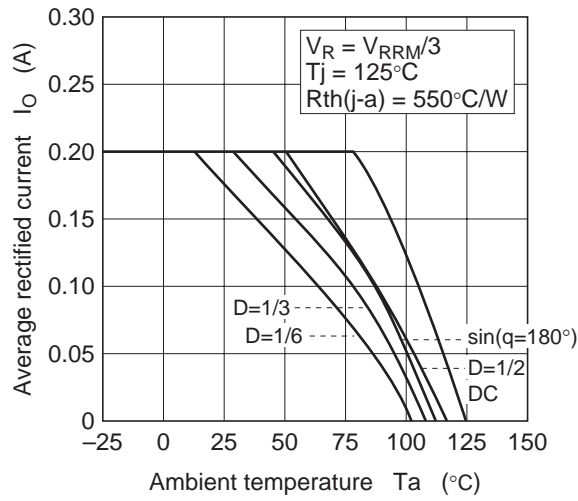


Fig.5 Average rectified current vs. Ambient temperature

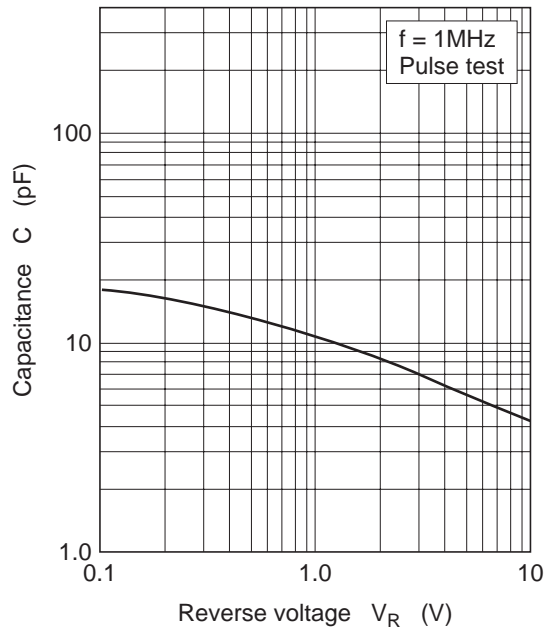
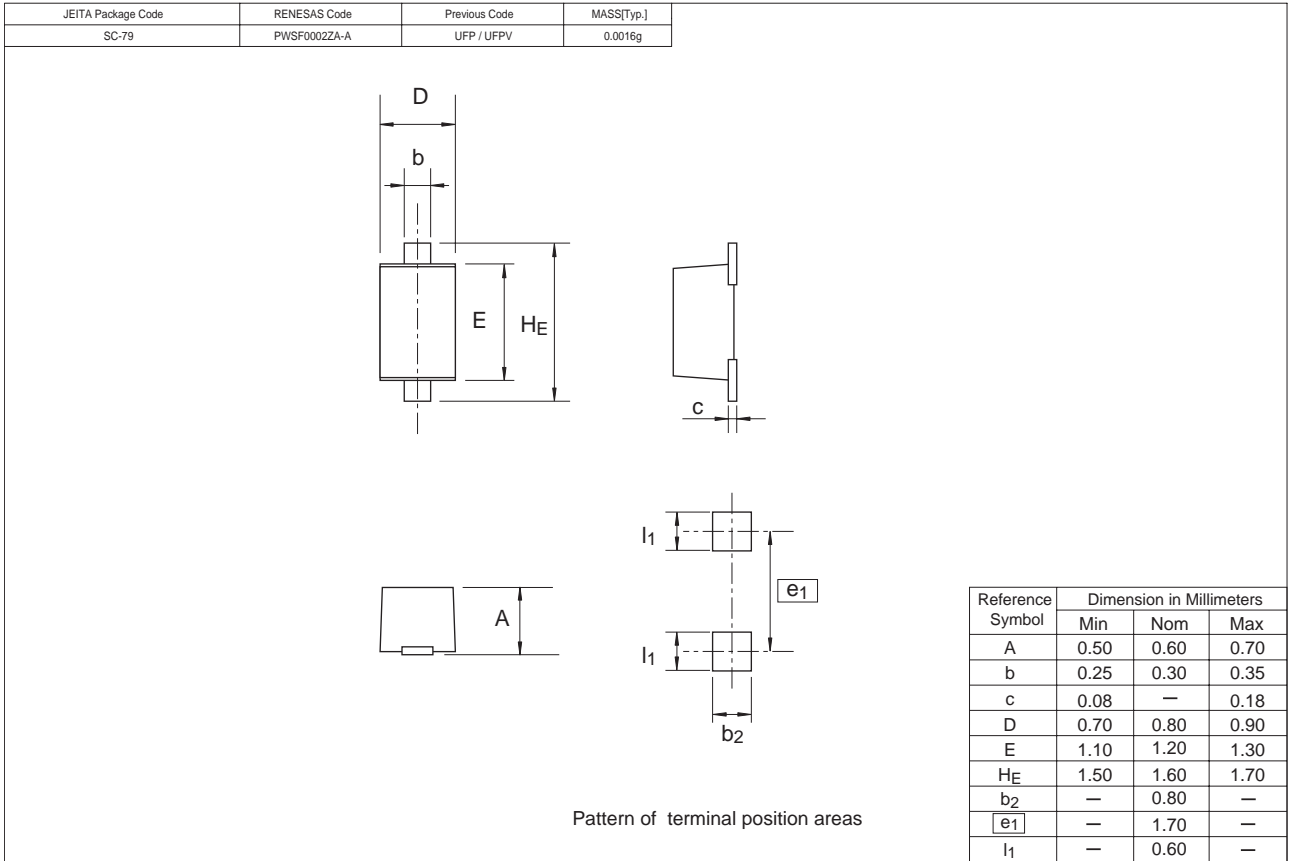


Fig.6 Capacitance vs. Reverse voltage

Package Dimensions



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