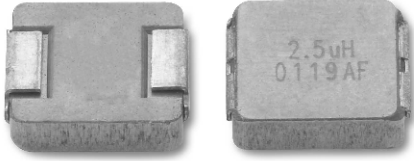


Low Profile, High Current Inductor



Manufactured under one or more of the following:
US Patents; 6,198,375/6,204,744/6,449,829/6,460,244.
 Several foreign patents, and other patents pending.



FEATURES

- Lowest height (1.8 mm) in this package footprint
- Shielded construction
- Frequency range up to 5.0 MHz
- Lowest DCR/ μ H, in this package size
- Handles high transient current spikes without saturation
- Ultra low buzz noise, due to composite construction
- 100 % lead (Pb)-free and RoHS compliant

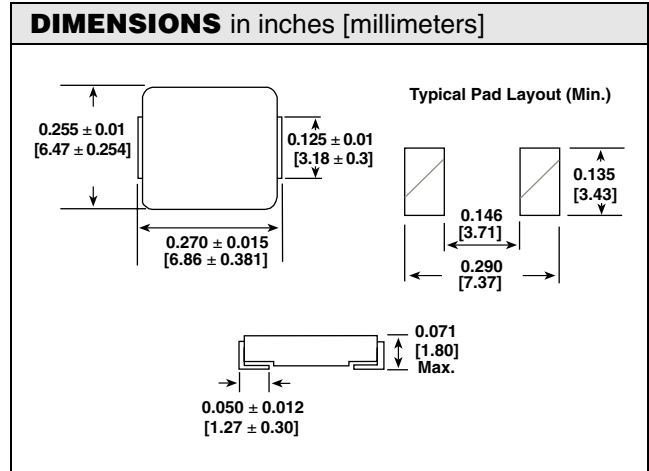
APPLICATIONS

- PDA/Notebook/Desktop/Server applications
- High current POL converters
- Low profile, high current power supplies
- Battery powered devices
- DC/DC converters in distributed power systems
- DC/DC converter for Field Programmable Gate Array (FPGA)

STANDARD ELECTRICAL SPECIFICATIONS				
VALUE (μ H)	TYPICAL DCR (m Ω)	MAX DCR (m Ω)	HEAT RATING CURRENT ³ (Amps)	SATURATION CURRENT ⁴ (Amps)
0.1	3.0	3.5	18	40
0.15	4.7	5.2	15	38
0.22	5.3	5.7	14	26
0.33	6.6	7.0	12	18
0.47	8.4	9.3	11	18
0.68	12.7	13.9	9	17
0.82	13.8	15.9	8	17
1.0	17.5	18.3	7	14
1.5	32.6	34.0	4	11.5
2.2	40.3	46.0	3.75	13
2.5	49.9	52.4	3.5	10.4
3.3	56.2	60.1	3.25	10
4.7	76.6	78.0	3	8

NOTES:

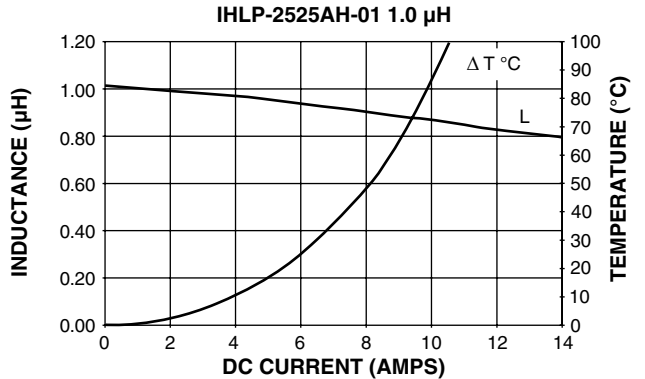
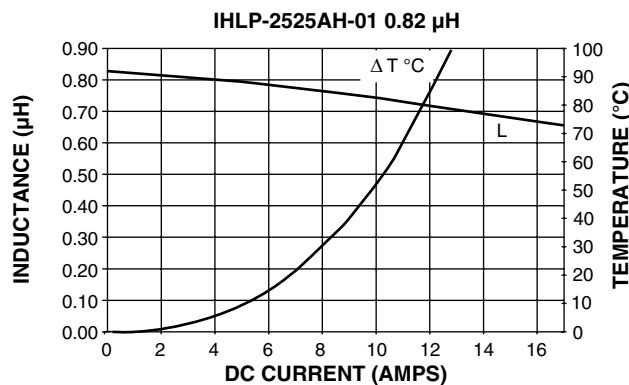
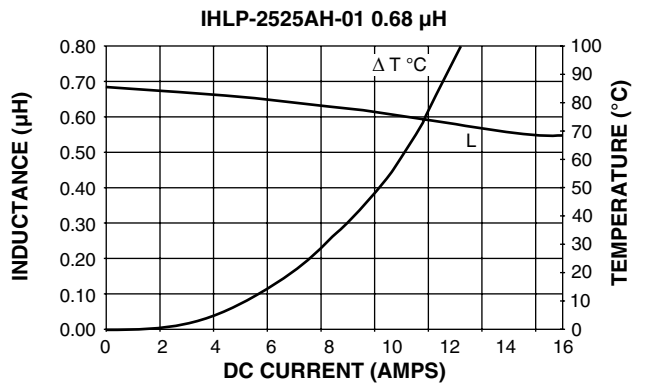
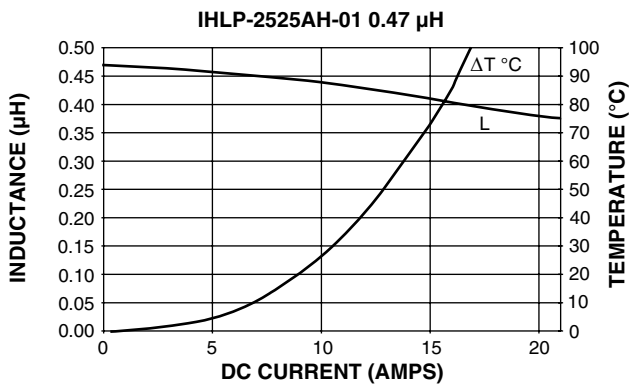
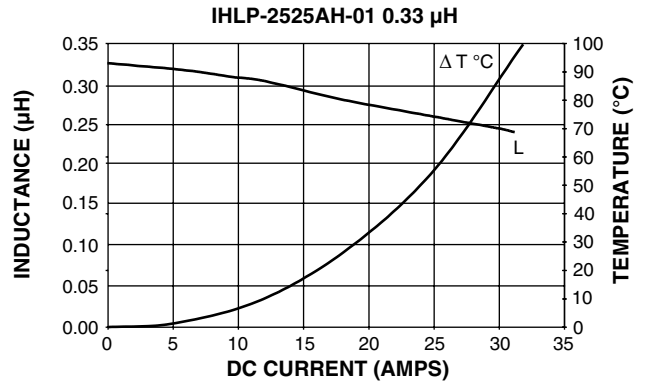
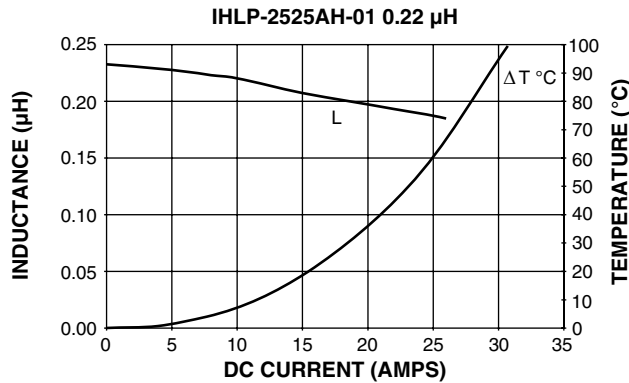
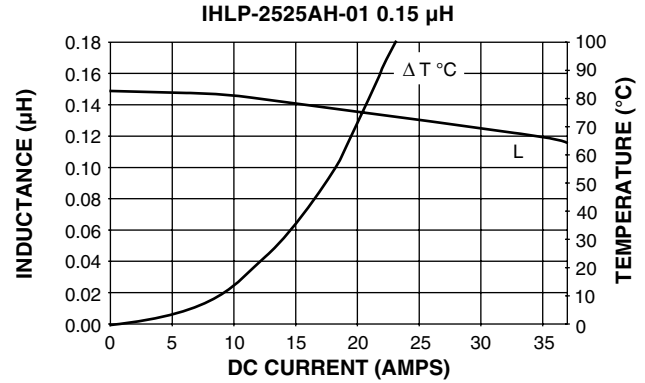
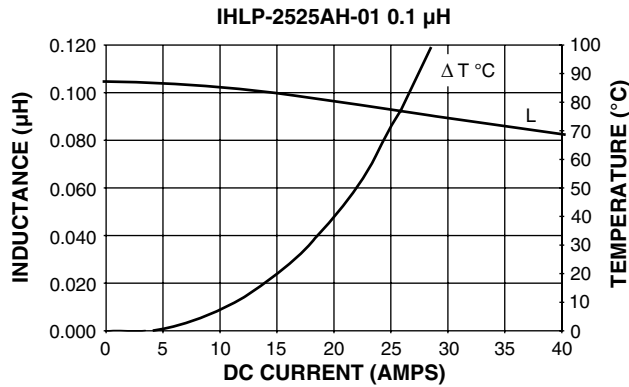
1. All test data is referenced to 25 °C ambient
2. Operating Temperature Range - 55 °C to + 125 °C
3. DC current (A) that will cause an approximate Δ T of 40 °C
4. DC current (A) that will cause L_o to drop approximately 20 %
5. The part temperature (ambient + temp rise) should not exceed 125 °C under worst case operating conditions. Circuit design, component placement, PWB trace size and thickness, airflow and other cooling provisions all affect the part temperature. Part temperature should be verified in the end application.



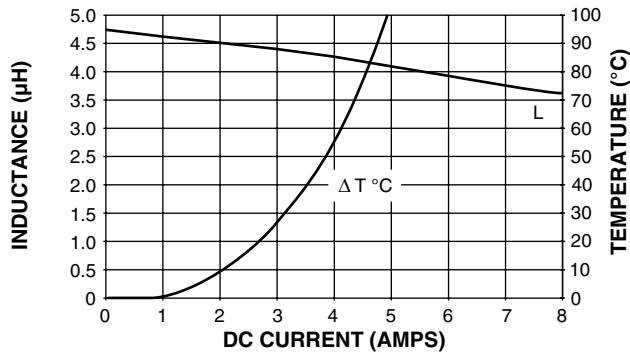
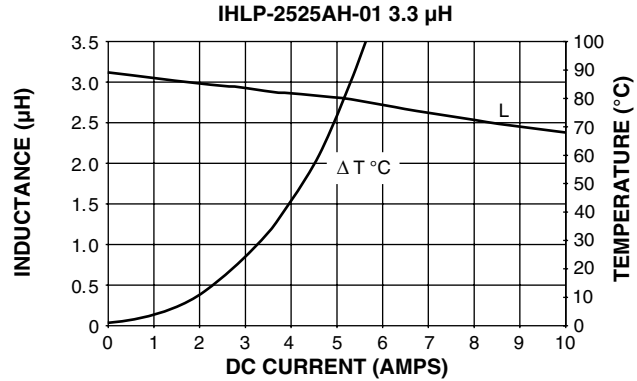
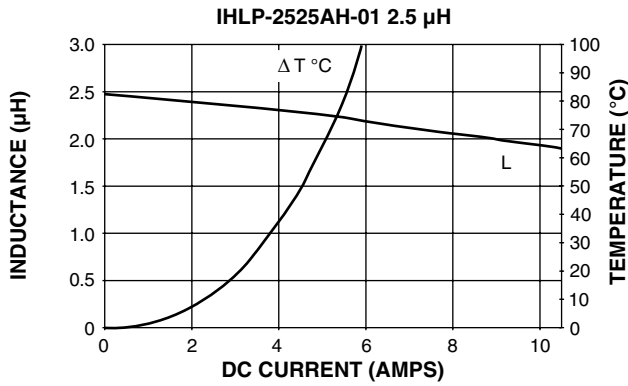
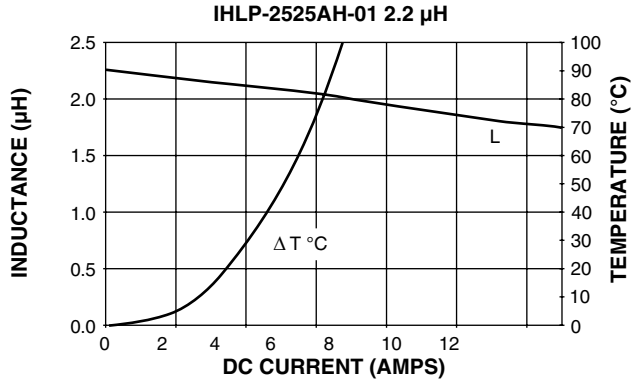
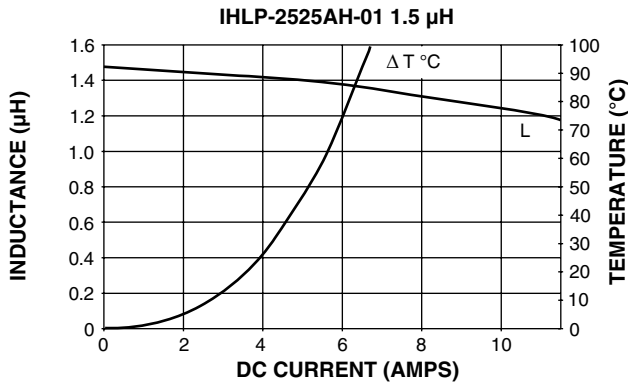
DESCRIPTION																	
IHLP-2525AH-01 MODEL	1.0 μ H INDUCTANCE VALUE	\pm 20 % INDUCTANCE TOLERANCE	ER PACKAGE CODE	e3 JEDEC LEAD (Pb)-FREE STANDARD													
GLOBAL PART NUMBER																	
I	H	L	P	2	5	2	5	A	H	E	R	1	R	0	M	0	1
MODEL				SIZE				PACKAGE CODE		INDUCTANCE VALUE		INDUCTANCE TOLERANCE		SERIES			



PERFORMANCE GRAPHS



PERFORMANCE GRAPHS





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