



PLL Product Specification

Model: PLL350-2125(Y) **Rev:** P1 **Date:** 9/1/2006
Customer: SIRENZA MICRODEVICES, INC. **AppNote:** 4153 Option 1032
Operating Temperature Range: (-40 ° to 85 ° C)

Pb RoHS Compliant

To order models as RoHS Compliant add "Y" suffix to base model number.

Parameter	Min	Typ	Max	Units	X	Remarks
Frequency Range -	2027		2222	MHz	X	
Step Size -		125		kHz	X	
Settling Time -		5	10	mSec		
Output Power -	0	3	6	dBm	X	
Output Phase Noise:						
5.5 kHz		-92	-86	dBc/Hz		
10 kHz		-93	-87	dBc/Hz		
100 kHz		-117	-111	dBc/Hz	X	
1000 kHz		-138	-132	dBc/Hz		
>=3000 kHz		-148	-142	dBc/Hz		
Integrated Phase Noise - .1-5.5 kHz		-49	-43	deg RMS		dB DSBN
Power Supply:						
VCO/CP	4.9	5	5.1	Volts	X	
PLL IC	2.9	3	3.1	Volts	X	
Supply Current:						
VCO/CP		35	50	mA	X	
PLL IC		20	25	mA		
Spurious Product:						
100 +/-5.5 kHz		-90	-82	dBc		
125 kHz		-85	-75	dBc	X	
1000 +/-500 kHz		-95	-81	dBc		
>=3000 +/-500 kHz		-95	-84	dBc		
Reference Feedthrough -		-95	-84	dBc		
Harmonic Suppression:						
2nd Harmonic		-25	-20	dBc	X	
3rd Harmonic		-25	-20	dBc	X	
Ref Osc Signal:						
Frequency		52		MHz	X	
Amplitude	1		3.3	Vp-p	X	

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Parameter	Min	Typ	Max	Units	X	Remarks
Phase Noise - 1 kHz			-130	dBc/Hz		
Input Impedance		100		k Ω		
Output Impedance -		50		Ω		Nominal

Package Information

Package Type:	PLL350 (0.8 x 0.582 x 0.154 inches)	Drawing Number:	61054
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Comments

X Indicates parameter to be tested 100% in production

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