

# HD74HCT125, HD74HCT126

Quad. Bus Buffer Gates (with 3-state outputs)

REJ03D0657-0200 (Previous ADE-205-545) Rev.2.00 Mar 30, 2006

## **Description**

The HD74HCT125, HD74HCT126 require the 3-state control input C to be taken high to put the output into the high impedance condition, whereas the HD74HCT125, HD74HCT126 requires the control input to be low to put the output into high impedance.

#### **Features**

• LSTTL Output Logic Level Compatibility as well as CMOS Output Compatibility

• High Speed Operation:  $t_{pd}$  (A to Y) = 12 ns typ ( $C_L = 50 \text{ pF}$ )

• High Output Current: Fanout of 15 LSTTL Loads

• Wide Operating Voltage:  $V_{CC} = 4.5$  to 5.5 V

• Low Input Current: 1 μA max

• Low Quiescent Supply Current:  $I_{CC}$  (static) = 4  $\mu$ A max (Ta = 25°C)

• Ordering Information

Part Name	Package Type	Package Code (Previous Code)	Package Abbreviation	Taping Abbreviation (Quantity)	
HD74HCT125P	DILP-14 pin	PRDP0014AB-B	Р	_	
HD74HCT125FPEL		(DP-14AV) PRSP0014DF-B			
HD74HCT126FPEL	SOP-14 pin (JEITA)	(FP-14DAV)	FP	EL (2,000 pcs/reel)	
HD74HCT125TELL	TSSOP-14 pin	PTSP0014JA-B	т	ELL (2.000 pec/reel)	
HD74HCT126TELL	1330F-14 pili	(TTP-14DV)	l l	ELL (2,000 pcs/reel)	

Note: Please consult the sales office for the above package availability.

#### **Function Table**

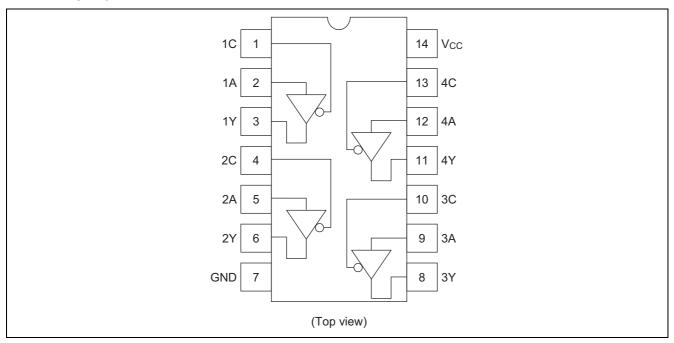
	Inputs	Output		
	С	Α	,	Υ
HCT125	HCT125 HCT126		HCT125	HCT126
Н	L	X	Z	Z
L	Н	L	L	L
L	Н	Н	Н	Н

H: High levelL: Low levelX: Irrelevant

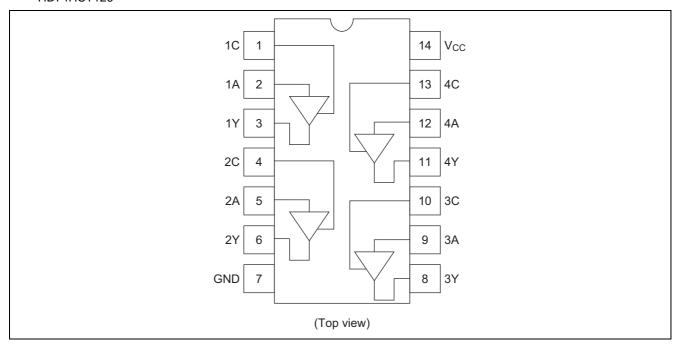
Z: Off (high-impedance) state of a 3-state output.

# **Pin Arrangement**

• HD74HCT125



• HD74HCT126



# **Absolute Maximum Ratings**

Item	Symbol	Rating	Unit
Supply voltage range	V <sub>CC</sub>	−0.5 to +7.0	V
Input voltage	V <sub>IN</sub>	-0.5 to V <sub>CC</sub> + 0.5	V
Output voltage	V <sub>OUT</sub>	-0.5 to V <sub>CC</sub> + 0.5	V
Output current	I <sub>OUT</sub>	±35	mA
DC current drain per V <sub>CC</sub> , GND	I <sub>CC</sub> , I <sub>GND</sub>	±75	mA
DC input diode current	I <sub>IK</sub>	±20	mA
DC output diode current	I <sub>OK</sub>	±20	mA
Power dissipation per package	P <sub>T</sub>	500	mW
Storage temperature	Tstg	-65 to +150	°C

Note: The absolute maximum ratings are values, which must not individually be exceeded, and furthermore, no two of which may be realized at the same time.

# **Recommended Operating Conditions**

Item	Symbol	Ratings	Unit	Conditions
Supply voltage	$V_{CC}$	4.5 to 5.5	V	
Input / Output voltage	$V_{IN}, V_{OUT}$	0 to V <sub>CC</sub>	V	
Operating temperature	Та	-40 to 85	°C	
Input rise / fall time*1	t <sub>r</sub> , t <sub>f</sub>	0 to 500	ns	V <sub>CC</sub> = 4.5 V

Notes: 1. This item guarantees maximum limit when one input switches.

Waveform: Refer to test circuit of switching characteristics.

### **Electrical Characteristics**

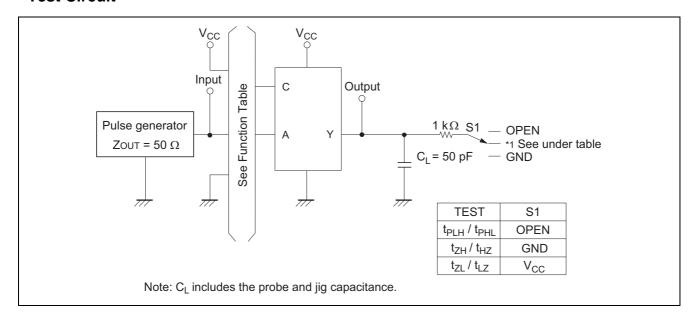
Item	Symbol	Symbol	V (\( \)	T	a = 25°	С	Ta = -40	to+85°C	Unit	Test Conditions	
item	Syllibol	V <sub>CC</sub> (V)	Min	Тур	Max	Min	Max	Oilit	lest con	iuitions	
Input voltage	V <sub>IH</sub>	4.5 to 5.5	2.0	_	_	2.0	_	V			
	V <sub>IL</sub>	4.5 to 5.5	_	_	0.8	_	0.8	V			
Output voltage	V <sub>OH</sub>	4.5	4.4	_	_	4.4	_	V	$Vin = V_{IH} \text{ or } V_{IL}$	$I_{OH} = -20 \mu A$	
		4.5	4.18	_	_	4.13	_			$I_{OH} = -6 \text{ mA}$	
	V <sub>OL</sub>	4.5	_	_	0.1	_	0.1	V	$Vin = V_{IH} \text{ or } V_{IL}$	$I_{OL} = 20 \mu A$	
		4.5	_	_	0.26	_	0.33			$I_{OL} = 6 \text{ mA}$	
Off-state output	l <sub>OZ</sub>	5.5	_	_	±0.5	_	±5.0	μA	$Vin = V_{IH} \text{ or } V_{IL},$		
current									Vout = $V_{CC}$ or $G$	ND	
Input current	lin	5.5	l	_	±0.1	_	±1.0	μΑ	$Vin = V_{CC}$ or $GN$	D	
Quiescent supply	I <sub>CC</sub>	5.5	_	_	4.0	_	40	μA	$Vin = V_{CC}$ or GND, $Iout = 0$		
current											

# **Switching Characteristics**

 $(C_L = 50 \text{ pF, Input } t_r = t_f = 6 \text{ ns})$ 

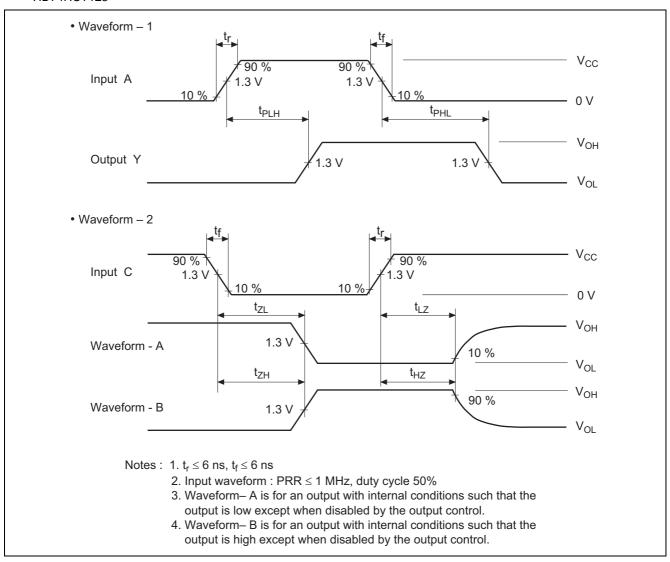
Item	Symbol	V (\( \)	Т	Ta = 25°C		Ta = -40 to +85°C		Unit	Test Conditions
item	Symbol	V <sub>CC</sub> (V)	Min	Тур	Max	Min	Max	Ollit	rest Conditions
Propagation delay time	t <sub>PHL</sub>	4.5	_	12	20	_	25	ns	
	t <sub>PLH</sub>	4.5	_	12	20	_	25		
Output enable time	t <sub>ZL</sub>	4.5	1	12	30	_	38	ns	
	t <sub>zH</sub>	4.5	l	12	30	_	38		
Output disable time	t <sub>LZ</sub>	4.5	l	15	30	_	38	ns	
	t <sub>HZ</sub>	4.5	l	15	30	_	38		
Output rise/fall time	t <sub>TLH</sub>	4.5	1	4	12	_	15	ns	
	t <sub>THL</sub>	4.5		4	12	_	15		
Input capacitance	Cin	_	_	5	10	_	10	pF	

# **Test Circuit**



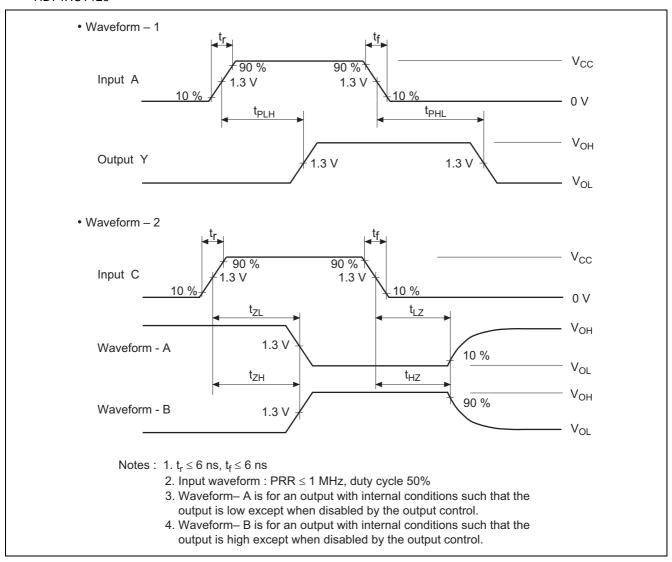
#### **Waveforms**

#### HD74HCT125

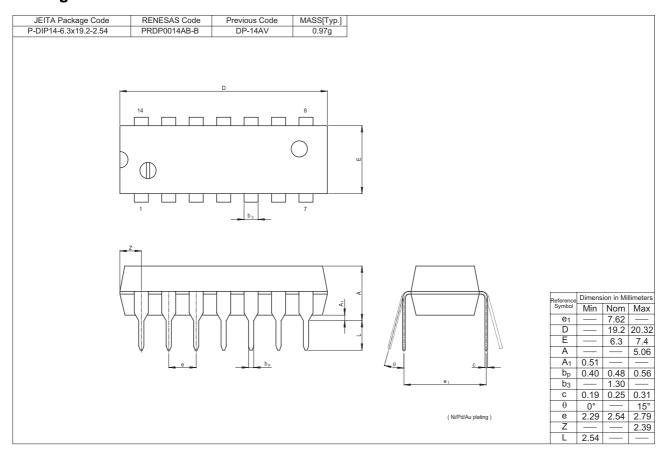


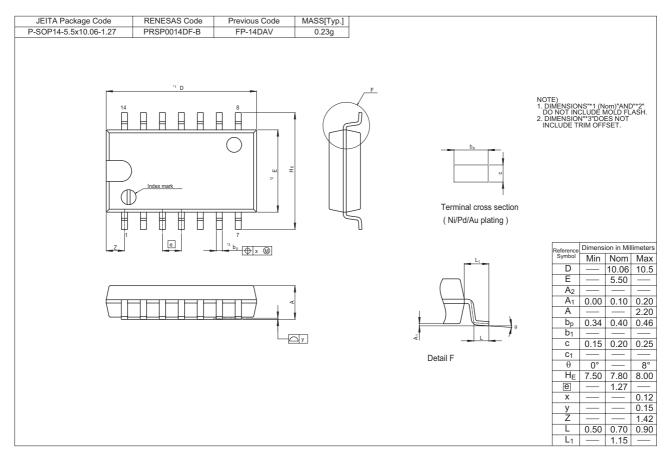
#### **Waveforms**

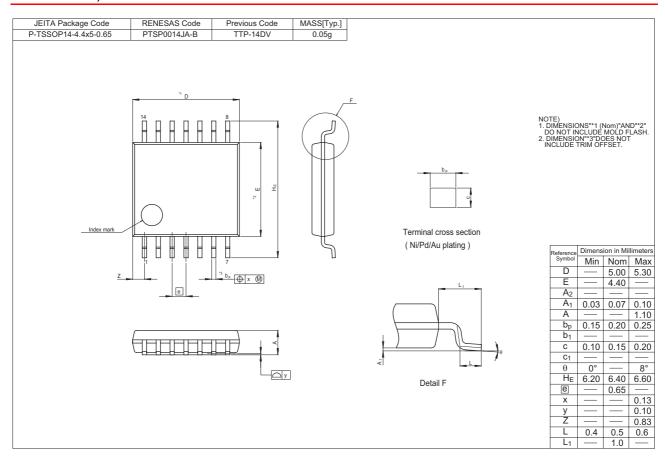
#### HD74HCT126



# **Package Dimensions**







#### Renesas Technology Corp. Sales Strategic Planning Div. Nippon Bldg., 2-6-2, Ohte-machi, Chiyoda-ku, Tokyo 100-0004, Japan

Keep safety first in your circuit designs!

1. Renesas Technology Corp. puts the maximum effort into making semiconductor products better and more reliable, but there is always the possibility that trouble may occur with them. Trouble with semiconductors may lead to personal injury, fire or property damage.

Remember to give due consideration to safety when making your circuit designs, with appropriate measures such as (i) placement of substitutive, auxiliary circuits, (ii) use of nonflammable material or (iii) prevention against any malfunction or mishap.

Notes regarding these materials

1. These materials are intended as a reference to assist our customers in the selection of the Renesas Technology Corp. product best suited to the customer's application; they do not convey any license under any intellectual property rights, or any other rights, belonging to Renesas Technology Corp. or a third party.

2. Renesas Technology Corp. assumes no responsibility for any damage, or infringement of any third-party's rights, originating in the use of any product data, diagrams, charts, programs, algorithms, or circuit application examples contained in these materials, including product data, diagrams, charts, programs and algorithms represents information on products at the time of publication of these materials, and are subject to change by Renesas Technology Corp. without notice due to product improvements or other reasons. It is therefore recommended that customers contact Renesas Technology Corp. or an authorized Renesas Technology Corp. product distributor for the latest product information before purchasing a product listed herein.

The information described here may contain technical inaccuracies or typographical errors.

Renesas Technology Corp. assumes no responsibility for any damage, liability, or other loss rising from these inaccuracies or errors.

Please also pay attention to information published by Renesas Technology Corp. by various means, including the Renesas Technology Corp. Semiconductor home page (http://www.renesas.com).

4. When using any or all of the information contained in these materials, including product data, diagrams, charts, programs, and algorithms, please be sure to

- However the state of the information contained in these materials, including product data, diagrams, charts, programs, and algorithms, please be sure to evaluate all information as a total system before making a final decision on the applicability of the information and products. Renesas Technology Corp. assumes no responsibility for any damage, liability or other loss resoluting from the information contained herein.

  5. Renesas Technology Corp. semiconductors are not designed or manufactured for use in a device or system that is used under circumstances in which human life is potentially at stake. Please contact Renesas Technology Corp. or an authorized Renesas Technology Corp. product distributor when considering the use of a product contained herein for any specific purposes, such as apparatus or systems for transportation, vehicular, medical, aerospace, nuclear, or undersea repeater use.
- use.

  6. The prior written approval of Renesas Technology Corp. is necessary to reprint or reproduce in whole or in part these materials.

  7. If these products or technologies are subject to the Japanese export control restrictions, they must be exported under a license from the Japanese government and cannot be imported into a country other than the approved destination.

  Any diversion or reexport contrary to the export control laws and regulations of Japan and/or the country of destination is prohibited.

  8. Please contact Renesas Technology Corp. for further details on these materials or the products contained therein.



**RENESAS SALES OFFICES** 

http://www.renesas.com

Refer to "http://www.renesas.com/en/network" for the latest and detailed information.

**Renesas Technology America, Inc.** 450 Holger Way, San Jose, CA 95134-1368, U.S.A Tel: <1> (408) 382-7500, Fax: <1> (408) 382-7501

Renesas Technology Europe Limited
Dukes Meadow, Millboard Road, Bourne End, Buckinghamshire, SL8 5FH, U.K.
Tel: <44> (1628) 585-100, Fax: <44> (1628) 585-900

Renesas Technology (Shanghai) Co., Ltd.
Unit 204, 205, AZIACenter, No.1233 Lujiazui Ring Rd, Pudong District, Shanghai, China 200120 Tel: <86> (21) 5877-1818, Fax: <86> (21) 6887-7898

Renesas Technology Hong Kong Ltd.
7th Floor, North Tower, World Finance Centre, Harbour City, 1 Canton Road, Tsimshatsui, Kowloon, Hong Kong Tel: <852> 2265-6688, Fax: <852> 2730-6071

**Renesas Technology Taiwan Co., Ltd.**10th Floor, No.99, Fushing North Road, Taipei, Taiwan Tel: <886> (2) 2715-2888, Fax: <886> (2) 2713-2999

Renesas Technology Singapore Pte. Ltd.
1 Harbour Front Avenue, #06-10, Keppel Bay Tower, Singapore 098632 Tel: <65> 6213-0200, Fax: <65> 6278-8001

Renesas Technology Korea Co., Ltd. Kukje Center Bldg. 18th Fl., 191, 2-ka, Hangang-ro, Yongsan-ku, Seoul 140-702, Korea Tel: <82> (2) 796-3115, Fax: <82> (2) 796-2145

Renesas Technology Malaysia Sdn. Bhd
Unit 906, Block B, Menara Amcorp, Amcorp Trade Centre, No.18, Jalan Persiaran Barat, 46050 Petaling Jaya, Selangor Darul Ehsan, Malaysia Tel: <603> 7955-9390, Fax: <603> 7955-9510