

100 mA Positive Voltage Regulators

The STC78L00 Series of positive voltage regulators are inexpensive, easy-to-use devices suitable for a multitude of applications that require a regulated supply of up to 100 mA. Like their higher powered STC7800 and STC78M00 Series cousins, these regulators feature internal current limiting and thermal shutdown making them remarkably rugged. No external components are required with the STC78L00 devices in many applications.

These devices offer a substantial performance advantage over the traditional zener diode-resistor combination, as output impedance and quiescent current are substantially reduced.

Features

- Wide Range of Available, Fixed Output Voltages
- Low Cost
- Internal Short Circuit Current Limiting
- Internal Thermal Overload Protection
- No External Components Required
- Complementary Negative Regulators Offered (STC79L00 Series)
- Pb-Free Packages are Available
- NCV Prefix for Automotive and Other Applications Requiring Site and Control Changes

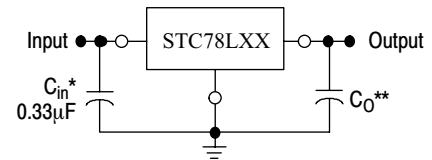
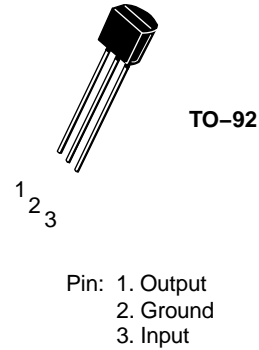


Figure 2. Standard Application

A common ground is required between the input and the output voltages. The input voltage must remain typically 2.0 V above the output voltage even during the low point on the input ripple voltage.

* C_{in} is required if regulator is located an appreciable distance from power supply filter.

** C_o is not needed for stability; however, it does improve transient response.

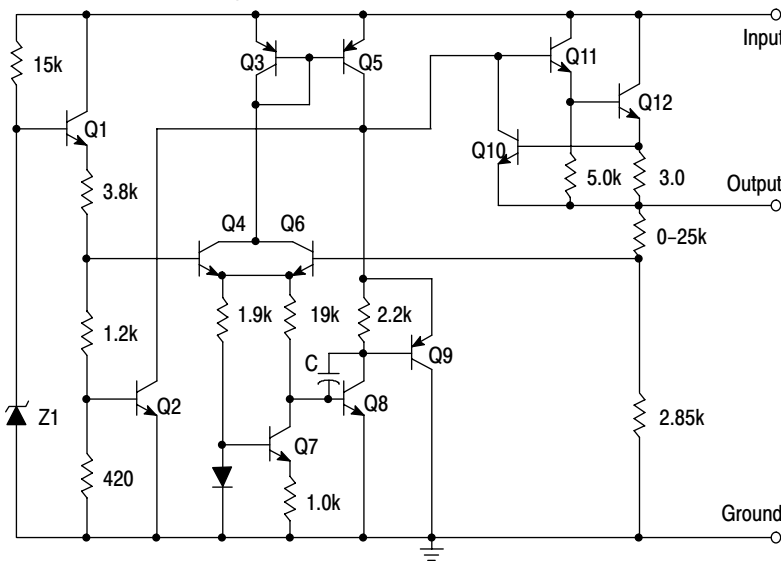


Figure 1. Representative Schematic Diagram

STC78L00 Series

MAXIMUM RATINGS (T_A = +125°C, unless otherwise noted.)

Rating	Symbol	Value	Unit
Input Voltage (2.6 V–8.0 V) (12 V–18 V)	V _I	30 35	Vdc
Storage Temperature Range	T _{stg}	–65 to +150	°C
Operating Junction Temperature Range	T _J	0 to +150	°C

Maximum ratings are those values beyond which device damage can occur. Maximum ratings applied to the device are individual stress limit values (not normal operating conditions) and are not valid simultaneously. If these limits are exceeded, device functional operation is not implied, damage may occur and reliability may be affected.

ELECTRICAL CHARACTERISTICS (V_I = 10 V, I_O = 40 mA, C_I = 0.33 μF, C_O = 0.1 μF, –40°C < T_J < +125°C, 0°C < T_J < +125°C, unless otherwise noted.)

Characteristics	Symbol	STC78L05			Unit
		Min	Typ	Max	
Output Voltage (T _J = +25°C)	V _O	4.8	5.0	5.2	Vdc
Line Regulation (T _J = +25°C, I _O = 40 mA) 7.0 Vdc ≤ V _I ≤ 20 Vdc 8.0 Vdc ≤ V _I ≤ 20 Vdc	Reg _{line}	– –	55 45	150 100	mV
Load Regulation (T _J = +25°C, 1.0 mA ≤ I _O ≤ 100 mA) (T _J = +25°C, 1.0 mA ≤ I _O ≤ 40 mA)	Reg _{load}	– –	11 5.0	60 30	mV
Output Voltage (7.0 Vdc ≤ V _I ≤ 20 Vdc, 1.0 mA ≤ I _O ≤ 40 mA) (V _I = 10 V, 1.0 mA ≤ I _O ≤ 70 mA)	V _O	4.75 4.75	– –	5.25 5.25	Vdc
Input Bias Current (T _J = +25°C) (T _J = +125°C)	I _{IB}	– –	3.8 –	6.0 5.5	mA
Input Bias Current Change (8.0 Vdc ≤ V _I ≤ 20 Vdc) (1.0 mA ≤ I _O ≤ 40 mA)	ΔI _{IB}	– –	– –	1.5 0.1	mA
Output Noise Voltage (T _A = +25°C, 10 Hz ≤ f ≤ 100 kHz)	V _n	–	40	–	μV
Ripple Rejection (I _O = 40 mA, f = 120 Hz, 8.0 Vdc ≤ V _I ≤ 18 V, T _J = +25°C)	RR	41	49	–	dB
Dropout Voltage (T _J = +25°C)	V _I – V _O	–	1.7	–	Vdc

NOTE: T_{low} = –40°C, T_{high} = +125°C. Guaranteed by design. NCV prefix is for automotive and other applications requiring site and change control.

STC78L00 Series

ELECTRICAL CHARACTERISTICS ($V_I = 14\text{ V}$, $I_O = 40\text{ mA}$, $C_I = 0.33\text{ }\mu\text{F}$, $C_O = 0.1\text{ }\mu\text{F}$, $-40^\circ\text{C} < T_J < +125^\circ\text{C}$, $0^\circ\text{C} < T_J < +125^\circ\text{C}$, unless otherwise noted.)

Characteristics	Symbol	STC78L08			Unit
		Min	Typ	Max	
Output Voltage ($T_J = +25^\circ\text{C}$)	V_O	7.7	8.0	8.3	Vdc
Line Regulation ($T_J = +25^\circ\text{C}$, $I_O = 40\text{ mA}$) $10.5\text{ Vdc} \leq V_I \leq 23\text{ Vdc}$ $11\text{ Vdc} \leq V_I \leq 23\text{ Vdc}$	Reg_{line}	– –	20 12	175 125	mV
Load Regulation ($T_J = +25^\circ\text{C}$, $1.0\text{ mA} \leq I_O \leq 100\text{ mA}$) ($T_J = +25^\circ\text{C}$, $1.0\text{ mA} \leq I_O \leq 40\text{ mA}$)	Reg_{load}	– –	15 8.0	80 40	mV
Output Voltage ($10.5\text{ Vdc} \leq V_I \leq 23\text{ Vdc}$, $1.0\text{ mA} \leq I_O \leq 40\text{ mA}$) ($V_I = 14\text{ V}$, $1.0\text{ mA} \leq I_O \leq 70\text{ mA}$)	V_O	7.6 7.6	– –	8.4 8.4	Vdc
Input Bias Current ($T_J = +25^\circ\text{C}$) ($T_J = +125^\circ\text{C}$)	I_{IB}	– –	3.0 –	6.0 5.5	mA
Input Bias Current Change ($11\text{ Vdc} \leq V_I \leq 23\text{ Vdc}$) ($1.0\text{ mA} \leq I_O \leq 40\text{ mA}$)	ΔI_{IB}	– –	– –	1.5 0.1	mA
Output Noise Voltage ($T_A = +25^\circ\text{C}$, $10\text{ Hz} \leq f \leq 100\text{ kHz}$)	V_n	–	60	–	μV
Ripple Rejection ($I_O = 40\text{ mA}$, $f = 120\text{ Hz}$, $12\text{ V} \leq V_I \leq 23\text{ V}$, $T_J = +25^\circ\text{C}$)	RR	37	57	–	dB
Dropout Voltage ($T_J = +25^\circ\text{C}$)	$V_I - V_O$	–	1.7	–	Vdc

ELECTRICAL CHARACTERISTICS ($V_I = 15\text{ V}$, $I_O = 40\text{ mA}$, $C_I = 0.33\text{ }\mu\text{F}$, $C_O = 0.1\text{ }\mu\text{F}$, $-40^\circ\text{C} < T_J < +125^\circ\text{C}$, $0^\circ\text{C} < T_J < +125^\circ\text{C}$, unless otherwise noted.)

Characteristics	Symbol	STC78L09			Unit
		Min	Typ	Max	
Output Voltage ($T_J = +25^\circ\text{C}$)	V_O	8.6	9.0	9.4	Vdc
Line Regulation ($T_J = +25^\circ\text{C}$, $I_O = 40\text{ mA}$) $11.5\text{ Vdc} \leq V_I \leq 24\text{ Vdc}$ $12\text{ Vdc} \leq V_I \leq 24\text{ Vdc}$	Reg_{line}	– –	20 12	175 125	mV
Load Regulation ($T_J = +25^\circ\text{C}$, $1.0\text{ mA} \leq I_O \leq 100\text{ mA}$) ($T_J = +25^\circ\text{C}$, $1.0\text{ mA} \leq I_O \leq 40\text{ mA}$)	Reg_{load}	– –	15 8.0	90 40	mV
Output Voltage ($11.5\text{ Vdc} \leq V_I \leq 24\text{ Vdc}$, $1.0\text{ mA} \leq I_O \leq 40\text{ mA}$) ($V_I = 15\text{ V}$, $1.0\text{ mA} \leq I_O \leq 70\text{ mA}$)	V_O	8.5 8.5	– –	9.5 9.5	Vdc
Input Bias Current ($T_J = +25^\circ\text{C}$) ($T_J = +125^\circ\text{C}$)	I_{IB}	– –	3.0 –	6.0 5.5	mA
Input Bias Current Change ($11\text{ Vdc} \leq V_I \leq 23\text{ Vdc}$) ($1.0\text{ mA} \leq I_O \leq 40\text{ mA}$)	ΔI_{IB}	– –	– –	1.5 0.1	mA
Output Noise Voltage ($T_A = +25^\circ\text{C}$, $10\text{ Hz} \leq f \leq 100\text{ kHz}$)	V_n	–	60	–	μV
Ripple Rejection ($I_O = 40\text{ mA}$, $f = 120\text{ Hz}$, $13\text{ V} \leq V_I \leq 24\text{ V}$, $T_J = +25^\circ\text{C}$)	RR	37	57	–	dB
Dropout Voltage ($T_J = +25^\circ\text{C}$)	$V_I - V_O$	–	1.7	–	Vdc

STC78L00 Series

ELECTRICAL CHARACTERISTICS ($V_I = 19\text{ V}$, $I_O = 40\text{ mA}$, $C_I = 0.33\text{ }\mu\text{F}$, $C_O = 0.1\text{ }\mu\text{F}$, $-40^\circ\text{C} < T_J < +125^\circ\text{C}$, $0^\circ\text{C} < T_J < +125^\circ\text{C}$, unless otherwise noted.)

Characteristics	Symbol	STC78L12			Unit
		Min	Typ	Max	
Output Voltage ($T_J = +25^\circ\text{C}$)	V_O	11.5	12	12.5	Vdc
Line Regulation ($T_J = +25^\circ\text{C}$, $I_O = 40\text{ mA}$) $14.5\text{ Vdc} \leq V_I \leq 27\text{ Vdc}$ $16\text{ Vdc} \leq V_I \leq 27\text{ Vdc}$	Reg_{line}	– –	120 100	250 200	mV
Load Regulation ($T_J = +25^\circ\text{C}$, $1.0\text{ mA} \leq I_O \leq 100\text{ mA}$) ($T_J = +25^\circ\text{C}$, $1.0\text{ mA} \leq I_O \leq 40\text{ mA}$)	Reg_{load}	– –	20 10	100 50	mV
Output Voltage ($14.5\text{ Vdc} \leq V_I \leq 27\text{ Vdc}$, $1.0\text{ mA} \leq I_O \leq 40\text{ mA}$) ($V_I = 19\text{ V}$, $1.0\text{ mA} \leq I_O \leq 70\text{ mA}$)	V_O	11.4 11.4	– –	12.6 12.6	Vdc
Input Bias Current ($T_J = +25^\circ\text{C}$) ($T_J = +125^\circ\text{C}$)	I_{IB}	– –	4.2 –	6.5 6.0	mA
Input Bias Current Change ($16\text{ Vdc} \leq V_I \leq 27\text{ Vdc}$) ($1.0\text{ mA} \leq I_O \leq 40\text{ mA}$)	ΔI_{IB}	– –	– –	1.5 0.1	mA
Output Noise Voltage ($T_A = +25^\circ\text{C}$, $10\text{ Hz} \leq f \leq 100\text{ kHz}$)	V_n	–	80	–	μV
Ripple Rejection ($I_O = 40\text{ mA}$, $f = 120\text{ Hz}$, $15\text{ V} \leq V_I \leq 25\text{ V}$, $T_J = +25^\circ\text{C}$)	RR	37	42	–	dB
Dropout Voltage ($T_J = +25^\circ\text{C}$)	$V_I - V_O$	–	1.7	–	Vdc

ELECTRICAL CHARACTERISTICS ($V_I = 23\text{ V}$, $I_O = 40\text{ mA}$, $C_I = 0.33\text{ }\mu\text{F}$, $C_O = 0.1\text{ }\mu\text{F}$, $-40^\circ\text{C} < T_J < +125^\circ\text{C}$, $0^\circ\text{C} < T_J < +125^\circ\text{C}$, unless otherwise noted.)

Characteristics	Symbol	STC78L15			Unit
		Min	Typ	Max	
Output Voltage ($T_J = +25^\circ\text{C}$)	V_O	14.4	15	15.6	Vdc
Line Regulation ($T_J = +25^\circ\text{C}$, $I_O = 40\text{ mA}$) $17.5\text{ Vdc} \leq V_I \leq 30\text{ Vdc}$ $20\text{ Vdc} \leq V_I \leq 30\text{ Vdc}$	Reg_{line}	– –	130 110	300 250	mV
Load Regulation ($T_J = +25^\circ\text{C}$, $1.0\text{ mA} \leq I_O \leq 100\text{ mA}$) ($T_J = +25^\circ\text{C}$, $1.0\text{ mA} \leq I_O \leq 40\text{ mA}$)	Reg_{load}	– –	25 12	150 75	mV
Output Voltage ($17.5\text{ Vdc} \leq V_I \leq 30\text{ Vdc}$, $1.0\text{ mA} \leq I_O \leq 40\text{ mA}$) ($V_I = 23\text{ V}$, $1.0\text{ mA} \leq I_O \leq 70\text{ mA}$)	V_O	14.25 14.25	– –	15.75 15.75	Vdc
Input Bias Current ($T_J = +25^\circ\text{C}$) ($T_J = +125^\circ\text{C}$)	I_{IB}	– –	4.4 –	6.5 6.0	mA
Input Bias Current Change ($20\text{ Vdc} \leq V_I \leq 30\text{ Vdc}$) ($1.0\text{ mA} \leq I_O \leq 40\text{ mA}$)	ΔI_{IB}	– –	– –	1.5 0.1	mA
Output Noise Voltage ($T_A = +25^\circ\text{C}$, $10\text{ Hz} \leq f \leq 100\text{ kHz}$)	V_n	–	90	–	μV
Ripple Rejection ($I_O = 40\text{ mA}$, $f = 120\text{ Hz}$, $18.5\text{ V} \leq V_I \leq 28.5\text{ V}$, $T_J = +25^\circ\text{C}$)	RR	34	39	–	dB
Dropout Voltage ($T_J = +25^\circ\text{C}$)	$V_I - V_O$	–	1.7	–	Vdc

STC78L00 Series

ELECTRICAL CHARACTERISTICS ($V_I = 27\text{ V}$, $I_O = 40\text{ mA}$, $C_I = 0.33\text{ }\mu\text{F}$, $C_O = 0.1\text{ }\mu\text{F}$, $0^\circ\text{C} < T_J < +125^\circ\text{C}$, unless otherwise noted.)

Characteristics	Symbol	STC78L18			Unit
		Min	Typ	Max	
Output Voltage ($T_J = +25^\circ\text{C}$)	V_O	17.3	18	18.7	Vdc
Line Regulation ($T_J = +25^\circ\text{C}$, $I_O = 40\text{ mA}$) $21.4\text{ Vdc} \leq V_I \leq 33\text{ Vdc}$ $20.7\text{ Vdc} \leq V_I \leq 33\text{ Vdc}$ $22\text{ Vdc} \leq V_I \leq 33\text{ Vdc}$ $21\text{ Vdc} \leq V_I \leq 33\text{ Vdc}$	Reg_{line}	–	45	325	mV
Load Regulation ($T_J = +25^\circ\text{C}$, $1.0\text{ mA} \leq I_O \leq 100\text{ mA}$) ($T_J = +25^\circ\text{C}$, $1.0\text{ mA} \leq I_O \leq 40\text{ mA}$)	Reg_{load}	–	30	170	mV
Output Voltage ($21.4\text{ Vdc} \leq V_I \leq 33\text{ Vdc}$, $1.0\text{ mA} \leq I_O \leq 40\text{ mA}$) ($20.7\text{ Vdc} \leq V_I \leq 33\text{ Vdc}$, $1.0\text{ mA} \leq I_O \leq 40\text{ mA}$) ($V_I = 27\text{ V}$, $1.0\text{ mA} \leq I_O \leq 70\text{ mA}$) ($V_I = 27\text{ V}$, $1.0\text{ mA} \leq I_O \leq 70\text{ mA}$)	V_O	17.1	–	18.9	Vdc
Input Bias Current ($T_J = +25^\circ\text{C}$) ($T_J = +125^\circ\text{C}$)	I_{IB}	–	3.1	6.5	mA
Input Bias Current Change ($22\text{ Vdc} \leq V_I \leq 33\text{ Vdc}$) ($21\text{ Vdc} \leq V_I \leq 33\text{ Vdc}$) ($1.0\text{ mA} \leq I_O \leq 40\text{ mA}$)	ΔI_{IB}	–	–	1.5	mA
Output Noise Voltage ($T_A = +25^\circ\text{C}$, $10\text{ Hz} \leq f \leq 100\text{ kHz}$)	V_n	–	150	–	μV
Ripple Rejection ($I_O = 40\text{ mA}$, $f = 120\text{ Hz}$, $23\text{ V} \leq V_I \leq 33\text{ V}$, $T_J = +25^\circ\text{C}$)	RR	33	48	–	dB
Dropout Voltage ($T_J = +25^\circ\text{C}$)	$V_I - V_O$	–	1.7	–	Vdc

STC78L00 Series

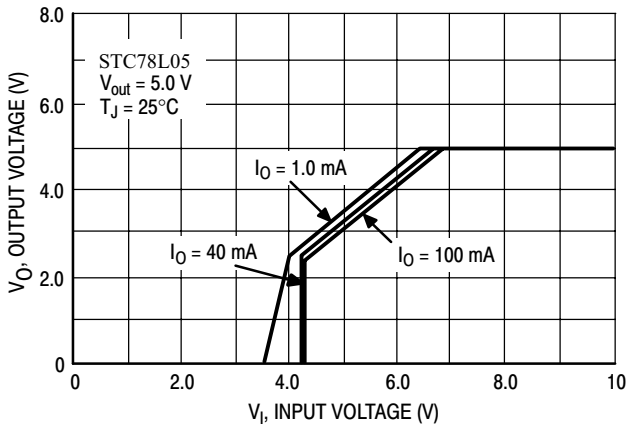


Figure 3. Dropout Characteristics

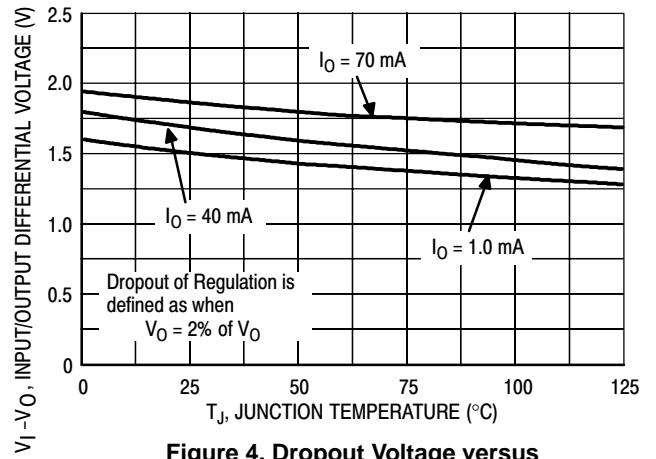


Figure 4. Dropout Voltage versus Junction Temperature

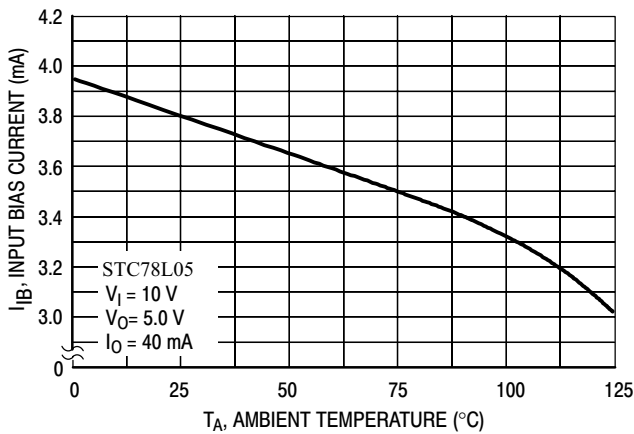


Figure 5. Input Bias Current versus Ambient Temperature

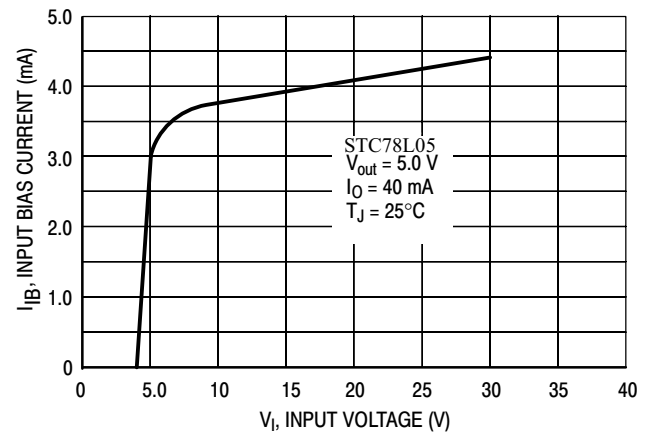


Figure 6. Input Bias Current versus Input Voltage

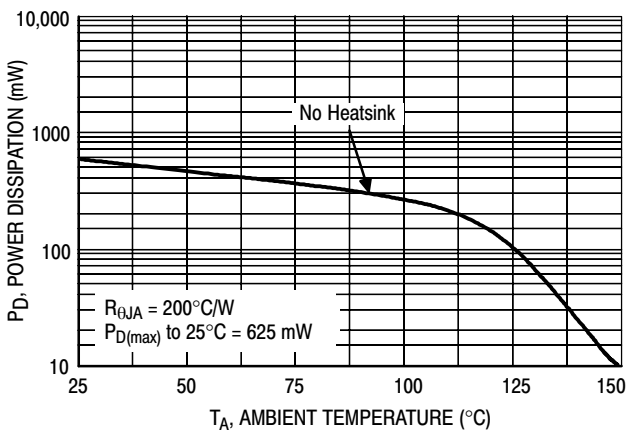


Figure 7. Maximum Average Power Dissipation versus Ambient Temperature – TO-92 Type Package

STC78L00 Series

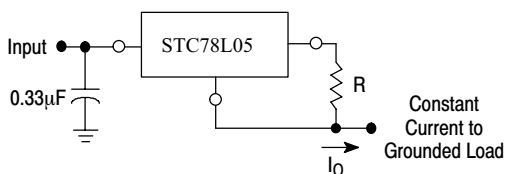
APPLICATIONS INFORMATION

Design Considerations

The STC78L00 Series of fixed voltage regulators are designed with Thermal Overload Protection that shuts down the circuit when subjected to an excessive power overload condition. Internal Short Circuit Protection limits the maximum current the circuit will pass.

In many low current applications, compensation capacitors are not required. However, it is recommended that the regulator input be bypassed with a capacitor if the regulator is connected to the power supply filter with long wire lengths, or if the output load capacitance is large. The

input bypass capacitor should be selected to provide good high-frequency characteristics to insure stable operation under all load conditions. A 0.33 μF or larger tantalum, mylar, or other capacitor having low internal impedance at high frequencies should be chosen. The bypass capacitor should be mounted with the shortest possible leads directly across the regulators input terminals. Good construction techniques should be used to minimize ground loops and lead resistance drops since the regulator has no external sense lead. Bypassing the output is also recommended.



The STC78L00 regulators can also be used as a current source when connected as above. In order to minimize dissipation the STC78L05 is chosen in this application. Resistor R determines the current as follows:

$$I_O = \frac{5.0\text{ V}}{R} + I_B$$

$I_B = 3.8\text{ mA}$ over line and load changes

For example, a 100 mA current source would require R to be a 50 Ω, 1/2 W resistor and the output voltage compliance would be the input voltage less 7 V.

Figure 8. Current Regulator

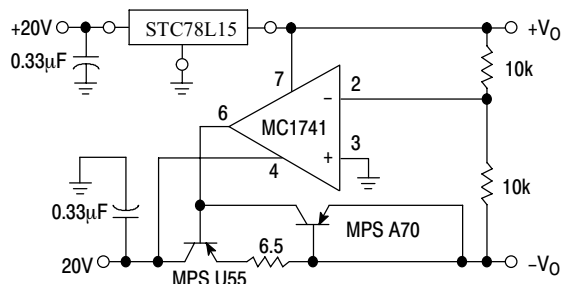


Figure 9. ± 15 V Tracking Voltage Regulator

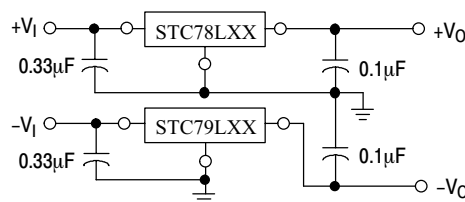


Figure 10. Positive and Negative Regulator