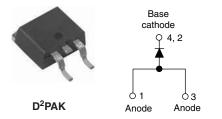


Vishay High Power Products

Phase Control SCR, 8 A



PRODUCT SUMMARY				
V _T at 8 A	< 1.2 V			
I _{TSM}	140 A			
V _{RRM}	800 V			

DESCRIPTION/FEATURES

The 12TTS08SPbF high voltage series of silicon controlled rectifiers are specifically designed for medium power switching and phase control applications. The glass passivation technology used has reliable operation up to 125 °C junction temperature.

Typical applications are in input rectification and crow-bar (soft start) and these products are designed to be used with Vishay HPP input diodes, switches and output rectifiers which are available in identical package outlines.

This product has been designed and qualified for industrial level and lead (Pb)-free ("PbF" suffix).

OUTPUT CURRENT IN TYPICAL APPLICATIONS								
APPLICATIONS	SINGLE-PHASE BRIDGE THREE-PHASE BRIDGE UNITS							
Capacitive input filter $T_A = 55 \text{ °C}$, $T_J = 125 \text{ °C}$, common heatsink of 1 °C/W	13.5	17	A					

MAJOR RATINGS AND CHARACTERISTICS						
PARAMETER	TEST CONDITIONS VALUES		UNITS			
I _{T(AV)}	Sinusoidal waveform	Sinusoidal waveform 8				
I _{T(RMS)}		12.5	A			
V _{RRM} /V _{DRM}		800	V			
I _{TSM}		140	А			
V _T	8 A, T _J = 25 °C	1.2	V			
dV/dt		150	V/µs			
dl/dt		100	A/µs			
TJ	Range	- 40 to 125	°C			

VOLTAGE RATINGS							
PART NUMBER	V _{RRM} , MAXIMUM PEAK REVERSE VOLTAGE V	V _{DRM} , MAXIMUM PEAK DIRECT VOLTAGE V	I _{RRM} /I _{DRM} AT 125 °C mA				
12TTS08SPbF	800	800	1.0				

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ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL		VALUES	UNITS	
Maximum average on-state current	I _{T(AV)}			8	
Maximum RMS on-state current	I _{T(RMS)}	1 _C = 108 C,	T_C = 108 °C, 180° conduction, half sine wave		
Maximum peak one-cycle	I	10 ms sine pu	Ilse, rated V_{RRM} applied, $T_J = 125 \ ^{\circ}C$	120	A
non-repetitive surge current	I _{TSM}	10 ms sine pu	Ilse, no voltage reapplied, $T_J = 125 \ ^{\circ}C$	140	
Maximum I ² t for fusing	l ² t	10 ms sine pu	Ilse, rated V_{RRM} applied, $T_J = 125 \ ^{\circ}C$	72	A ² s
	1-1	10 ms sine pu	Ilse, no voltage reapplied, $T_J = 125 \ ^{\circ}C$	100	
Maximum I ² \sqrt{t} for fusing	l²√t	t = 0.1 to 10 ms, no voltage reapplied, T_J = 125 °C		1000	A²√s
Maximum on-state voltage drop	V _{TM}	8 A, T _J = 25 °C		1.2	V
On-state slope resistance	r _t	T 125 °C		16.2	mΩ
Threshold voltage	V _{T(TO)}	1j=125 C	T _J = 125 °C		V
Maximum reverse and direct leakage current	1/1	T _J = 25 °C	V _R = Rated V _{RRM} /V _{DRM}	0.05	
Maximum reverse and direct leakage current	I _{RM} /I _{DM}	T _J = 125 °C	VR = naleu VRRM/ VDRM	1.0	
Typical holding current	Ι _Η	Anode supply = 6 V, resistive load, initial $I_T = 1 A$		30	mA
Maximum latching current	١L	Anode supply = 6 V, resistive load		50	
Maximum rate of rise of off-state voltage	dV/dt	T _J = 25 °C		150	V/µs
Maximum rate of rise of turned-on current	dl/dt			100	A/µs

TRIGGERING					
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Maximum peak gate power	P _{GM}		8.0	w	
Maximum average gate power	P _{G(AV)}		2.0	vv	
Maximum peak positive gate current	+ I _{GM}		1.5	А	
Maximum peak negative gate voltage	- V _{GM}		10	V	
	I _{GT}	Anode supply = 6 V, resistive load, T_J = - 65 °C	20		
Maximum required DC gate current to trigger		Anode supply = 6 V, resistive load, T_J = 25 °C	15	mA	
		Anode supply = 6 V, resistive load, T_J = 125 °C	10		
	V _{GT}	Anode supply = 6 V, resistive load, T_J = - 65 °C	1.2		
Maximum required DC gate voltage to trigger		Anode supply = 6 V, resistive load, T_J = 25 °C	1	v	
volage to algger		Anode supply = 6 V, resistive load, $T_J = 125 \text{ °C}$	0.7	v	
Maximum DC gate voltage not to trigger	V_{GD}	$- T_{\rm J} = 125 ^{\circ}\text{C}, V_{\rm DRM} = \text{Rated value} \qquad \qquad$			
Maximum DC gate current not to trigger	I _{GD}			mA	

SWITCHING					
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Typical turn-on time	t _{gt}	T _J = 25 °C	0.8		
Typical reverse recovery time	t _{rr}	T 105 %	3	μs	
Typical turn-off time	t _q	T _J = 125 °C	100		

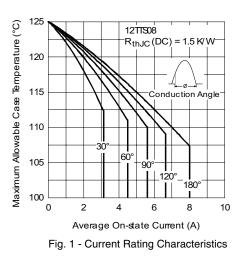


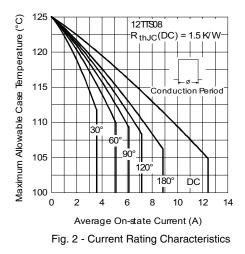
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THERMAL AND MECHANICAL SPECIFICATIONS					
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction and sto temperature range	rage	T _J , T _{Stg}		- 40 to 125	°C
Maximum thermal resistar junction to case	ice,	R _{thJC}	DC operation	1.5	
Maximum thermal resistar junction to ambient	ice,	R _{thJA}		62	°C/W
Typical thermal resistance case to heatsink	3	R _{thCS}	Mounting surface, smooth and greased	0.5	
Approvimete weight				2	g
Approximate weight				0.07	oz.
Mounting torque minimum maximum				6 (5)	kgf · cm
				12 (10)	(lbf ⋅ in)
Marking device			Case style D ² PAK (SMD-220)	12TTS	08S

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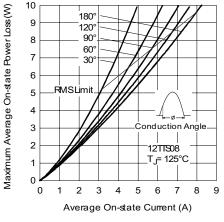


Fig. 3 - On-State Power Loss Characteristics

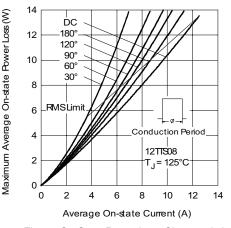


Fig. 4 - On-State Power Loss Characteristics

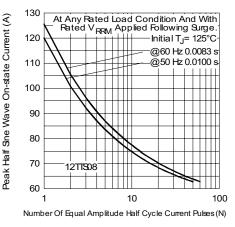


Fig. 5 - Maximum Non-Repetitive Surge Current

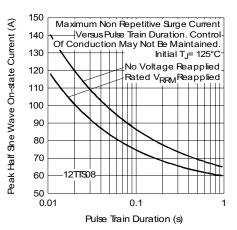


Fig. 6 - Maximum Non-Repetitive Surge Current



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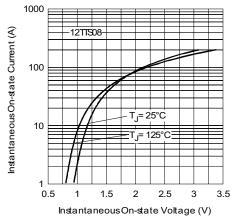


Fig. 7 - On-State Voltage Drop Characteristics

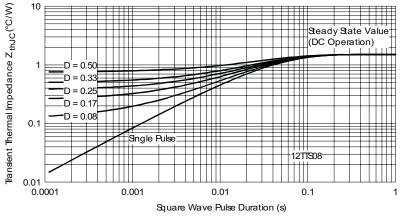


Fig. 8 - Thermal Impedance Z_{thJC} Characteristics

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ORDERING INFORMATION TABLE

Device code	12	т	т	S	08	S	TRL	PbF
	1	2	3	4	5	6	7	8
	1 - 2 -	Circ	rent ratii cuit confi Single t	guration	-			
	3 -	T = Single thyristor - Package: T = TO-220AC						
	4 -	Тур	e of silic Standa	on:	ery rect	ifier		
	5 - 6 - 7 -	Voltage rating (08 = 800 V) S = TO-220 D ² PAK (SMD-220) version • None = Tube						
	8 -	• TF • No	 TRL = Tape and reel (left oriented) TRR = Tape and reel (right oriented) None = Standard production PbF = Lead (Pb)-free 					

LINKS TO RELATED DOCUMENTS				
Dimensions	http://www.vishay.com/doc?95046			
Part marking information	http://www.vishay.com/doc?95054			
Packaging information	http://www.vishay.com/doc?95032			



Vishay

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