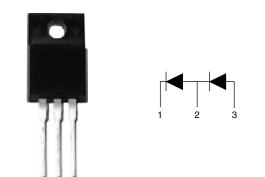


### Vishay High Power Products

## Hyperfast Rectifier, 8 A FRED Pt<sup>TM</sup>



PRODUCT SUMMARY				
t <sub>rr</sub>	19 ns			
I <sub>F(AV)</sub>	8 A			
V <sub>R</sub>	600 V			

3L TO-220 FULL-PAK

#### **FEATURES**

- Hyperfast recovery time
- · Low forward voltage drop
- · Low leakage current
- 175 °C operating junction temperature
- Designed and qualified for industrial level

#### **DESCRIPTION**

8STH06FP 600 V series are the state of the art tandem hyperfast recovery rectifiers: excellent switching performance and extremely low forward voltage drop trade off is overcome, boosting overall application performance.

Specially designed for CCM PFC application, these devices show incomparable performance in every current intensive hard switching application.

Optimized reverse recovery stored charge enables downsizing of boosting switch and cooling system, increased operating frequency make possible use of smaller reactive elements. Cost effective PFC application is then possible with high efficiency over wide input voltage range and loading factor.

Plastic insulated package features easy mounting together with not insulated parts.

ABSOLUTE MAXIMUM RATINGS FOR BOTH DIODES					
PARAMETER	SYMBOL TEST CONDITIONS		MAX.	UNITS	
Repetitive peak reverse voltage	$V_{RRM}$		600	V	
DC forward current	I <sub>F</sub> 50 % duty cycle, rect. waveforms, T <sub>C</sub> = 93 °C		8	^	
Non-repetitive peak surge current	I <sub>FSM</sub>	T <sub>C</sub> = 25 °C	100	A	
Operating junction and storage temperatures	$T_J$ , $T_{Stg}$		- 55 to 175	°C	

<b>ELECTRICAL SPECIFICATIONS FOR BOTH DIODES</b> (T <sub>J</sub> = 25 °C unless otherwise specified)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Breakdown voltage, blocking voltage	V <sub>BR</sub> , V <sub>R</sub>	Ι <sub>R</sub> = 100 μΑ	600	-	-	
		I <sub>F</sub> = 8 A	-	2.1	2.4	V
Forward voltage V <sub>F</sub>	$V_{F}$	I <sub>F</sub> = 8 A, T <sub>J</sub> = 125 °C	-	1.7	2	
	I <sub>F</sub> = 8 A, T <sub>J</sub> = 150 °C	-	1.6	1.8		
		$V_R = V_R$ rated	-	< 1	10	
Reverse leakage current I <sub>R</sub>	I <sub>R</sub>	T <sub>J</sub> = 125 °C, V <sub>R</sub> = V <sub>R</sub> rated	-	7	80	μΑ
		T <sub>J</sub> = 150 °C, V <sub>R</sub> = V <sub>R</sub> rated	-	27	100	
Junction capacitance	C <sub>T</sub>	V <sub>R</sub> = 600 V	-	12	-	pF

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### **New Product**

# 8STH06FP

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<b>DYNAMIC RECOVERY CHARACTERISTICS FOR BOTH DIODES</b> ( $T_J = 25$ °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNITS
		$I_F = 1.0 \text{ A}, dI_F/dt = -50 \text{ A/}\mu\text{s}, V_R = 30 \text{ V}$		-	-	25	
Reverse recovery time	erse recovery time t <sub>rr</sub>	T <sub>J</sub> = 25 °C		-	19	-	ns
		T <sub>J</sub> = 125 °C		-	35	-	
Peak recovery current I <sub>RRM</sub>	rrent I <sub>RRM</sub>	T <sub>J</sub> = 25 °C	$I_F = 8 \text{ A}$ $dI_F/dt = -200 \text{ A/}\mu\text{s}$ $V_R = 390 \text{ V}$	-	2.8	-	А
		T <sub>J</sub> = 125 °C		-	4.6	5.5	Α
Reverse recovery charge Q <sub>r</sub>	0	T <sub>J</sub> = 25 °C		=	26	=	nC
	Q <sub>rr</sub>	T <sub>J</sub> = 125 °C		-	84	-	IIC

THERMAL - MECHANICAL SPECIFICATIONS FOR BOTH DIODES						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Maximum junction and storage temperature range	T <sub>J</sub> , T <sub>Stg</sub>		- 55	-	175	°C
Thermal resistance, junction to case	R <sub>thJC</sub>		-	4.1	4.8	°C/W
Thermal resistance, case to heatsink	R <sub>thCS</sub>	Mounting surface, flat, smooth and greased	-	0.2	-	- *C/VV
Weight			-	2.0	-	g
vveigni			-	0.07	-	OZ.
Mounting torque			6.0 (5.0)	-	12 (10)	kgf · cm (lbf · in)
Marking device		Case style 3L TO-220 FULL-PAK	8STH06FP			

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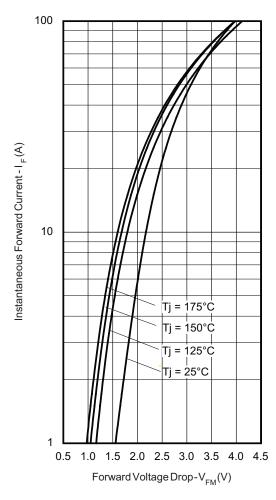


Fig. 1 - Maximum Forward Voltage Drop Characteristics

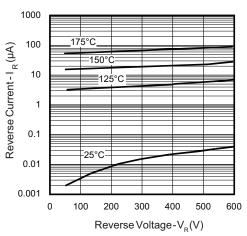


Fig. 2 - Typical Values of Reverse Current vs.
Reverse Voltage

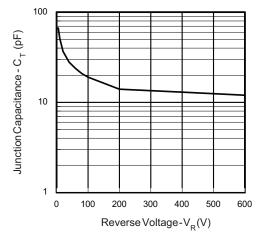


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

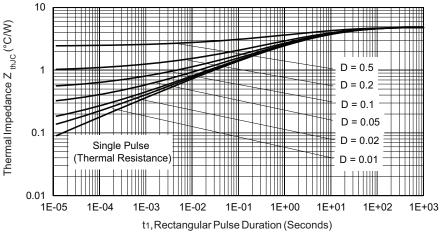


Fig. 4 - Maximum Thermal Impedance Z<sub>thJC</sub> Characteristics

## 8STH06FP

# Vishay High Power Products

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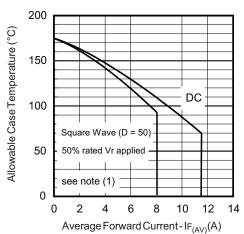


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current

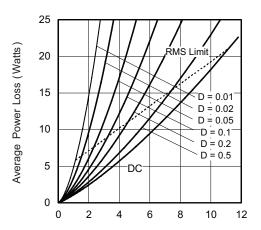


Fig. 6 - Forward Power Loss Characteristics

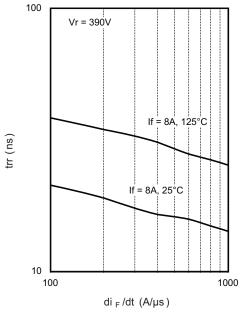


Fig. 7 - Typical Reverse Recovery Time vs.  $dI_F/dt$ 

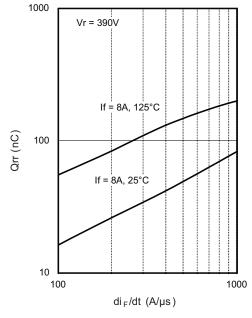


Fig. 8 - Typical Stored Charge vs.  $dI_F/dt$ 

#### Note

(1) Formula used:  $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}$ ;  $Pd = Forward power loss = I_{F(AV)} \times V_{FM} at (I_{F(AV)}/D)$  (see fig. 6);  $Pd_{REV} = Inverse power loss = V_{R1} \times I_R (1 - D)$ ;  $I_R$  at  $V_{R1} = 50$  % rated  $V_R$ 

LINKS TO RELATED DOCUMENTS				
Dimensions http://www.vishay.com/doc?95264				
Part marking information	http://www.vishay.com/doc?95266			



Vishay

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