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3-to-8-line Decoder/Demultiplexer



ADE-205-444 (Z) 1st. Edition Sep. 2000

#### Description

The HD74HC138 has 3 binary select inputs (A, B and C). If the device is enabled these inputs determine which one of the eight normally high outputs will go low. Two active low and one active high enables ( $G_1$ ,  $G_{2A}$  and  $G_{2B}$ ) are provided to ease the cascading of decoders.

#### Features

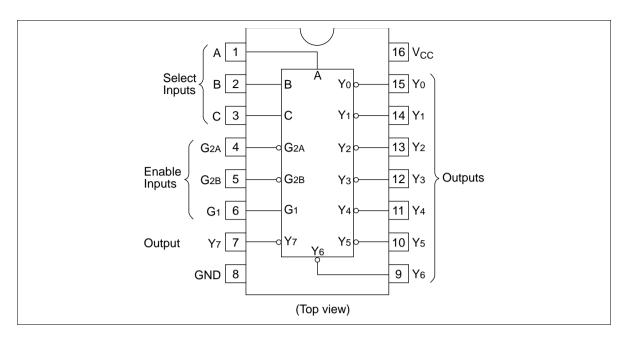
- High Speed Operation:  $t_{pd}$  (A, B, C to Y) = 16.5 ns typ ( $C_L$  = 50 pF)
- High Output Current: Fanout of 10 LSTTL Loads
- Wide Operating Voltage:  $V_{CC} = 2 V \text{ to } 6 V$
- Low Input Current: 1 µA max
- Low Quiescent Supply Current:  $I_{CC}$  (static) = 4  $\mu$ A max (Ta = 25°C)

mpt	113													
Ena	Enable Select					Outputs								
G1	$\mathbf{G}_{_{2A}}$	$\mathbf{G}_{_{2B}}$	С	В	Α	Y <sub>0</sub>	Υ <sub>1</sub>	Y <sub>2</sub>	Y <sub>3</sub>	Y <sub>4</sub>	Y <sub>5</sub>	Y <sub>6</sub>	<b>Y</b> <sub>7</sub>	
Х	Х	Н	Х	Х	Х	Н	Н	Н	Н	Н	Н	Н	Н	
Х	Н	Х	Х	Х	Х	Н	Н	Н	Н	Н	Н	Н	Н	_
L	Х	Х	Х	Х	Х	Н	Н	Н	Н	Н	Н	Н	Н	_
Н	L	L	L	L	L	L	Н	Н	Н	Н	Н	Н	Н	
Н	L	L	L	L	Н	Н	L	Н	Н	Н	Н	Н	Н	_
Н	L	L	L	Н	L	Н	Н	L	Н	Н	Н	Н	Н	
Н	L	L	L	Н	Н	Н	Н	Н	L	Н	Н	Н	Н	
Н	L	L	Н	L	L	Н	Н	Н	Н	L	Н	Н	Н	
Н	L	L	Н	L	Н	Н	Н	Н	Н	Н	L	Н	Н	
Н	L	L	Н	Н	L	Н	Н	Н	Н	Н	Н	L	Н	
Н	L	L	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	L	_

## **Function Table**

Inputs

## **Pin Arrangement**





## **DC** Characteristics

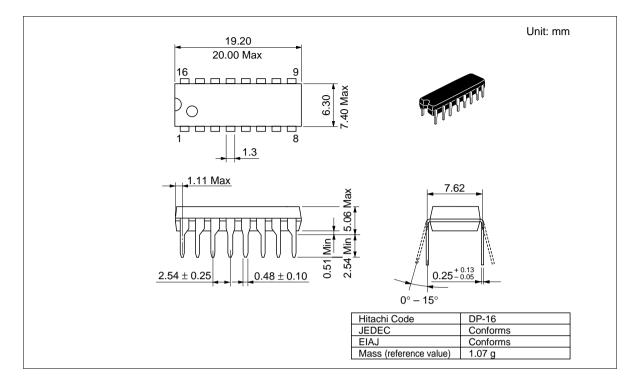
			Ta =	: 25°C		Ta = - +85°C	–40 to C			
Item	Symbol	V <sub>cc</sub> (V)	Min	Тур	Max	Min	Max	Unit	Test Condition	าร
Input voltage	V <sub>IH</sub>	2.0	1.5			1.5	_	V		
		4.5	3.15	—	—	3.15	—			
		6.0	4.2		—	4.2	—	_		
	V <sub>IL</sub>	2.0			0.5	—	0.5	V		
		4.5		_	1.35	_	1.35	_		
		6.0	_	_	1.8	_	1.8	_		
Output voltage	V <sub>OH</sub>	2.0	1.9	2.0	_	1.9	_	V	$Vin = V_{IH} \text{ or } V_{IL}$	I <sub>OH</sub> = -20 μA
		4.5	4.4	4.5	_	4.4	_	_		
		6.0	5.9	6.0	—	5.9	—	_		
		4.5	4.18			4.13	—	_		$I_{OH} = -4 \text{ mA}$
		6.0	5.68		_	5.63	_	_		I <sub>он</sub> = -5.2 mA
	V <sub>OL</sub>	2.0		0.0	0.1	—	0.1	V	$Vin = V_{IH} \text{ or } V_{IL}$	$I_{OL} = 20 \ \mu A$
		4.5		0.0	0.1	—	0.1	_		
		6.0	_	0.0	0.1	_	0.1	_		
		4.5			0.26		0.33	_		$I_{OL} = 4 \text{ mA}$
		6.0			0.26		0.33	_		I <sub>oL</sub> = 5.2 mA
Input current	lin	6.0	_	_	±0.1	_	±1.0	μΑ	$Vin = V_{CC} \text{ or } GN$	ND
Quiescent supply current	I <sub>cc</sub>	6.0		—	4.0	—	40	μΑ	Vin = V <sub>cc</sub> or GN	ND, lout = 0 μA

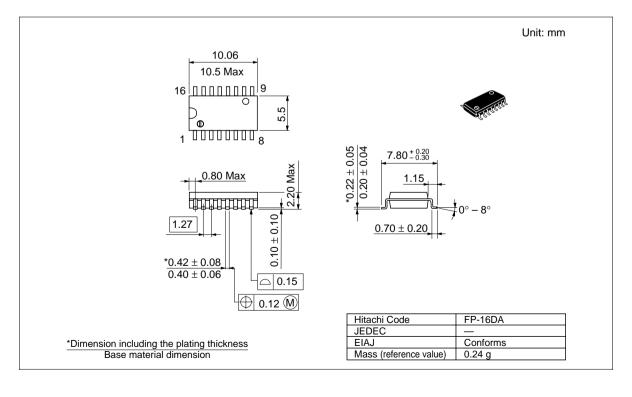


## **AC Characteristics** ( $C_L = 50 \text{ pF}$ , Input $t_r = t_f = 6 \text{ ns}$ )

			Ta =	: 25°C	;	Ta = - +85°C	-40 to		
Item	Symbol	$V_{cc}$ (V)	Min	Тур	Max	Min	Max	Unit	Test Conditions
Propagation delay	t <sub>PHL</sub>	2.0	_	—	175	_	220	ns	A, B or C to Output
time		4.5	—	17	35	_	44	_	
		6.0	—	—	30	_	37		
	t <sub>PLH</sub>	2.0		—	150	_	190	ns	
		4.5		16	30	—	38		
		6.0	_	—	26	—	33	_	
	t <sub>PHL</sub>	2.0		_	150	—	190	ns	G <sub>1</sub> to Output
		4.5		16	30	—	38	-	
		6.0		_	26	—	33	-	
	t <sub>PLH</sub>	2.0		_	150	—	190	ns	_
		4.5		17	30	—	38	-	
		6.0		_	26	—	33	-	
	t <sub>PHL</sub>	2.0		_	175	—	220	ns	$G_{2A}$ or $G_{2B}$ to Output
		4.5		15	35	_	44	-	
		6.0		_	30	—	37	=	
	t <sub>PLH</sub>	2.0		_	150	—	190	ns	_
		4.5		17	30	_	38	-	
		6.0	_	_	26	_	33	=	
Output rise/fall	t <sub>TLH</sub>	2.0	—	—	75	_	95	ns	
time	t <sub>THL</sub>	4.5	_	5	15	_	19	-	
		6.0	_	_	13	_	16	-	
Input capacitance	Cin		_	5	10	_	10	pF	

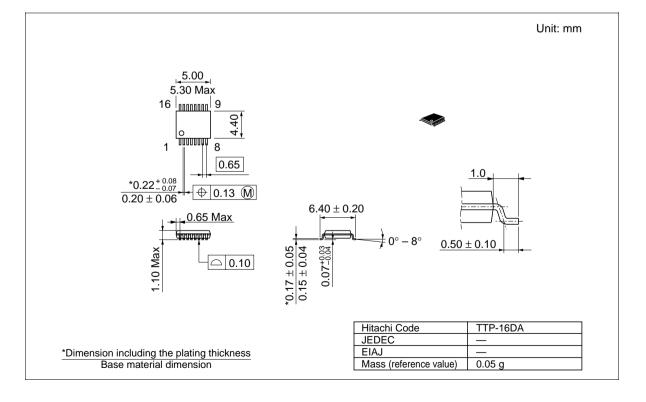
#### **Package Dimensions**







	Unit: mm
9.9 10.3 Max 16 <u>п</u> пппппп 9 0 0 1 0000000 8	Perfected
1.27 $1.27$	$\begin{array}{c} & & & & & & & & & & & & & & & & & & &$
*Dimension including the plating thickness Base material dimension	Hitachi CodeFP-16DNJEDECConformsEIAJConformsMass (reference value)0.15 g



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