



PLL Product Specification

Model: PLL250-588(Y) **Rev:** P4 **Date:** 9/1/2006
Customer: SIRENZA MICRODEVICES, INC. **AppNote:** 113 Option 007000
Operating Temperature Range: (-10 ° to 85 ° C)

Pb RoHS Compliant

To order models as RoHS Compliant add "Y" suffix to base model number.

Parameter	Min	Typ	Max	Units	X	Remarks
Frequency Range -	575		600	MHz	X	
Step Size -		200		kHz	X	
Settling Time - to within 5 deg.		0.4	0.45	mSec		
Output Power:						
RF Switch ON	0	2	4	dBm	X	
RF Switch OFF		-56	-50	dBm	X	
Output Phase Noise:						
1 kHz		-95	-90	dBc/Hz		
100 kHz		-128	-120	dBc/Hz		
200 kHz		-137	-130	dBc/Hz		
400 kHz		-144	-140	dBc/Hz		
800 kHz		-153	-149	dBc/Hz	X	
3000 kHz		-162	-156	dBc/Hz		
Power Supply:						
VCC 1: VCO, PLL CP	5.75	6	6.25	Volts	X	
VCC 2: Amp	4.75	5	5.25	Volts	X	
VCC 3: PLL IC,Switch	3.15	3.3	3.45	Volts	X	
Supply Current:						
VCC 1		15	40	mA	X	
VCC 2		30	80	mA		
VCC 3		10	15	mA		
Spurious Product:						
200 kHz		-94	-85	dBc	X	
400 kHz		-105	-90	dBc		
600 kHz		-110	-100	dBc		
800 kHz		-117	-105	dBc		
3000 kHz		-126	-115	dBc		
non-harmonic			-100	dBc		
Reference Feedthrough -		-98	-95	dBc		
Harmonic Suppression:						
2nd Harmonic		-27	-20	dBc	X	
3rd Harmonic		-28	-25	dBc	X	

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Parameter	Min	Typ	Max	Units	X	Remarks
Ref Osc Signal:						
Frequency		52		MHz		
Amplitude	0.75	1	1.25	Vp-p		
Input Impedance		100		k Ω		
Output Impedance -		50		Ω		
Output VSWR -		1.7:1				

Package Information

Package Type:	PLL250 (1 x 1 x 0.252 inches)	Drawing Number:	61070
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Comments

X Indicates parameter to be tested 100% in production

Switching Phase Error: By Design: < 50 us, 1 deg. typical - 3 deg. max.; > 50 us, 1 deg. max

Max Phase Noise Peaking = 6 dBc/Hz, By Design

All programming lines decoupled with RCR Low-Pass filter

Pin 19 decoupled with RCR Low-Pass filter

All Power Supplies decoupled with LCL-Low-Pass filter

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