

HD74LS158

Quadruple 2-line-to-1-line Data Selectors / Multiplexers (inverted outputs)

REJ03D0443-0200 Rev.2.00 Feb.18.2005

This data selector / multiplexer contains inverters and drivers to supply full on-chip data selection to the four output gates. A separate strobe input is provided. A 4-bit word is selected from one of two sources and is routed to the four outputs. Then, outputs present inverted data to minimize propagation delay time.

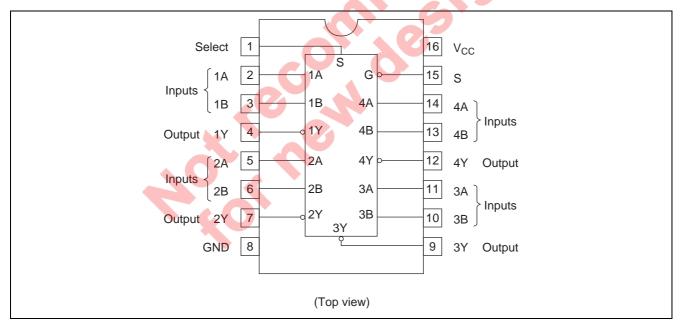
Features

Ordering Information

Part Name	Package Type	Package Code (Previous Code)	Package Abbreviation	Taping Abbreviation (Quantity)
HD74LS158FPEL	SOP-16 pin (JEITA)	PRSP0016DH-B (FP-16DAV)	FP	EL (2,000 pcs/reel)

Note: Please consult the sales office for the above package availability.

Pin Arrangement

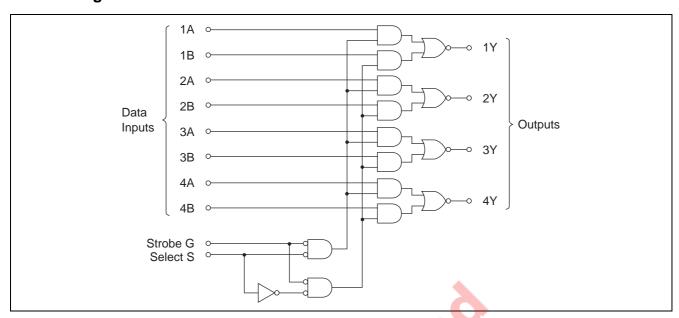


Function Table

	Output			
Strobe	Select	Υ		
Н	X	X	X	Н
L	L	L	X	Н
L	L	Н	X	L
L	Н	X	L	Н
L	Н	X	Н	L

 \boldsymbol{H} ; high level, \boldsymbol{L} ; low level, \boldsymbol{X} ; irrelevant

Block Diagram



Absolute Maximum Ratings

Item	Symbol	Ratings	Unit
Supply voltage	V _{CC}	7	V
Input voltage	V _{IN}	7	V
Power dissipation	Pt	400	mW
Storage temperature	Tstg	-65 to +150	°C

Note: Voltage value, unless otherwise noted, are with respect to network ground terminal.

Recommended Operating Conditions

Item	Symbol	Min	Тур	Max	Unit
Supply voltage	V _{CC}	4.75	5.00	5.25	V
Output ourront	I _{OH}	_	_	-400	μΑ
Output current	l _{OL}	_	_	8	mA
Operating temperature	Topr	-20	25	75	°C

Electrical Characteristics

 $(Ta = -20 \text{ to } +75 \text{ }^{\circ}\text{C})$

Item		Symbol	min.	typ.*	max.	Unit	Condition		
Input voltage		V _{IH}	2.0	_	_	V			
		V_{IL}	_	_	0.8	V			
0		V _{OH}	2.7	_	_	V	$V_{CC} = 4.75 \text{ V}, V_{IH} = 2 \text{ V}, V_{IL} = 0.8 \text{ V},$ $I_{OH} = -400 \mu\text{A}$		
Output voltage		V	_	_	0.4	V	$I_{OL} = 4 \text{ mA}$ $V_{CC} = 4.75 \text{ V}, V_{IH} = 2 \text{ V},$		
		V _{OL}	_	_	0.5	٧	$I_{OL} = 8 \text{ mA}$ $V_{IL} = 0.8 \text{ V}$		
	G, S		_	_	40	^	$V_{CC} = 5.25 \text{ V}, V_1 = 2.7 \text{ V}$		
	A, B	I _{IH}	_	_	20	μΑ	$v_{CC} = 5.25 \text{ v}, \text{ v}_1 = 2.7 \text{ v}$		
Input current	G, S	- I _{IL}	_	_	-0.8	mA	V _{CC} = 5.25 V, V _I = 0.4 V		
Input current	A, B		_	_	-0.4	ША	VCC = 3.23 V, VI = 0.4 V		
	G, S	ı	_	_	0.2	mA	V _{CC} = 5.25 V, V _I = 7 V		
	A, B	I _I	_	_	0.1	ША	VCC = 3.23 V, V = 1 V		
Short-circuit output current		los	-20	_	-100	mA	V _{CC} = 5.25 V		
Supply current**		Icc	_	4.8	8	mA	V _{CC} = 5.25 V		
Input clamp voltage		V _{IK}	_	_	-1.5	V	$V_{CC} = 4.75 \text{ V}, I_{IN} = -18 \text{ mA}$		

Notes: * V_{CC} = 5 V, Ta = 25°C

Switching Characteristics

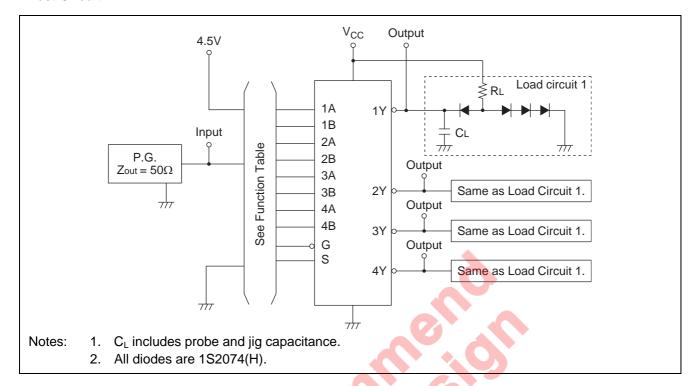
 $(V_{CC} = 5 \text{ V}, \text{ Ta} = 25^{\circ}\text{C})$

Item	Symbol	Inputs	Output	min.	typ.	max.	Unit	Condition
Propagation delay time	t _{PLH}	Data	Y	<u> </u>	7	12	ns	$C_L = 15 \text{ pF}, R_L = 2 \text{ k}\Omega$
	t _{PHL}			_	7	12		
	t _{PLH}	Strobe	Υ	4	11	17	ns	
	t _{PHL}			4	12	18		
	t _{PLH}	Select	Y	X -	13	20	ns	
	t _{PHL}	Select		_	16	24		

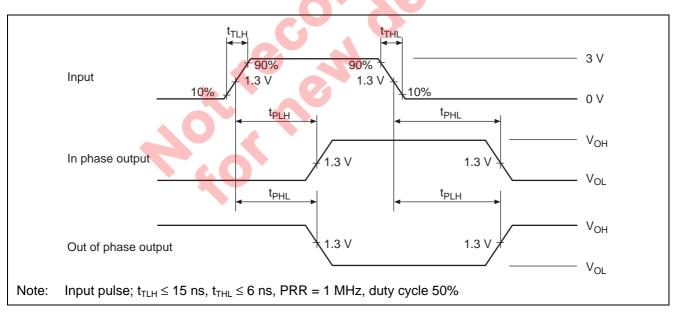
 $^{^{\}star\star}$ I_{CC} is measured with all outputs open and all inputs at 4.5 V.

Testing Method

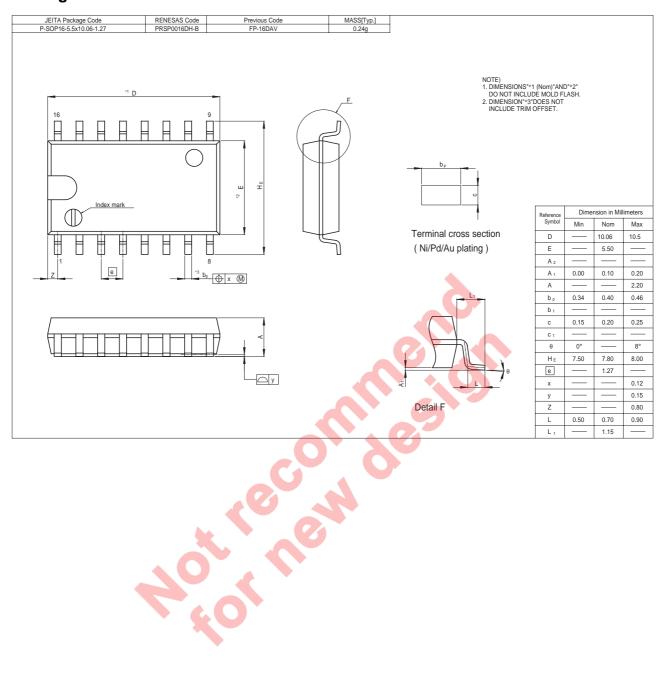
Test Circuit



Waveform



Package Dimensions



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