

# HD74LVC14 Hex Schmitt-trigger Inverters

REJ03D0345-0400 Rev.4.00 Nov 01, 2007

### Description

The HD74LVC14 has six schmitt trigger inverters in a 14 pin package. Low voltage and high-speed operation is suitable at the battery drive product (note type personal computer) and low power consumption extends the life of a battery for long time operation.

#### Features

- $V_{CC} = 2.0 \text{ V to } 5.5 \text{ V}$
- All inputs  $V_{IH}$  (Max.) = 5.5 V (@V<sub>CC</sub> = 0 V to 5.5 V)
- Typical V<sub>OL</sub> ground bounce < 0.8 V (@V<sub>CC</sub> = 3.3 V, Ta = 25°C)
- Typical  $V_{OH}$  undershoot > 2.0 V (@V<sub>CC</sub> = 3.3 V, Ta = 25°C)
- High output current  $\pm 24$  mA (@V<sub>CC</sub> = 3.0 V to 5.5 V)
- Ordering Information

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Part Name	Package Type	Package Code	Package	Taping Abbreviation	
	гаскауе туре	(Previous Code)	Abbreviation	(Quantity)	
HD74LVC14FPEL	SOP-14 pin (JEITA)	PRSP0014DF-B (FP–14DAV)	FP	EL (2,000 pcs/reel)	
HD74LVC14TELL	TSSOP-14 pin	PTSP0014JA-B (TTP–14DV)	Т	ELL (2,000 pcs/reel)	

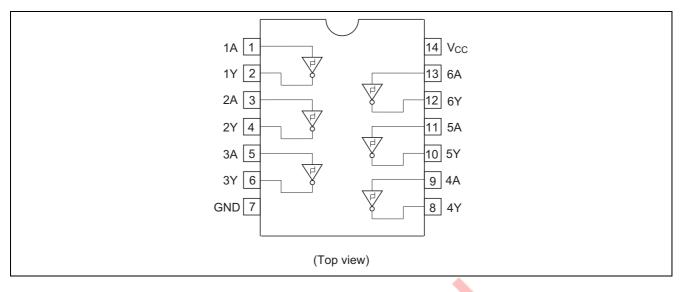
Note: Please consult the sales office for the above package availability.

### **Function Table**

		Input A	Output Y
		L	Н
		Н	L
H:	High level		

L: Low level

#### **Pin Arrangement**



### **Absolute Maximum Ratings**

Item	Symbol	Ratings	Unit	Conditions		
Supply voltage range	V <sub>CC</sub>	-0.5 to 6.0	V			
Input diode current	I <sub>IK</sub>	-50	mA	$V_1 = -0.5 V$		
Input voltage	VI	-0.5 to 6.0	V			
Output diada ourrant	I <sub>OK</sub>	-50	mA	$V_0 = -0.5 V$		
Output diode current		50	mA	$V_{O} = V_{CC} + 0.5 V$		
Output voltage	Vo	-0.5 to V <sub>cc</sub> +0.5	V			
Output current	lo	±50	mA			
V <sub>CC</sub> , GND current / pin	Icc or Ignd	100	mA			
Storage temperature	Tstg	-65 to +150	°C			

Note: The absolute maximum ratings are values, which must not individually be exceeded, and furthermore, no two of which may be realized at the same time.

### **Recommended Operating Conditions**

Item	Symbol	Ratings	Unit	Conditions	
Supply voltage	V <sub>cc</sub>	1.5 to 5.5	V	Data retention	
	VCC	2.0 to 5.5	v	At operation	
Input / Output voltage	VI	0 to 5.5	V	A	
	Vo	0 to V <sub>CC</sub>	v	Υ	
Operating temperature	Та	-40 to 85	°C		
	L	-12	mA	$V_{CC} = 2.7 V$	
Output current	I <sub>OH</sub>	-24 <sup>*1</sup>	ША	$V_{CC} = 3.0 \text{ V to } 5.5 \text{ V}$	
		12	mA	$V_{CC} = 2.7 V$	
	I <sub>OL</sub>	24 <sup>*1</sup>		$V_{CC} = 3.0 \text{ V} \text{ to } 5.5 \text{ V}$	

Notes: 1. Duty cycle  $\leq 50\%$ 

### **Electrical Characteristics**

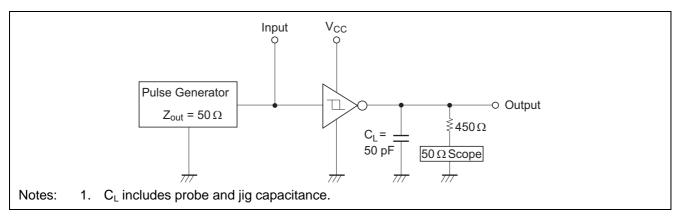
Item	Symbol	$V_{cc}(V)$ Ta = -4		to 85°C	Unit	Test Conditions		
nem		VCC (V)	Min	Max	Unit	Test conditions		
		2.7	1.0	2.0				
		3.0	1.2	2.2	V			
	$V_{T}^{+}$	3.6	1.5	2.4				
		4.5	1.6	2.6				
Threshold voltage		5.5	2.0	3.0				
Theshold voltage		2.7	0.4	1.4				
		3.0	0.6	1.5				
	$V_{T}^{-}$	3.6	0.8	1.8	V			
		4.5	1.0	2.0				
		5.5	1.4	2.4				
		2.7	0.3	1.1				
	V <sub>H</sub>	3.0	0.4	1.2				
Hysteresis voltage		3.6	0.4	1.2	V	$V_T^+ - V_T^-$		
		4.5	0.4	1.2				
		5.5	0.4	1.2				
	Vон	2.7 to 5.5	V <sub>CC</sub> -0.2			I <sub>OH</sub> = -100 μA		
		2.7	2.2			I <sub>он</sub> = –12 mA		
		3.0	2.4	-	V	I <sub>он</sub> = –12 mA		
		3.0	2.0			I <sub>ОН</sub> = -24 mA		
Output voltage		4.5	3.8			I <sub>ОН</sub> = -24 mA		
	V <sub>OL</sub>	2.7 to 5.5	—	0.2		I <sub>OL</sub> = 100 μA		
		2.7	—	0.4	N	I <sub>OL</sub> = 12 mA		
		3.0		0.55	v	I <sub>OL</sub> = 24 mA		
		4.5		0.55		I <sub>OL</sub> = 24 mA		
Input current	I <sub>IN</sub>	0 to 5.5		±5.0	μA	V <sub>IN</sub> = 5.5 V or GND		
	Icc	5.5	-	20	μA	$V_{IN} = V_{CC}$ or GND		
Quiescent supply current	Δl <sub>cc</sub>	3.0 to 3.6	7	500	μA	$V_{IN}$ = one input at (V <sub>CC</sub> -0.6)V, other inputs at V <sub>CC</sub> or GND		

## Switching Characteristics

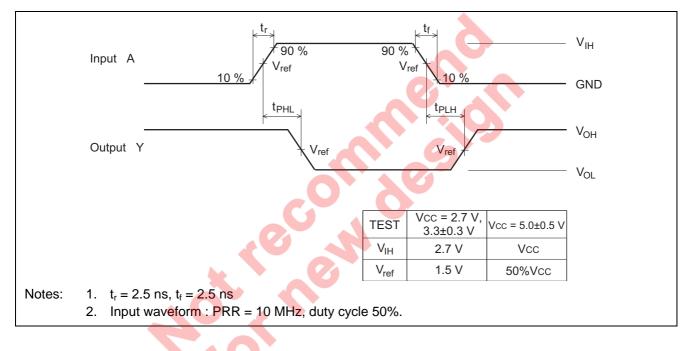
Item	Symbol	V <sub>cc</sub> (V)	Ta = –40 to 85°C			Unit	From	То
	Symbol		Min	Тур	Max	Unit	(Input)	(Output)
Propagation delay time		2.7		6.0	9.5			
	t <sub>PLH</sub>	3.3±0.3	1.5	5.0	8.5	ns	A	Y
	t <sub>PHL</sub>	5.0±0.5		3.5	7.0			
Input capacitance	C <sub>IN</sub>	2.7		3.0	—	pF		
Output capacitance	Co	2.7		15.0	—	pF		

#### HD74LVC14

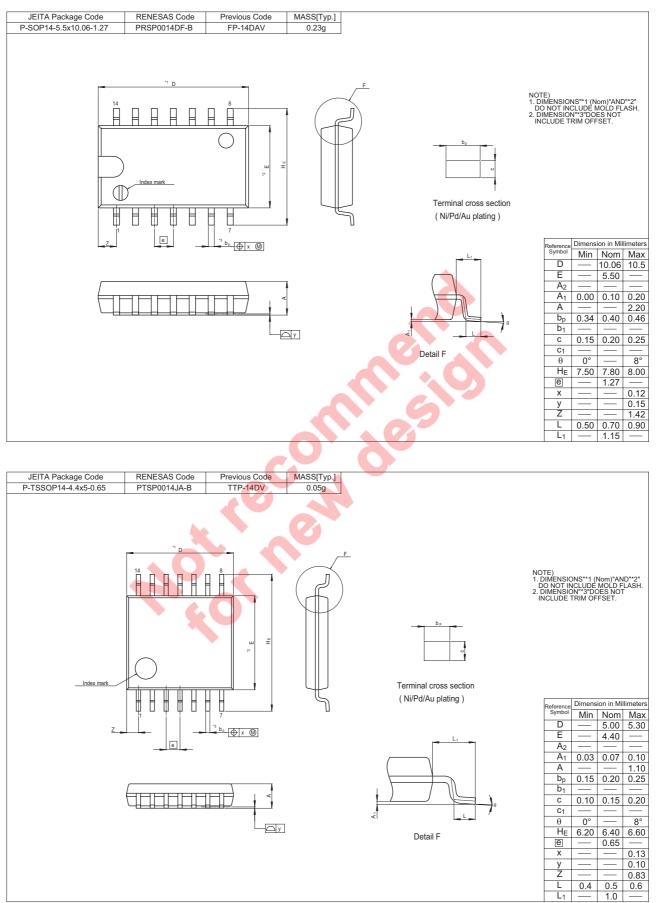
### **Test Circuit**



#### Waveforms



### **Package Dimensions**



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