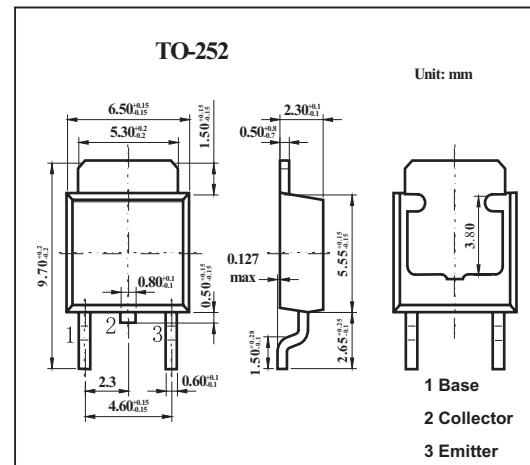


Low V_{CE(sat)} Transistor

2SD2118



■ Features

- Low V_{CE(sat)}.
- Excellent DC current gain characteristics.
- NPN silicon transistor.

■ Absolute Maximum Ratings Ta = 25°C

Parameter	Symbol	Rating	Unit
Collector-base voltage	V _{CBO}	50	V
Collector-emitter voltage	V _{CEO}	20	V
Emitter-base voltage	V _{EBO}	6	V
Collector current	I _C	5	A (DC)
		10	A(Pulse)*
Collector current (pulse) *	I _{CP}	10	A
Collector power dissipation	P _C	1	W
T _c = 25°C		10	W
Junction temperature	T _j	150	°C
Storage temperature	T _{stg}	-55 to +150	°C

* Pw=10ms.

■ Electrical Characteristics Ta = 25°C

Parameter	Symbol	Testconditons	Min	Typ	Max	Unit
Collector-base breakdown voltage	BV _{CBO}	I _C =50μA	50			V
Collector-emitter breakdown voltage	BV _{CEO}	I _C =1mA	20			V
Emitter-base breakdown voltage	BV _{EBO}	I _E =50μA	6			V
Collector cutoff current	I _{CBO}	V _{CB} =40V			0.5	μA
Emitter cutoff current	I _{EBO}	V _{EB} =5V			0.5	μA
Collector-emitter saturation voltage	V _{CE(sat)}	I _C =4 A, I _B =0.1A		0.3	1.0	V
DC current transfer ratio	h _{FE}	V _{CE} =2V, I _C =0.5A	120		390	
Output capacitance	f _T	V _{CE} =6V, I _E = -50mA, f=100MHz		150		MHz
Transition frequency	C _{ob}	V _{CB} =20V, I _E =0A, f=1MHz		30		pF

■ hFE Classification

Rank	Q	R
hFE	120~270	180~390