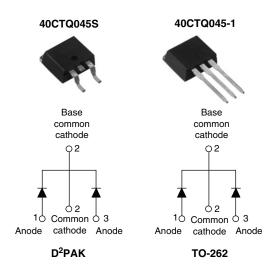


Vishay High Power Products

Schottky Rectifier, 2 x 20 A



PRODUCT SUMMARY					
I _{F(AV)} 2 x 20 A					
V _R	45 V				

FEATURES

- 150 °C T_J operation
- · Center tap configuration
- · Very low forward voltage drop
- High frequency operation
- High purity, high temperature epoxy encapsulation for enhanced mechanical strength and moisture resistance
- Guard ring for enhanced ruggedness and long term reliability
- Designed and qualified for Q101 level

DESCRIPTION

This center tap Schottky rectifier has been optimized for very low forward voltage drop, with moderate leakage. The proprietary barrier technology allows for reliable operation up to 150 °C junction temperature. Typical applications are in switching power supplies, converters, freewheeling diodes, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS						
SYMBOL	CHARACTERISTICS	CHARACTERISTICS VALUES U				
I _{F(AV)}	Rectangular waveform	40	Α			
V _{RRM}		45	V			
I _{FSM}	t _p = 5 μs sine	1240	Α			
V _F	20 Apk, T _J = 125 °C (per leg)	0.48	V			
T _J	Range	- 55 to 150	°C			

VOLTAGE RATINGS				
PARAMETER SYMBOL		40CTQ045S 40CTQ045-1	UNITS	
Maximum DC reverse voltage	V_{R}	45	V	
Maximum working peak reverse voltage	V_{RWM}	45	V	

ABSOLUTE MAXIMUM RATINGS								
PARAMETER		SYMBOL	L TEST CONDITIONS		VALUES	UNITS		
Maximum average	per leg	, ,	50 % duty cycle at T _C = 116 °C, rectangular waveform		50.0/ distributed at T = 110.00 variation and a variation and		20	
forward current See fig. 5 pe	r device	I _{F(AV)}			40			
Maximum peak one cycle non-repetitive surge current per leg See fig. 7		I _{FSM}	5 µs sine or 3 µs rect. pulse	Following any rated load condition and with rated V _{RRM} applied	1240	A		
			10 ms sine or 6 ms rect. pulse		350			
Non-repetitive avalanche energy per leg		E _{AS}	$T_J = 25 ^{\circ}\text{C}, I_{AS} = 3 \text{A}, L = 4.40 \text{mH}$		20	mJ		
Repetitive avalanche current per leg		I _{AR}	Current decaying linearly to zero in 1 μ s Frequency limited by T _J maximum V _A = 1.5 x V _R typical		3	Α		

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40CTQ045S/40CTQ045-1

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ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
	V _{FM} ⁽¹⁾	20 A	T _J = 25 °C	0.53	- V
Maximum forward voltage drop per leg		40 A		0.68	
See fig. 1		20 A	T _J = 125 °C	0.48	
		40 A		0.67	
Maximum reverse leakage current per leg	1 (1)	T _J = 25 °C	V _R = Rated V _R	3	mA
See fig. 2	I _{RM} ⁽¹⁾	T _J = 125 °C		115	IIIA
Threshold voltage	V _{F(TO)}	$T_J = T_J$ maximum		0.27	V
Forward slope resistance	r _t			8.72	mΩ
Maximum junction capacitance per leg	C _T	V _R = 5 V _{DC} (test signal range 100 kHz to 1 MHz) 25 °C 2800		pF	
Typical series inductance per leg	L _S	Measured lead to lead 5 mm from package body 8.0		nΗ	
Maximum voltage rate of change	dV/dt	Rated V _R 10 000 V		V/µs	

Note

 $^{^{(1)}\,}$ Pulse width < 300 $\mu s,$ duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS					
PARAMETER		SYMBOL TEST CONDITIONS		VALUES	UNITS
Maximum junction and storage temperature range		T _J , T _{Stg}		- 55 to 150	°C
Maximum thermal resistance, junction to case per leg		R _{thJC} DC operation Mounting surface, smooth and greased (Only for TO-262)		2.0	
Maximum thermal resistance, junction to case per package				1.0	°C/W
Typical thermal resistance, case to heatsink				0.50	
Approximate weight				2	g
Approximate weight				0.07	OZ.
Mounting torque —	minimum			6 (5)	kgf ⋅ cm
	maximum			12 (10)	(lbf · in)
			Case style D ² PAK	40CTQ045S	
Marking device			Case style TO-262	40CTQ045-1	



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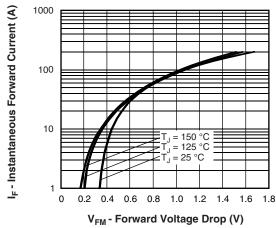


Fig. 1 - Maximum Forward Voltage Drop Characteristics (Per Leg)

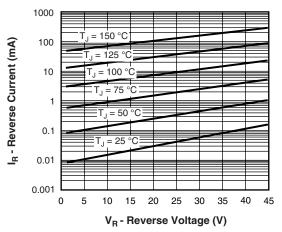


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage (Per Leg)

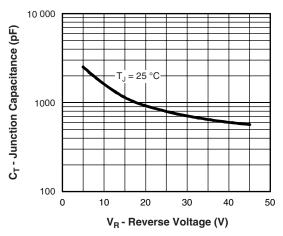


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

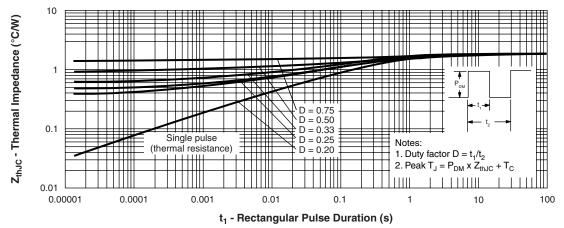


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics (Per Leg)

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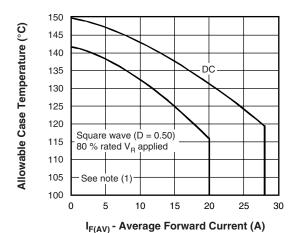


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current (Per Leg)

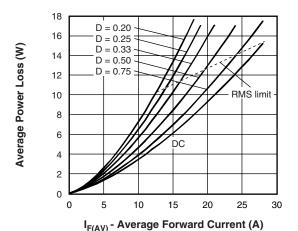


Fig. 6 - Forward Power Loss Characteristics (Per Leg)

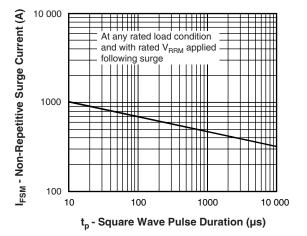


Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)

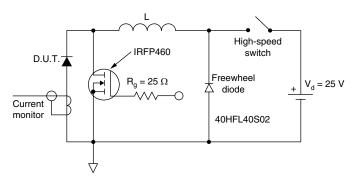


Fig. 8 - Unclamped Inductive Test Circuit

Note

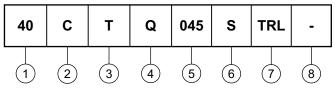
 $\begin{array}{l} \text{(1)} \ \ \text{Formula used: } T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}; \\ Pd = \text{Forward power loss} = I_{F(AV)} \times V_{FM} \text{ at } (I_{F(AV)}/D) \text{ (see fig. 6);} \\ Pd_{REV} = \text{Inverse power loss} = V_{R1} \times I_R \text{ (1 - D); } I_R \text{ at } V_{R1} = 10 \text{ V} \\ \end{array}$



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ORDERING INFORMATION TABLE

Device code



1 - Current rating (40 A)

2 - Circuit configuration:

C = Common cathode

3 - T = TO-220

- Schottky "Q" series

5 - Voltage rating (045 = 45 V)

6 - • S = D²PAK

• -1 = TO-262

7 - • None = Tube (50 pieces)

• TRL = Tape and reel (left oriented - for D²PAK only)

• TRR = Tape and reel (right oriented - for D²PAK only)

None = Standard production

• PbF = Lead (Pb)-free

LINKS TO RELATED DOCUMENTS					
Dimensions http://www.vishay.com/doc?95014					
Part marking information	http://www.vishay.com/doc?95008				
Packaging information	http://www.vishay.com/doc?95032				



Vishay

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