

HAT3015T

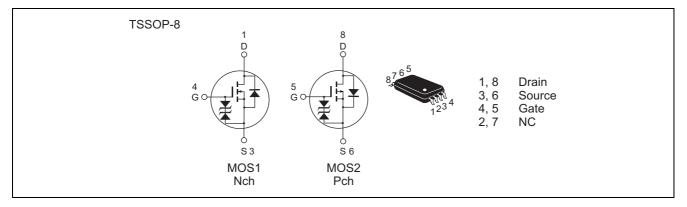
Silicon N/P Channel Power MOS FET High Speed Power Switching

> REJ03G0405-0200 Rev.2.00 Sep.07.2004

Features

- Low on-resistance
- Capable of 4 V gate drive
- High density mounting

Outline



Absolute Maximum Ratings

 $(Ta = 25^{\circ}C)$

		Rat	Ratings		
Item	Symbol	Nch	Pch	Unit	
Drain to Source voltage	V _{DSS}	200	-200	V	
Gate to Source voltage	V _{GSS}	±15	±15	V	
Drain current	I _D	0.5	-0.25	А	
Drain peak current	I _{D(pulse)} Note1	2	-1	А	
Body-Drain diode reverse drain current	I _{DR}	0.5	-0.25	А	
Channel dissipation	Pch Note2	1	1	W	
	Pch Note3	1.5	1.5	W	
Channel temperature	Tch	150	150	°C	
Storage temperature	Tstg	-55 to +150	-55 to +150	°C	

Notes: 1. PW \leq 10 μ s, duty cycle \leq 1 %

2. 1 Drive operation; When using the glass epoxy board (FR4 40 x 40 x 1.6 mm), PW \leq 10 s

3. 2 Drive operation; When using the glass epoxy board (FR4 40 x 40 x 1.6 mm), PW \leq 10 s



Electrical Characteristics

• N Channel

Item	Symbol	Min	Тур	Max	Unit	Test Conditions
Drain to Source breakdown voltage	V _{(BR)DSS}	200	—	—	V	$I_D = 10 \text{ mA}, V_{GS} = 0$
Gate to Source breakdown voltage	V _{(BR)GSS}	±15	—	—	V	$I_{G} = \pm 100 \ \mu A, \ V_{DS} = 0$
Gate to Source leak current	I _{GSS}	—	—	±10	μA	$V_{GS} = \pm 12 \text{ V}, V_{DS} = 0$
Zero Gate voltage Drain current	I _{DSS}	—	—	5	μA	$V_{DS} = 200 \text{ V}, V_{GS} = 0$
Gate to Source cutoff voltage	V _{GS(off)}	1.0	—	2.1	V	$V_{DS} = 10 \text{ V}, \text{ I}_{D} = 1 \text{ mA}$
Static Drain to Source on state	R _{DS(on)}	—	1.6	2.2	Ω	$I_D = 0.5 \text{ A}, V_{GS} = 10 \text{ V}^{Note4}$
resistance	R _{DS(on)}	—	1.9	2.7	Ω	$I_D = 0.5 \text{ A}, V_{GS} = 4 \text{ V}^{\text{Note4}}$
	R _{DS(on)}	—	2.4	5.5	Ω	$I_D = 2 \text{ A}, V_{GS} = 5 \text{ V}^{\text{Note4}}$
Forward transfer admittance	y _{fs}	0.56	0.86	—	S	$I_D = 0.5 \text{ A}, V_{DS} = 10 \text{ V}^{Note4}$
Input capacitance	Ciss	—	120		pF	$V_{DS} = 10 V, V_{GS} = 0$
Output capacitance	Coss	_	29	—	pF	f = 1 MHz
Reverse transfer capacitance	Crss	—	10	—	pF	
Turn-on delay time	t _{d(on)}	—	10		ns	$V_{GS} = 5 V, I_D = 0.5 A$
Rise time	tr	_	14	—	ns	V _{DD} ≅ 30 V
Turn-off delay time	t _{d(off)}	_	24		ns	
Fall time	t _f	_	9		ns	
Body–Drain diode forward voltage	V_{DF}	_	0.9	1.4	V	$IF = 0.5 A, V_{GS} = 0^{Note4}$

Notes: 4. Pulse test

• P Channel

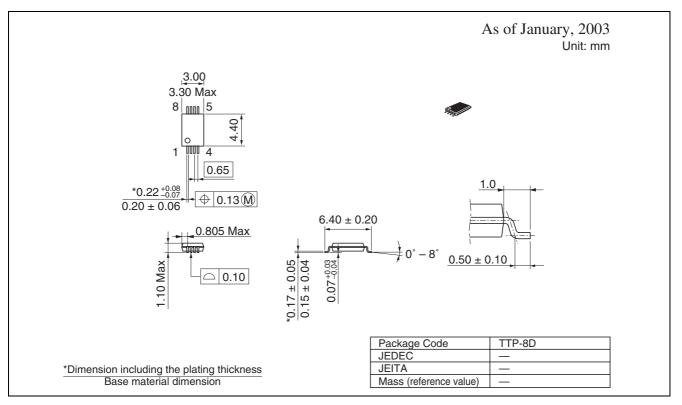
Item	Symbol	Min	Тур	Max	Unit	Test Conditions
Drain to Source breakdown voltage	V _{(BR)DSS}	-200	—	—	V	$I_D = -10 \text{ mA}, V_{GS} = 0$
Gate to Source breakdown voltage	$V_{(BR)GSS}$	±15	—	—	V	$I_G = \pm 100 \ \mu A, \ V_{DS} = 0$
Gate to Source leak current	I _{GSS}	—	—	±10	μA	$V_{GS} = \pm 12 \text{ V}, V_{DS} = 0$
Zero Gate voltage Drain current	I _{DSS}	—	—	-5	μA	$V_{DS} = -200 V, V_{GS} = 0$
Gate to Source cutoff voltage	V _{GS(off)}	-1.0	—	-2.0	V	$V_{DS} = -10 \text{ V}, \text{ I}_{D} = -1 \text{ mA}$
Static Drain to Source on state	R _{DS(on)}	—	5.0	6.2	Ω	$I_D = -0.25 \text{ A}, V_{GS} = -10 \text{ V}^{Note4}$
resistance	R _{DS(on)}	—	6.0	7.5	Ω	$I_D = -0.25 \text{ A}, V_{GS} = -4 \text{ V}^{\text{Note4}}$
	R _{DS(on)}	—	7.0	10.0	Ω	$I_D = -1 \text{ A}, V_{GS} = -5 \text{ V}^{Note4}$
Forward transfer admittance	y _{fs}	0.29	0.45	—	S	$I_D = -0.25 \text{ A}, V_{DS} = -10 \text{ V}^{Note4}$
Input capacitance	Ciss	—	140	—	pF	$V_{DS} = -10 \text{ V}, \text{ V}_{GS} = 0$
Output capacitance	Coss	—	37	—	pF	f = 1 MHz
Reverse transfer capacitance	Crss	—	10	—	pF	
Turn-on delay time	t _{d(on)}	—	12	—	ns	$V_{GS} = -5 \text{ V}, \text{ I}_{D} = -0.25 \text{ A}$
Rise time	t _r	—	9	—	ns	$V_{DD} \cong -30 \text{ V}$
Turn-off delay time	t _{d(off)}	—	25	—	ns	
Fall time	t _f	—	15	—	ns	
Body–Drain diode forward voltage	V_{DF}	—	-0.9	-1.4	V	$IF = -0.25 A, V_{GS} = 0^{Note4}$

Notes: 4. Pulse test

 $(Ta = 25^{\circ}C)$



Package Dimensions



Ordering Information

Part Name	Quantity	Shipping Container		
HAT3015T-EL-E	3000 pcs	Taping		
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Note: For some grades, production may be terminated. Please contact the Renesas sales office to check the state of production before ordering the product.



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