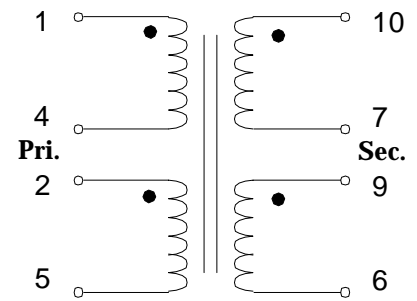


# ADSL Transformer

| Turns Ratio  |       | 1 : 1 ±2% |       |                  |
|--|-------|-----------|-------|------------------|
| Parameter  | Min.  | Typ.      | Max.  | Units            |
| Inductance <sup>1</sup><br>1-5 (Tie 2+4)   | 383.0 | 407.5     | 431.9 | μH               |
| Leakage Inductance <sup>2</sup><br>1-5(Tie 7+9, 10+6, 2+4)   |       |           | 10    | μH               |
| Interwinding Capacitance <sup>2</sup>  |       |           | 25    | pF               |
| DC Resistance 6-9 or 10-7  |       |           | 0.363 | Ω                |
| DC Resistance 5-2 or 4-1   |       |           | 0.429 | Ω                |
| Frequency Response 20k - 2MHz  |       |           | 3.0   | dB               |
| Longitudinal Balance 20k - 1.1MHz  | 55    |           |       | dB               |
| Hi-Pot Primary to Secondary  | 1875  |           |       | V <sub>RMS</sub> |
| Inserion Loss 772 kHz  |       |           | 0.5   | dB               |
| Total Harmonic Distortion -85dB Max. 100kHz, 5.3 Vrms across primary, 100 ohm load, 100 ohm input,(Tie 2+4, 7+9) |       |           |       |                  |

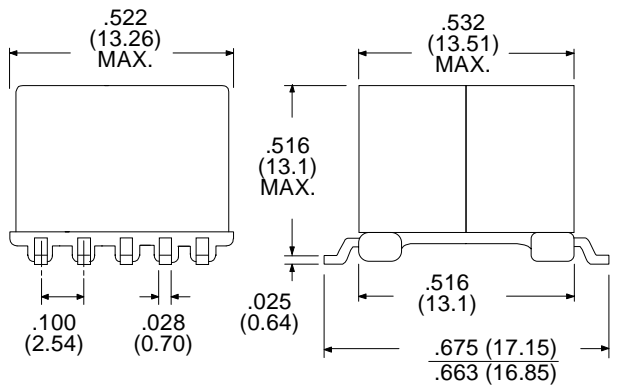
- 1 10kHz and 100mV  
2 100kHz and 100mV

## Schematic Diagram

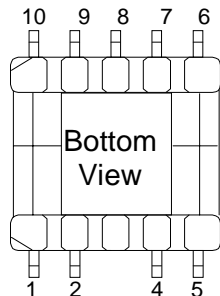


Designed to meet supplementary insulation requirements of IEC950 for a working voltage of 250V

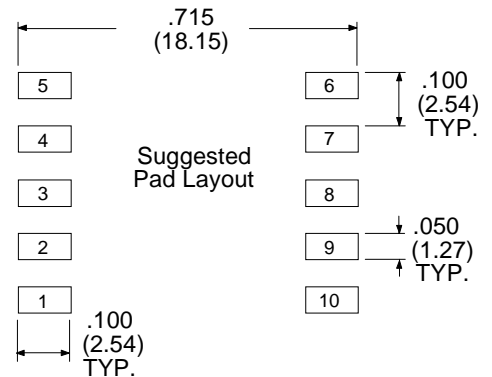
Materials used in construction of this component meet or exceed UL Class B and can be operated up to 130°C



Dimensions in Inches (mm)



Unused pin #3 Removed



RHOMBUS P/N: T-1462G

CUST P/N:

NAME:

DATE: 10/24/01

SHEET: