3 V InGaP DCS Power Amplifier

AP132-317

Features

- Single Supply, 3.2 V Nominal Operating Voltage
- DCS1800 and PCS1900 Operation
- Output Power Greater Than 33 dBm
- High Power Added Efficiency of 50%
- Ultra Small, Thermally Enhanced Micro Leadframe Package
- Low Current Standby Mode: < 10 µA
- Integral Analog Power Control With 70 dB of Dynamic Range
- GPRS Class 12 Capable
- Designed to Work With AP131-317 as a Dual-/Tri-Band Solution

Description

The AP132-317 is a high performance IC power amplifier designed for use as the final amplification stage in GSM or GPRS mobile phones, and other digital wireless applications in the 1700–2000 MHz band. It features 3-cell battery operation, integrated analog power control with over 70 dB of dynamic range, and exceptional power added efficiency over the full battery voltage range. The amplifier is manufactured on an advanced InGaP HBT process, known industry-wide for its excellent reliability and performance. The AP132-317 is designed to be stable over a wide temperature range of -40 to +85°C and over a 10:1 output VSWR load. Output matching is provided externally to maximize performance, reduce costs, and allow optimal matching for output power and efficiency over a broader frequency range. A dual- and/or tri-band solution can be obtained by combining the AP132-317 with Alpha's AP131-317. The AP132-317 is packaged in a thermally enhanced, ultra small micro leadframe package.

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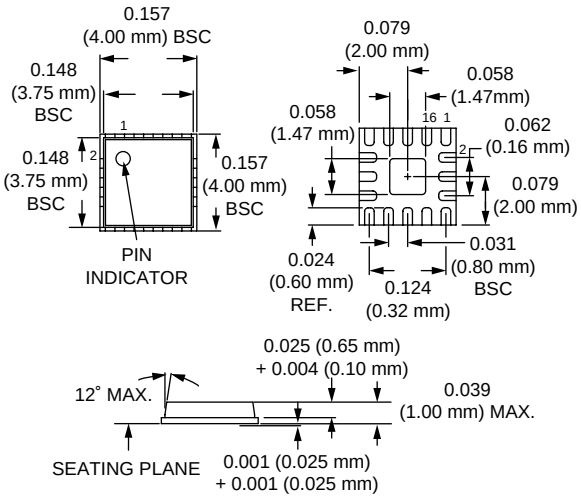


Absolute Maximum Ratings

Characteristic	Value
Supply Voltage V_{CC}, Standby Mode, V_{APC} < 0.3 (No RF Input Power)	6 V Max.
Power Control Voltage	4 V Max.
Input Power (CW)	15 dBm Max.
Operating Case Temperature	-40 to +85°C
Storage Temperature	-45 to +120°C

DC Specifications

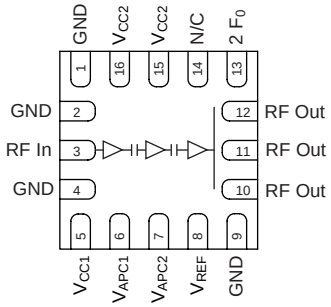
Parameter	Condition	Min.	Typ.	Max.	Unit
Supply Voltage		2.8	3.2	4.2	V
Leakage Current	No Input RF Power			10	µA
Power Control Voltage		0.1		2.6	V
Power Control Current	$V_{APC1,2}$ = 2.6 V, V_{CC} = 3.2 V, CW			5	mA

Electrical Specifications at 25°C

Parameter	Condition	Min.	Typ.	Max.	Unit
Frequency	DCS PCS	1710 1850		1785 1910	MHz MHz
Output Power	$V_{APC1,2}$ = 2.6 V, V_{CC} = 3.2 V, CW $V_{APC1,2}$ = 2.8 V, V_{CC} = 3.5 V, CW $V_{APC1,2}$ = 2.8 V, V_{CC} = 2.7 V, T = -20 to +85°C, CW	32.0 32.5 30.5	33.0 33.5 32.0		dBm dBm dBm
Dynamic Range	V_{APC} = 0.1 to 2.8 V	60			dB
Power Control Slope	V_{APC} = 0.1 to 2.8 V		75	180	dB/V_{APC}
Power Added Efficiency	P_{OUT} = P_{OUT} Max.	42	50		%
Input Power		3	6	10	dBm
Input VSWR	P_{OUT} = 0–32.5 dBm			2:1	
Forward Isolation	P_{IN} = 6 dBm, V_{APC} = 0.1 V P_{IN} = 9 dBm, V_{APC} = 0.1 V			-40 -35	dBm dBm
Second Harmonic	At P_{OUT} Max., V_{CC} = 3.2 V		-50	-45	dBc
Third Harmonic	At P_{OUT} Max., V_{CC} = 3.2 V		-60	-55	dBc
All Others Non-harmonic Spurious				-40	dBm
Noise in the R_X Band	1805–1880 MHz, 100 KHz BW			-76	dBm
Ruggedness	Output VSWR = 10:1 All Phase Angles, V_{CC} = 4.2 V, P_{IN} = 10 dBm, V_{APC} = 2.6 V	No Module Damage or Permanent Performance Degradation			
Stability	Output VSWR = 10:1 All Phase Angles, V_{CC} = 4.2 V, P_{IN} = 10 dBm, V_{APC} = 2.6 V			-36	dBm

Unless otherwise stated: pulsed operation @ 12.5% duty cycle, 50 Ω system, V_{CC} = 3.2 and T_A = 25°C.

Pin Out



Pin Configuration

Pin	Symbol	Function
1	GND	Ground connection.
2	GND	Ground connection.
3	RF In	RF input to power amplifier. A 33 pF DC blocking capacitor is required.
4	GND	Ground connection.
5	V_{CC}	Power supply input voltage. 1 µF and 33 pF RF bypassing capacitors are required.
6	V_{APC1}	Power control input voltage for the first two stages of the amplifier. 10 nF, 100 pF, and 10,000 pF RF bypassing capacitors are required. Can be connected to Pin 7 for single power control operation.
7	V_{APC2}	Power control input voltage for the third stage of the amplifier. 10 nF, 100 pF and 10,000 pF RF bypassing capacitors are required. Can be connected to Pin 6 for single power control operation.
8	V_{CC}	Power supply input voltage. 10 nF, 1 µF and 100 pF RF bypassing capacitors are required.
9	GND	Ground connection.
10	RF Out/V_{CC3}	1. RF output: Two shunt matching capacitors, 4.5 pF high Q and 1.5 pF, and series 33 pF DC blocking capacitors are required. 2. V_{CC3}: 100 pF, 10 nF, and 1 µF RF bypassing capacitors are required.
11	RF Out/V_{CC3}	RF output and power supply input voltage. See description for Pin 10.
12	RF Out/V_{CC3}	RF output and power supply input voltage. See description for Pin 10.
13	2	Second harmonic termination. This pin can be used to alter the second harmonic output characteristics, but for nominal GSM operation, no matching elements are required.
14	NC	No connect.
15	V_{CC}	Power supply input voltage. 1 µF, 100 pF, 10 nF, 5.6 pF and 10 pF interstage tuning and RF bypassing capacitors are required.
16	V_{CC}	Power supply input voltage connected to Pin 15.