

MOS Fied Effect Transistor

2SJ206

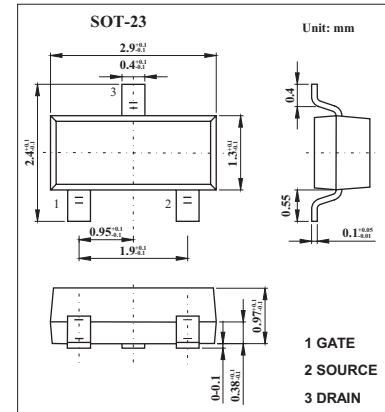
■ Features

- Directly driven by Ics having a 5V poer supply.

- Has low on-state resistance

$R_{DS(on)}=4\ \Omega$ MAX.@ $V_{GS}=-4.0V, I_D=-0.3A$

$R_{DS(on)}=3\ \Omega$ MAX.@ $V_{GS}=-10V, I_D=-0.3A$



■ Absolute Maximum Ratings $T_a = 25^\circ\text{C}$

Parameter	Symbol	Rating	Unit
Drain to source voltage $V_{GS}=0$	V_{DSS}	-30	V
Gate to source voltage $V_{DS}=0$	V_{GSS}	± 20	V
Drain current (DC)	I_D	± 500	mA
Drain current(pulse) *	I_D	± 1.0	A
Power dissipation	P_D	2.0	W
Channel temperature	T_{ch}	150	$^\circ\text{C}$
Storage temperature	T_{stg}	-55 to +150	$^\circ\text{C}$

* $PW \leq 10\ \text{ms}$; $d \leq 50\%$.

■ Electrical Characteristics $T_a = 25^\circ\text{C}$

Parameter	Symbol	Testconditons	Min	Typ	Max	Unit
Drain cut-off current	I_{BSS}	$V_{DS}=-30V, V_{GS}=0$			-10	μA
Gate leakage current	I_{GSS}	$V_{GS}=\pm 16V, V_{DS}=0$			± 5	μA
Gate cut-off voltage	$V_{GS(off)}$	$V_{DS}=-5.0V, I_D=-1\text{mA}$	-1.0	-2.3	-3.0	V
Forward transfer admittance	$ Y_{fs} $	$V_{DS}=-5.0V, I_D=-0.3A$	0.4			s
Drain to source on-state resistance	$R_{DS(on)}$	$V_{GS}=-4.0V, I_D=-0.3A$		2.0	4.0	Ω
		$V_{GS}=-4V, I_D=-0.3A$		0.8	3.0	Ω
Input capacitance	C_{iss}	$V_{DS}=-5V, V_{GS}=0, f=1\text{MHZ}$		100		pF
Output capacitance	C_{oss}			80		pF
Reverse transfer capacitance	C_{rss}			15		pF
Turn-on delay time	$t_{d(on)}$				120	
Rise time	t_r	$V_{GS(on)}=-4V, R_G=10\ \Omega, V_{DD}=-5V, I_D=-0.3A, R_L=17\ \Omega$		420		ns
Turn-off delay time	$t_{d(off)}$			75		ns
Fall time	t_f				140	

■ Marking

Marking	PH
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