

HD74LVC1G02

2-input NOR Gate

REJ03D0008-0300Z Rev.3.00 Jun. 30, 2004

Description

The HD74LVC1G02 has two-input NOR gate in a 5-pin package. Low voltage and high-speed operation is suitable for the battery powered products (e.g., notebook computers), and the low power consumption extends the battery life.

Features

• The basic gate function is lined up as renesas uni logic series.

• Supply voltage range: 1.65 to 5.5 V

Operating temperature range: -40 to +85°C

• All inputs: V_{IH} (Max.) = 5.5 V (@ V_{CC} = 0 V to 5.5 V)

All outputs: V_0 (Max.) = 5.5 V (@ V_{CC} = 0 V)

• Output current: $\pm 4 \text{ mA } (@V_{CC} = 1.65 \text{ V})$

 $\pm 8 \text{ mA } (@V_{CC} = 2.3 \text{ V})$

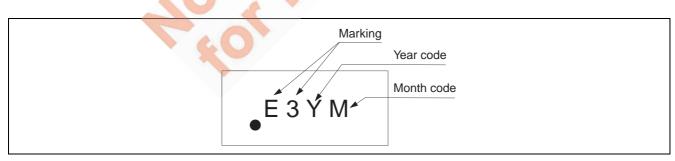
 $\pm 24 \text{ mA } (@V_{CC} = 3.0 \text{ V})$

 $\pm 32 \text{ mA} (@V_{CC} = 4.5 \text{ V})$

• Ordering Information

Part Name	Package Type	Package Code	Package Abbreviation	Taping Abbreviation (Quantity)
HD74LVC1G02CPE	WCSP-5 pin	TBS-5V	CP	E (3,000 pcs/reel)
HD74LVC1G02CLE		TBS-5AV	CL	

Article Indication



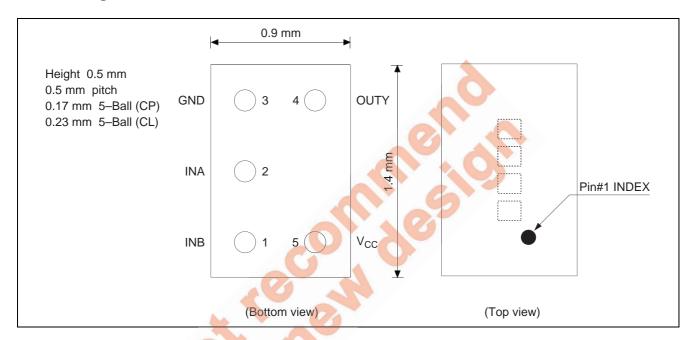
Function Table

Inp		
Α	Output Y	
L	L	Н
L	Н	L
Н	L	L
Н	Н	L

H: High level

L: Low level

Pin Arrangement



Logic Diagram



Absolute Maximum Ratings

Item	Symbol	Ratings	Unit	Test Conditions
Supply voltage range	V _{CC}	-0.5 to 6.5	V	
Input voltage range *1	Vı	-0.5 to 6.5	V	
Output voltage range *1, 2	Vo	-0.5 to V _{CC} +0.5	V	Output : H or L
		-0.5 to 6.5		V _{CC} : OFF
Input clamp current	I _{IK}	– 50	mA	V _I < 0
Output clamp current	I _{OK}	– 50	mA	V _O < 0
Continuous output current	Io	±50	mA	$V_O = 0$ to V_{CC}
Continuous current through	I _{CC} or I _{GND}	±100	mA	
V _{CC} or GND				
Package Thermal impedance	θ_{ja}	154	°C/W	СР
		132		CL
Storage temperature	Tstg	-65 to 150	°C	

Notes: The absolute maximum ratings are values, which must not individually be exceeded, and furthermore no two of which may be realized at the same time.

- 1. The input and output voltage ratings may be exceeded if the input and output clamp-current ratings are observed.
- 2. This value is limited to 5.5 V maximum.

Recommended Operating Conditions

Item	Symbol	Min	Max	Unit	Conditions
Supply voltage range	V _{CC}	1.65	5.5	V	
Input voltage range	Vı	0	5.5	V	
Output voltage range	Vo	0	Vcc	V	
Output current	I _{OL}	+ 1	4	mA	V _{CC} = 1.65 V
		-	8		V _{CC} = 2.3 V
			16		V _{CC} = 3.0 V
			24		
	A		32		V _{CC} = 4.5 V
	Іон	_	-4		V _{CC} = 1.65 V
		_	-8		V _{CC} = 2.3 V
*		_	–16		V _{CC} = 3.0 V
	· ·	_	-24		
		_	-32		V _{CC} = 4.5 V
Input transition rise or fall rate	Δt / Δν	0	20	ns / V	V _{CC} = 1.65 to 1.95 V,
					2.3 to 2.7 V
		0	10		V _{CC} = 3.0 to 3.6 V
		0	5		V _{CC} = 4.5 to 5.5 V
Operating free-air temperature	Ta	–40	85	°C	

Note: Unused or floating inputs must be held high or low.

Electrical Characteristics

Ta = -40 to $85^{\circ}C$

Item	Symbol	V _{CC} (V)	Min	Тур	Max	Unit	Test condition
Input voltage	V_{IH}	1.65 to 1.95	V _{CC} ×0.65	_	_	V	
		2.3 to 2.7	1.7	_	_		
		3.0 to 3.6	2.0	_	_		
		4.5 to 5.5	V _{CC} ×0.7	_	_		
	V _{IL}	1.65 to 1.95	_	_	V _{CC} ×0.35		
		2.3 to 2.7	_	_	0.7		
		3.0 to 3.6	_	_	0.8		
		4.5 to 5.5	_	_	V _{CC} ×0.3		
Output voltage	V _{OH}	Min to Max	V _{CC} -0.1	_	_	V	$I_{OH} = -100 \mu A$
		1.65	1.2	_	_		$I_{OH} = -4 \text{ mA}$
		2.3	1.9	_	_		$I_{OH} = -8 \text{ mA}$
		3.0	2.4	_	_		I _{OH} = -16 mA
			2.3	_	_	0	I _{OH} = -24 mA
		4.5	3.8	_	-		I _{OH} = -32 mA
	V_{OL}	Min to Max	_	_	0.1		I _{OL} = 100 μA
		1.65	_	_	0.45		I _{OL} = 4 mA
		2.3	_	-	0.3		I _{OL} = 8 mA
		3.0	_		0.4	1	I _{OL} = 16 mA
			- 4	-	0.55		I _{OL} = 24 mA
		4.5	- /		0.55		I _{OL} = 32 mA
Input current	I _{IN}	0 to 5.5	- 0	- /	±5	μΑ	V _{IN} = 5.5 V or GND
Quiescent	Icc	5.5	-	_ \	10	μΑ	$V_{IN} = V_{CC}$ or GND,
supply current							$I_{O} = 0$
	ΔI_{CC}	3 to 5.5		4	500		One input at V _{CC} -0.6 V,
							Other input at V _{CC} or GND
Output leakage	I _{OFF}	0	-	_	±10	μΑ	V_{IN} or $V_O = 0$ to 5.5 V
current							
Input capacitance	CIN	3.3	_	4.0	_	pF	$V_{IN} = V_{CC}$ or GND

Note: For conditions shown as Min or Max, use the appropriate values under recommended operating conditions.

Switching Characteristics

 $V_{CC}=1.8\pm0.15~V$

		Ta = -40	Ta = -40 to 85°C			FROM	то
Item	Symbol	Min	Max	Unit	Test Conditions	(Input)	(Output)
Propagation delay time	t _{PLH}	1.9	7.2	ns	$C_L = 15 \text{ pF}, R_L = 1 \text{ M}\Omega$	A or B	Υ
	t _{PHL}	2.8	8.0		$C_L = 30 \text{ pF}, R_L = 1.0 \text{ k}\Omega$		

 $V_{CC}=2.5\pm0.2\ V$

		Ta = -40	Ta = -40 to 85°C			FROM	то
Item	Symbol	Min	Max	Unit	Test Conditions	(Input)	(Output)
Propagation delay time	t _{PLH}	0.8	4.4	ns	$C_L = 15 \text{ pF}, R_L = 1 \text{ M}\Omega$	A or B	Υ
	t _{PHL}	1.2	5.5		$C_L = 30 \text{ pF}, R_L = 500 \Omega$		

 $V_{CC}=3.3\pm0.3~V$

		Ta = -40	Ta = -40 to 85°C			FROM	то
Item	Symbol	Min	Max	Unit	Test Conditions	(Input)	(Output)
Propagation delay time	t _{PLH}	0.8	3.6	ns	$C_L = 15 \text{ pF}, R_L = 1 \text{ M}\Omega$	A or B	Υ
	t _{PHL}	1.0	4.5		$C_L = 50 \text{ pF}, R_L = 500 \Omega$		

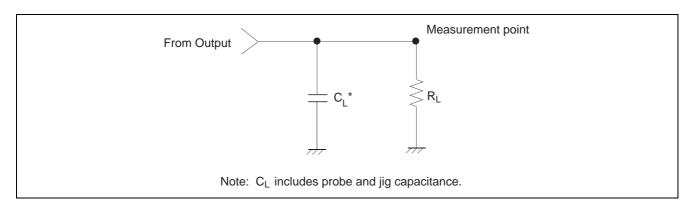
 $V_{CC}=5.0\pm0.5~V$

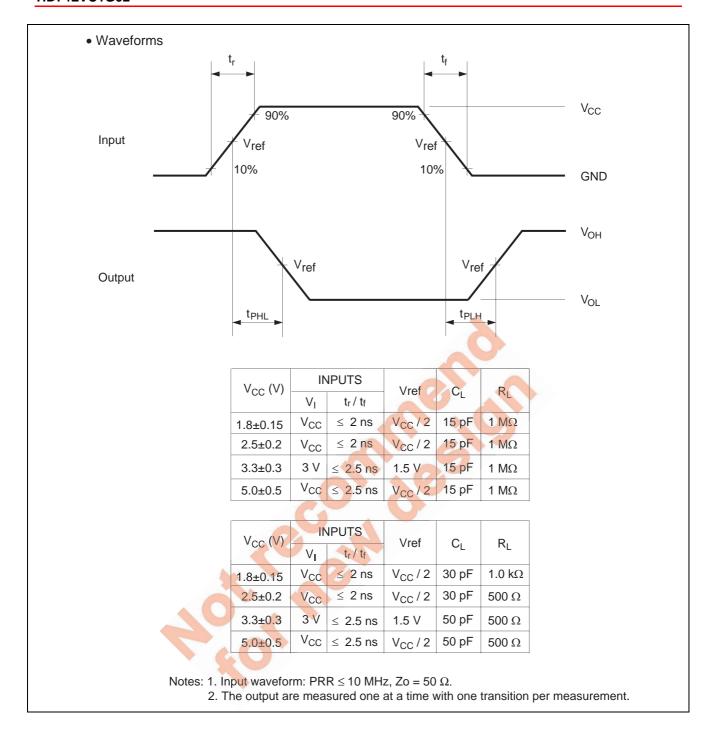
		Ta = -40 to 85°C				FROM	
Item	Symbol	Min	Max	Unit	Test Conditions	(Input)	(Output)
Propagation delay time	t _{PLH}	0.8	3.4	ns	$C_L = 15 \text{ pF}, R_L = 1 \text{ M}\Omega$	A or B	Υ
	t _{PHL}	1.0	4.0		$C_L = 50 \text{ pF}, R_L = 500 \Omega$		

Operating Characteristics

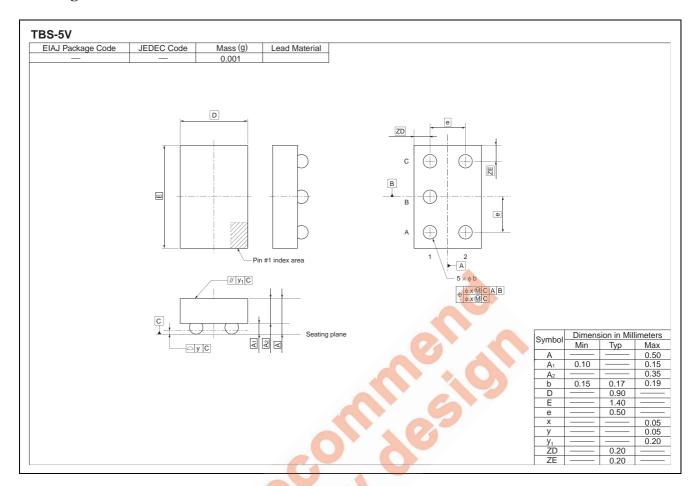
	6		6)	Ta = 25°C	;		
Item	Symbol	Vcc (V)	Min	Тур	Max	Unit	Test Conditions
Power dissipation capacitance	C _{PD}	1.8		23	_	pF	f = 10 MHz
		2.5	_	23	_		
	4.0	3.3	_	23	_		
	X.	5.0	_	25	_		

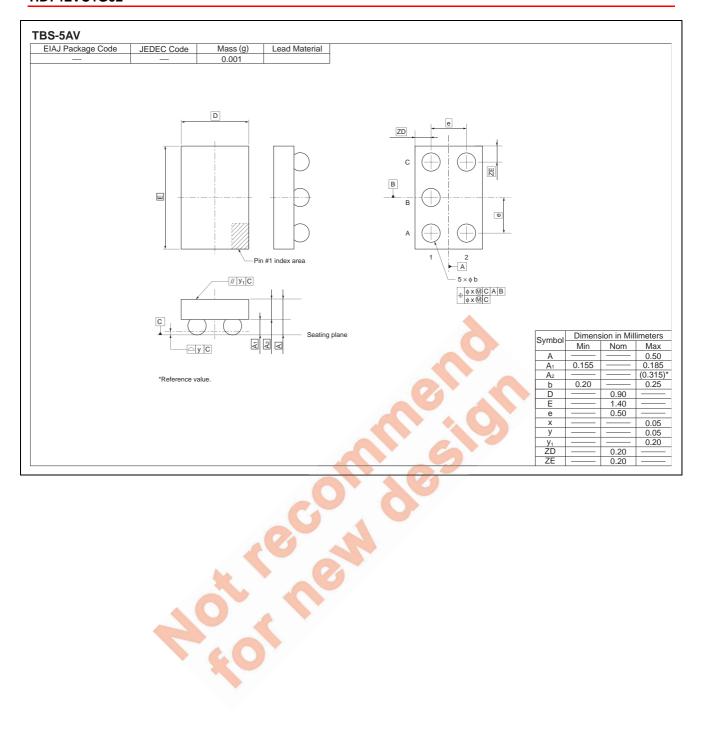
Test Circuit





Package Dimensions





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