



#### **Features**

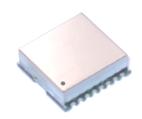
4 dBm Output Level at 830 MHz
Channel Step Size: 200 kHz
2<sup>nd</sup> Harmonic: < -25 dBc</li>
Spurious Level: < -70 dBc</li>

· Lock Time : < 10 ms

· 40 mA Current Consumption

### **Description**

The plerow<sup>TM</sup> PLL synthesizer module was designed for use in wireless and wireline systems in a wide range of frequency from 50 MHz to 6 GHz. ASB's PLL provides exceptionally low spurious and phase noise performance with fast locking time and low current consumption. All products are available in a surface-mount type package.



# **Specifications**

Parameter	Unit	Min.	Typical	Max.	
Frequency Range	MHz		830		
Output Power	dBm	3	4	5	
Supply Voltage	V	4.75	5	5.25	
Current Consumption	mA			40	
Channel Step Size	kHz		200		
2 <sup>nd</sup> Harmonics	dBc			-25	
Spurious Level	dBc			-70	
Lock Time	ms		3	10	
Reference Frequency	MHz		13		
Reference Input Level	dBm				
Phase Noise (C / N)					
@ 1 kHz					
@ 10 kHz	dBc/Hz		-108	-104	
@ 100 kHz					
Output Impedance	Ω		50		
Operating Temp. Range	°C	-40	25	80	
Package Type & Size	mm	SMT, 15.0W×15.0L×3.5H			

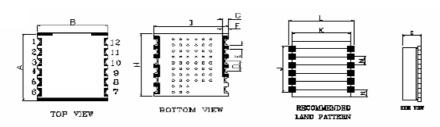
## **More Information**

Website: www.asb.co.kr E-mail: sales@asb.co.kr

Tel: (82) 42-528-7223 Fax: (82) 42-528-7222

ASB Inc., 4th FI. Venture Town Bldg., 367-17 Goijeong-Dong, Seo-Gu, Daejon 302-716, Korea

### **Outline Drawing**



Dimension														
Symbol	A	В	C	D	Е	F	G	Н	I	J	K	L	N	M
Man	14.60	14.60	3.50 3	202 0.5	0.51	51 0.61	1.50	15.24	15.24	11.68	1.68 14.02	17.07	2.54	2.03
Mm	±0.1	±0.1	3.50	2.03	0.51	0.61	1.50	±0.2	±0.2					

Pin Out for PLL						
Pin No.	Application	Pin No.	Application			
1	GND	7	VCC (VCO)			
2	GND	10	RF OUT			
3	GND	11	VCC (PLL)			
4	GND	12	LOCK DETECT			
4		12 s are Grounded	LOCK			

<sup>1)</sup> Measurement conditions are as follows: T =  $25^{\circ}$ C,  $V_{CC}$  = 5 V, Freq. = 830 MHz, 50 ohm system.

<sup>2)</sup> Frequency stability of internal ref. TCXO is  $\pm$  2.5 ppm (max.) over the operating temperature range.