

AMI'N NUT

# EIM5864-4

#### ISSUED 10/23/2008

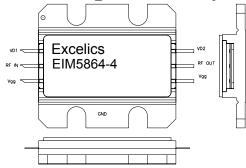
## 5.8 – 6.4 GHz Multi-Stage Power Amplifier

#### **FEATURES**

- 5.8– 6.4GHz Operating Frequency Range
- 35.5dBm Output Power at 1dB Compression
- 29.0 dB Typical Power Gain @1dB gain compression
- -45dBc Typical OIM3@ each tone Pout 22.5dBm
- Non-Hermetic Metal Flange Package

### APPLICATIONS

- Point-to-point and point-to-multipoint radio
- Military Radar Systems



Caution! ESD sensitive device.

## ELECTRICAL CHARACTERISTICS (Tb = 25 °C, 50 ohm, VD1=7V, VD2=10V, Vgg=-5V)

SYMBOL	PARAMETER/TEST CONDITIONS	MIN	TYP	MAX	UNITS
F	Operating Frequency Range	5.8		6.4	GHz
P1dB	Output Power at 1dB Gain Compression 34.5		35.5		dBm
G1dB	Gain @1dB gain compression	25	29		dB
OIMD3	Output $3^{rd}$ Order Intermodulation Distortion @ $\Delta f$ =10MHz, Each Tone Pout 22.5dBm		-45	-42	dBc
Input RL	Input Return Loss		-12	-8	dB
Output RL	Output Return Loss		-15	-10	dB
VD1	Drain Supply Voltage 1		7		V
VD2	Drain Supply Voltage 2		10		V
I <sub>DQ1</sub>	Quiescent Drain Current 1		800		mA
I <sub>DQ2</sub>	Quiescent Drain Current 2		1100	1300	mA
Vgg	Gate Supply Voltage		-5		V
Rth	Thermal Resistance		4.2		°C/W
Tb	Operating Base Plate Temperature	- 30		+ 80	°C

Note: Turn on/off sequence is required: ---to turn on: apply -5V on both Vgg first, then +7V and +10V. ---to turn off: turn +7V and +10V off first, then turn -5V off



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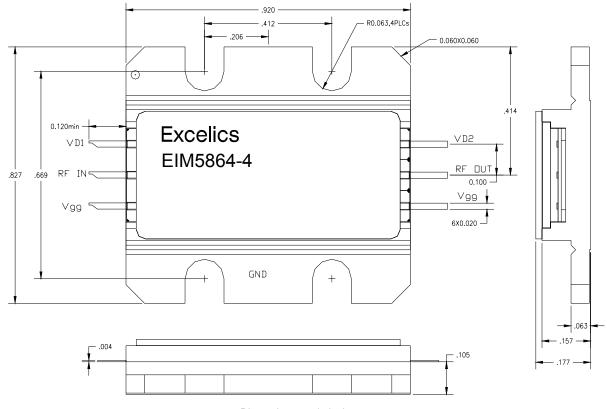
### MAXIMUM RATINGS @25°C<sup>1,2</sup>

SYMBOL	CHARACTERISTIC	ABSOLUTE	CONTINUOUS 1,2	
V <sub>D1</sub>	Drain Supply Voltage 1	12V	8V	
V <sub>D2</sub>	Drain Supply Voltage 2	14V	10V	
$V_{gg}$	Gate Supply Voltage	-10V	-6 V	
l <sub>gq</sub>	Gate Current	150mA	50 mA	
P <sub>IN</sub>	Input Power	17dBm	@ 3dB compression	
Т <sub>сн</sub>	Channel Temperature	175°C	150°C	
T <sub>STG</sub>	Storage Temperature	-65/175°C	-65/150°C	
Ρτ	Total Power Dissipation	29.8W	25W	

Notes: 1. Operating the device beyond any of the above rating may reduce MTTF and cause permanent damage.

2. Bias conditions must also satisfy the following equation Vdd\*Idd <  $(T_{CH} - Tb)/R_{TH}$ 

## Package Dimension and Pin Assignment



Dimensions are in inches



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