

HRL0103C

Silicon Schottky Barrier Diode for Rectifying

REJ03G0367-0300 Rev.3.00 Apr 08, 2008

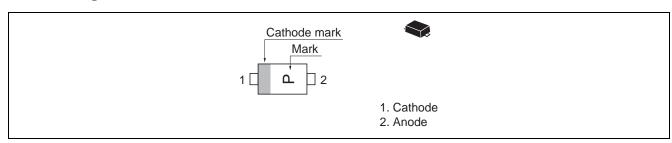
Features

- Low reverse voltage drop and suitable for high efficiency reverse current.
- Lineup of environmental friendly Halogen free type (HRL0103C-N)
- Extremely small Flat Lead Package (EFP) is suitable for surface mount design.

Ordering Information

Part No.	Laser Mark	Package Name	Package Code
HRL0103C	Р	EFP	PXSF0002ZA-A
HRL0103C-N			
(Halogen-free type)			

Pin Arrangement



Absolute Maximum Ratings

 $(Ta = 25^{\circ}C)$

Item	Symbol	Value	Unit
Peak reverse voltage	V _{RM} * ¹	30	V
Reverse voltage	V_R	30	V
Average rectified current	I ₀ *1	100	mA
Peak forward surge current	I _{FM}	300	mA
Non-Repetitive peak forward surge current	I _{FSM} * ²	1	А
Junction temperature	Tj	125	°C
Storage temperature	Tstg	−55 to +125	°C

Notes: 1. See from Fig.3 to Fig.5.

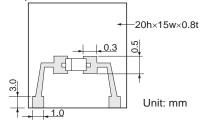
2. 10 ms sine wave 1 pulse.

Electrical Characteristics

 $(Ta = 25^{\circ}C)$

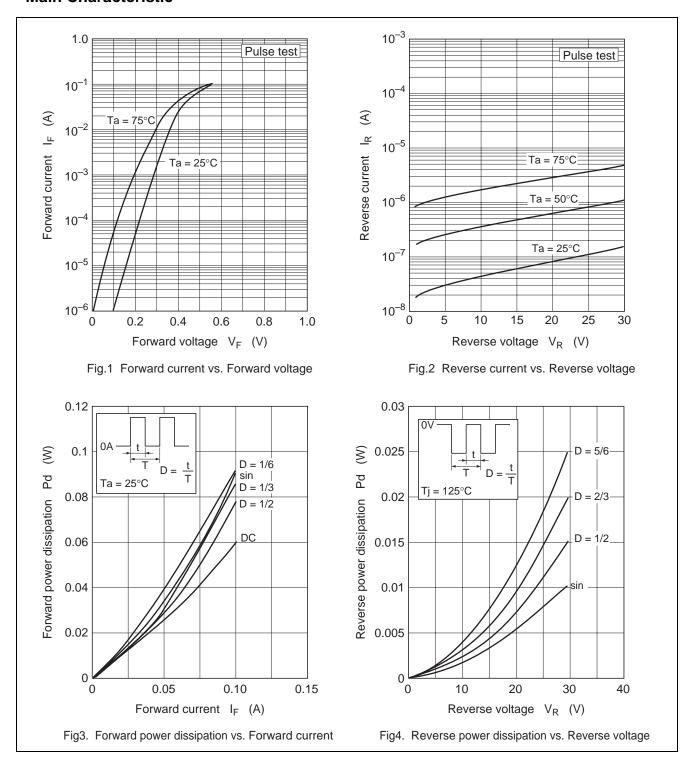
Item	Symbol	Min	Тур	Max	Unit	Test Condition
Forward voltage	V _{F1}	_	_	0.40	V	I _F = 10 mA
	V _{F2}	_	_	0.60	V	I _F = 100 mA
Reverse current	I _{R1}	_	_	0.1	μА	V _R = 5 V
	I _{R2}	_	_	0.2		V _R = 10 V
Capacitance	С	_	_	8.0	pF	V _R = 0.5 V, f = 1 MHz
Thermal resistance	Rth(j-a)	_	800	_	°C/W	Polyimide board *1

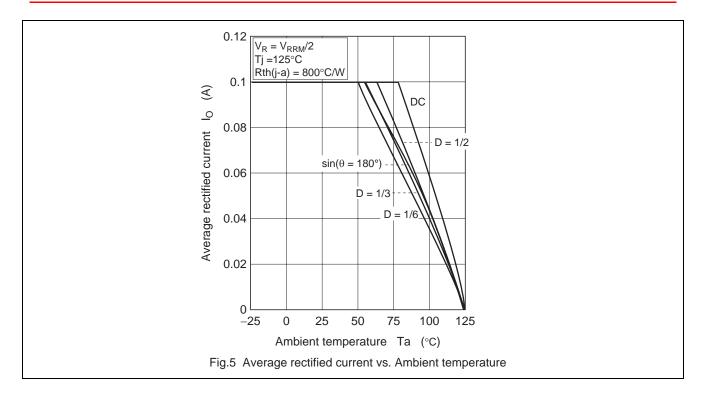
Notes: 1. Polyimide board



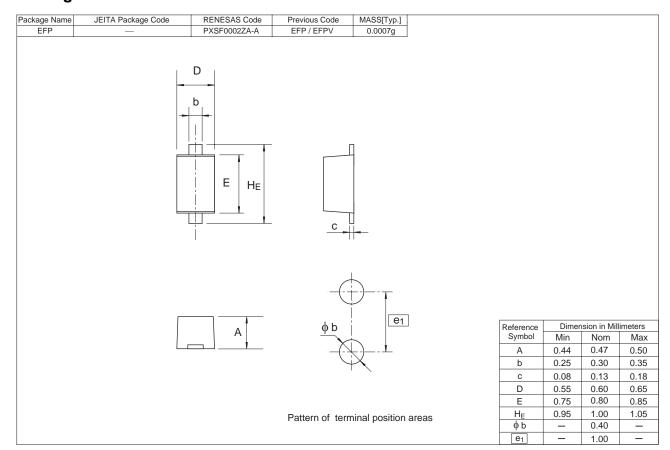
2. For EFP package, the material of lead is exposed for cutting plane. There for, soldering nature of lead tip part is considered as unquestioned. Please kindly consider soldering nature.

Main Characteristic





Package Dimensions



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