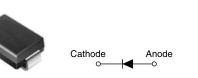
MBRS360TRPbF

Vishay High Power Products

Schottky Rectifier, 3.0 A



SMC

PRODUCT SUMMARY			
I _{F(AV)}	3.0 A		
V _R	60 V		
I _{RM}	30 mA at 125 °C		

FEATURES

- · Small foot print, surface mountable
- · Very low forward voltage drop
- High frequency operation
- · Guard ring for enhanced ruggedness and long term reliability
- Lead (Pb)-free ("PbF" suffix)
- Designed and qualified for industrial level

DESCRIPTION

The MBRS360TRPbF surface mount Schottky rectifier has been designed for applications requiring low forward drop and small foot prints on PC boards. Typical applications are in disk drives, switching power supplies, converters, freewheeling diodes, battery charging, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS					
SYMBOL	CHARACTERISTICS	CHARACTERISTICS VALUES			
I _{F(AV)}	Rectangular waveform	3.0	A		
V _{RRM}		60	V		
I _{FSM}	t _p = 5 μs sine	790	A		
V _F	3.0 Apk, T _J = 125 °C	0.61	V		
TJ	Range	- 55 to 150	°C		

VOLTAGE RATINGS				
PARAMETER	SYMBOL	MBRS360TRPbF	UNITS	
Maximum DC reverse voltage	V _R 60		V	
Maximum working peak reverse voltage	V _{RWM}	80	v	

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum annual formant		50 % duty cycle at T_L = 118 °C, rectangular waveform		3.0	
Maximum average forward current	I _{F(AV)}	50 % duty cycle at T_L = 105 °C, rectangular waveform		4.0	
Maximum peak one cycle non-repetitive surge current		5 μs sine or 3 μs rect. pulse	Following any rated load condition and with rated	790	A
	10 ms sine or 6 ms rect. pulse	V _{RRM} applied	80		
Non-repetitive avalanche energy	E _{AS}	T _J = 25 °C, I _{AS} = 1.0 A, L = 10 mH		5.0	mJ
Repetitive avalanche current	I _{AR}	Current decaying linearly to zero in 1 μ s Frequency limited by T _J maximum V _A = 1.5 x V _B typical		1.0	А

For technical questions, contact: diodes-tech@vishay.com





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ELECTRICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS		TYP.	MAX.	UNITS
Maximum forward voltage drop		3 A	T 05 %C	0.57	0.74	
	V _{FM} ⁽¹⁾	6 A	- Τ _J = 25 °C	0.72	0.9	V
	V FM (*)	3 A	T _J = 125 °C	0.51	0.61	
		6 A		0.62	0.77	
Maximum reverse leakage current		T _J = 25 °C	V _R = Rated V _R	-	0.5	mA
	I _{RM} ⁽¹⁾	$T_J = 100 \ ^{\circ}C$		-	20	
		T _J = 125 °C		-	30	
Maximum junction capacitance	CT	$V_{\rm R}$ = 5 $V_{\rm DC}$ (test signal range 100 kHz to 1 MHz) 25 °C		-	180	pF
Typical series inductance	L _S	Measured lead to lead 5 mm from package body		-	3.0	nH
Maximum voltage rate of change	dV/dt	Rated V _R		-	10 000	V/µs

Note

 $^{(1)}\,$ Pulse width < 300 $\mu s,$ duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS				
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction and storage temperature range	$T_{J}^{(1)}, T_{Stg}$		- 55 to 150	°C
Maximum thermal resistance, junction to lead	R _{thJL} ⁽²⁾		12	°C/W
Maximum thermal resistance, junction to ambient	R _{thJA}	DC operation	46	
Approximate weight			0.24	g
			0.008	oz.
Marking device		Case style SMC (similar to DO-214AB)	V36	

Notes

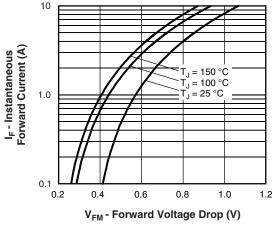
(1) $\frac{dP_{tot}}{dT_J} < \frac{1}{R_{thJA}}$ thermal runaway condition for a diode on its own heatsink

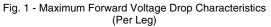
(2) Mounted 1" square PCB



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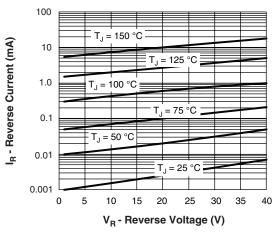


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage (Per Leg)

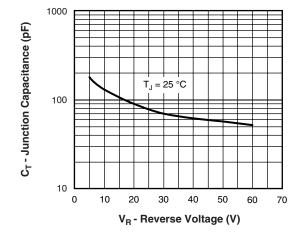


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

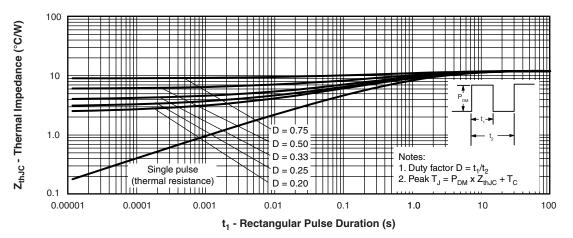
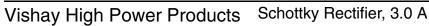
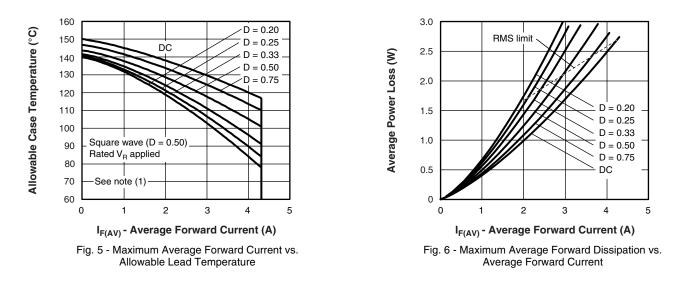


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics (Per Leg)

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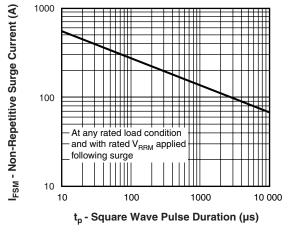


Fig. 7 - Maximum Peak Surge Forward Current vs. Pulse Duration

Note

- ⁽¹⁾ Formula used: $T_C = T_J (Pd + Pd_{REV}) \times R_{thJC}$; $Pd = Forward power loss = I_{F(AV)} \times V_{FM} at (I_{F(AV)}/D)$ (see fig. 6); $Pd_{REV} = Inverse power loss = V_{R1} \times I_R (1 D)$; $I_R at V_{R1} = 80 \%$ rated V_R

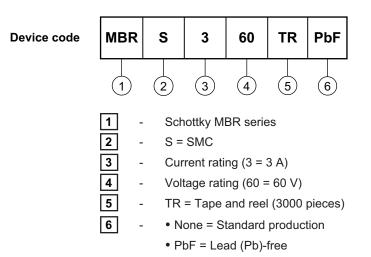
VISHA



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ORDERING INFORMATION TABLE



LINKS TO RELATED DOCUMENTS			
Dimensions http://www.vishay.com/doc?95023			
Part marking information	http://www.vishay.com/doc?95029		
Packaging information	http://www.vishay.com/doc?95034		



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