

Dielectric Resonator Oscillator Model MDR5530

Phase Locked Internal Reference 9.0 to 13.0 GHz

Specifications ⁽¹⁾:

Frequency Available: ⁽²⁾	9.0 - 13.0 GHz Coverage
Stability (max.)	±2 ppm max.
Harmonics (max.)	-40 dBc
Spurious (max.)	-85 dBc
Output Power (min.)	+17 dBm ±2dB

Phase Noise (typ.)	
100 Hz Offset	-70 dBc/Hz max.
1 kHz Offset	-95 dBc/Hz max
10 kHz Offset	-105 dBc/Hz max
100 kHz Offset	-110 dBc/Hz max
1 MHz Offset	-130 dBc/Hz max

Notes:

1. Specifications labeled "min." or "max." are guaranteed in a 50 Ohm system over the specified temperature range.
2. Exact output frequency must be specified.

Internal Crystal Reference Frequency	100 MHz nominal
Phase Voltage Set to (nom.)	+5.0 VDC
Lock Range (min.)	+2 to +9 VDC
Phase-Lock Alarm	TTL Compatible
Locked	TTL High
Unlocked	TTL Low
Input DC	+12 ±1 VDC @ 725 mA max. quiescent 1 A max. at cold start
Operating Temperature	-40 to +85°C
RF Connector	SMA Male
DC Connector	Solder Pin

