



Small-Signal Chip Diode

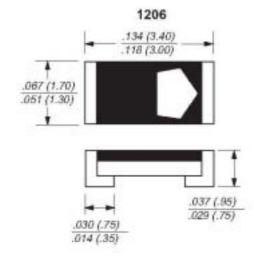
Features

- This diode is also available in other case styles including the 0805 case with the type designation CD4148WSP, and the 0603 case with the type designation CD4148WTP
- · Silicon Epitaxial Planar Diode
- · Fast switching diode.

Mechanical Data

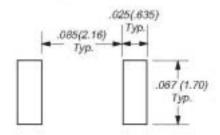
Case: 1206

Weight: approx. 10 mg Marking: Cathode arrow



Dimensions in inches and (millimeters)

Mounting Pad Layout



Absolute Maximum Ratings & Thermal Characteristics Tamp = 25 °C, unless otherwise specified

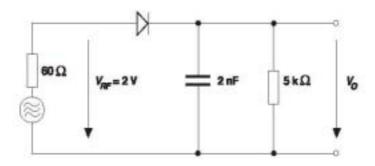
Parameter	Symbol	Value	Unit
Reverse voltage	V _R	75	V
Peak reverse voltage	V _{RM}	100	V
Forward continuous current	1 _{FW}	300	mA
Average rectified current sin half wave rectification with resistive load 1>=50 Hz	l _{F\$(0/)}	1501)	mA
Surge forward current t < 1 s and T _j = 25 °C	FSM	500	mA
Power dissipation	P _{tot}	500 ⁽¹⁾	mW
Typical Thermal Resistance Junction to Ambiant Air	R _{RIA}	450 ¹⁾	K/W
Junction temperature	Tj	150	°C
Storage temperature	Ts	- 85 to + 175	°C

f) Valid provided that electrodes are kept at ambient temperature.

Electrical Characteristics Tanto = 25 °C, unless otherwise specified

Parameter		Symbol	Min	Max	Unit
orward voltage	I _F = 10 mA	V _F		1.0	V
	V _R = 20 V	-		25	nA
Leakage current	V _R = 75 V	1 _R		5.0	μA
	V _R = 20 V, T _J = 150 °C			50	μА
Capacitance	V _F = V _R = 0 V	Ctot		4	pF
Voltage rise when switching ON	tested with 50 mA pulses, $t_p = 0.1$ s, rise time < 30 ns, $t_p = (5 \text{ to } 100) \text{ kHz}$	V _b		2.5	V
Reverse recovery time	I_F = 10 mA to I_R = 1 mA, V_R = 6 V, R_L = 100 Ω	t _{er}		4	ns
Rectification efficiency	f = 100 MHz, V _{RF} = 2 V		0.45		
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Rectification Efficiency Measurement Circuit



Typical Characteristics (T_{amb} = 25 °C unless otherwise specified)

Figure 1. Forward Characteristics

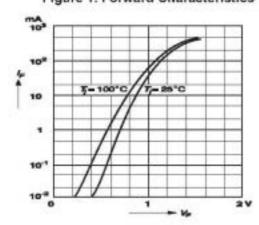


Figure 2. Dynamic Forward Resistance

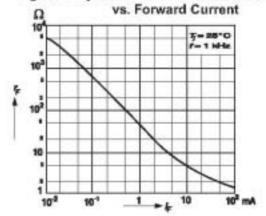


Figure 3. Admissible Power Dissipation vs. Ambient Temperature

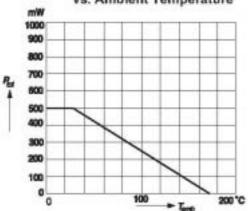


Figure 4. Relative Capacitance vs. Reverse Voltage

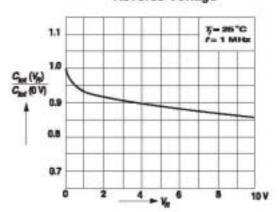


Figure 5. Leakage Current vs. Junction Temperature

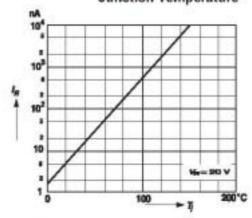


Figure 6. Admissible Repetitive Peak Forward Current vs. Pulse Duration

