



**CHENMKO ENTERPRISE CO.,LTD**

*Lead free devices*

**SURFACE MOUNT  
NPN Silicon Transistor**

**VOLTAGE 100 Volts CURRENT 5 Ampere**

**CHT5338ZPT**

**APPLICATION**

- \* Telephony and professional communication equipment.
- \* Other switching applications.

**FEATURE**

- \* Small flat package. ( SC-73/SOT-223 )
- \* Suitable for high packing density.
- \* High saturation current capability.
- \* Voltage controlled small signal switch.

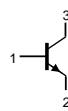
**CONSTRUCTION**

- \* NPN Silicon Power Transistor

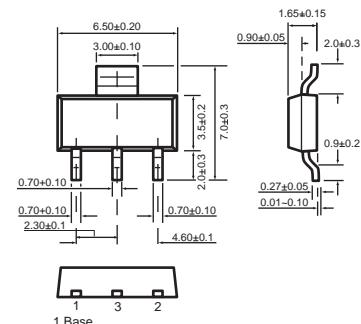
**MARKING**

ZLN

**CIRCUIT**



**SC-73/SOT-223**



Dimensions in millimeters

**SC-73/SOT-223**

**LIMITING VALUES**

In accordance with the Absolute Maximum Rating System (IEC 60134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
$V_{CBO}$	collector-base voltage	open emitter	—	100	V
$V_{CEO}$	collector-emitter voltage	open base	—	100	V
$V_{EBO}$	emitter-base voltage	open collector	—	6.0	V
$I_C$	collector current DC		—	5.0	A
$I_B$	base current		—	1.0	A
$P_{tot}$	total power dissipation	$T_{amb} \leq 25^\circ\text{C}$ ; note 1	—	2.0	W
$T_{stg}$	storage temperature		-65	+150	°C
$T_j$	junction temperature		—	150	°C
$T_{amb}$	operating ambient temperature		-65	+150	°C

**Note**

1. Transistor mounted on an FR4 printed-circuit board.

## RATING CHARACTERISTIC CURVES ( CHZ5338ZPT )

### THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
$R_{th\ j-a}$	thermal resistance from junction to ambient	note 1	500	K/W

**Note**

- Transistor mounted on an FR4 printed-circuit board.

### CHARACTERISTICS

$T_{amb} = 25^\circ C$  unless otherwise specified.

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
$I_{CBO}$	collector cut-off current	$I_E = 0; V_{CB} = 100V$	–	10	nA
$I_{EBO}$	emitter cut-off current	$I_C = 0; V_{EB} = 6 V$	–	100	nA
$h_{FE}$	DC current gain	$V_{CE} = 2 V$ ; note 1 $I_C = 500mA$ $I_C = 2.0 A$ $I_C = 5.0 A$	30 30 20	– 120 –	
$V_{CEsat}$	collector-emitter saturation voltage	$I_C = 2.0 A; I_B = 200 mA$ $I_C = 5.0 A; I_B = 500 mA$	– –	0.7 1.2	V
$V_{BEsat}$	base-emitter saturation voltage	$I_C = 2.0 A; I_B = 200 mA$ $I_C = 5.0 A; I_B = 500 mA$	– –	1.2 1.8	V
$C_c$	collector capacitance	$I_E = i_e = 0; V_{CB} = 10 V; f = 1 MHz$	–	250	pF
$C_e$	emitter capacitance	$I_C = i_e = 0; V_{BE} = 2V; f = 1 MHz$	–	1000	pF
$f_T$	transition frequency	$I_C = 500 mA; V_{CE} = 10 V; f = 10 MHz$	30	–	MHz

### Switching times (between 10% and 90% levels);

$t_d$	delay time	$V_{CC}=40V, V_{BE}=3.0V, I_C=2.0A, I_B1=200mA$	–	100	ns
$t_r$	rise time		–	100	ns
$t_s$	storage time	$V_{CC}=40V, I_C=2.0A, I_B1=I_B2=200mA$	–	2.0	ns
$t_f$	fall time		–	200	ns

**Note**

- Pulse test:  $t_p \leq 300 \mu s$ ;  $\delta \leq 0.02$ .