

Low Cost SMT Dual Band Diplexer AMPS/PCS and GSM/DCS

Rev. V2

Features

- Small Size and Low Profile
- Superior Repeatability
- Passbands 824 960 MHz and 1850 1990
- Passbands 880 960 MHz and 1700 1900
- Typical Passband Insertion Loss: 0.5 dB
- 2 Watt Power Handling
- Low Cost
- Lead-Free SO-8 Package
- 100% Matte Tin Plating over Copper
- Halogen-Free "Green" Mold Compound
- 260°C Reflow Compatible
- RoHS* Compliant Version of DP52-0002

Description

M/A-COM's MAFLCC0001 is an IC-based monolithic diplexer in a low cost SOIC-8 SMT plastic package. This diplexer is ideally suited for applications where small size, low insertion loss, superior repeatability, and low cost are required. Typical applications include AMPS/PCS and GSM/DCS dual mode portable devices.

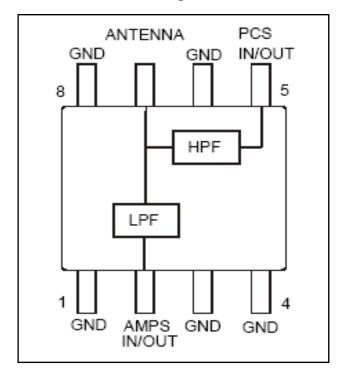
The MAFLCC0001 is fabricated using a passiveintegrated circuit process. The process features fullchip pssivation for increased performance and reliability.

Ordering Information

Part Number	Package
MAFLCC0001	Bulk Packaging
MAFLCC0001-TR	1000 piece reel
MAFLCC0001-TB	Sample Test Board

Note: Reference Application Note M513 for reel size information.

Functional Block Diagram^{1,2}



- All unused pins must be RF and DC grounded.
- Pins 1 and 4 are thermal ground contacts.

Pin Configuration

Pin No.	Function	Pin No.	Function
1	GND	5	PCS IN/OUT
2	AMPS IN/OUT	6	GND
3	GND	7	ANTENNA
4	GND	8	GND

^{*} Restrictions on Hazardous Substances, European Union Directive 2002/95/EC.

[•] North America Tel: 800.366.2266 • Europe Tel: +353.21.244.6400



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824 – 960 / 1850 – 1990 MHz Electrical Specifications: T_A = 25°C, Z_0 = 50 Ω

Parameter	Units	Min	Тур	Max
Passband Insertion Loss	dB	_	0.5	0.8
Stopband Isolation	dB	12	18	_
Passband VSWR	_	_	1.4:1	1.7:1

880 – 960 / 1700 – 1900 MHz Electrical Specifications: $T_A = 25$ °C, $Z_0 = 50\Omega$

Parameter	Units	Min	Тур	Max
Passband Insertion Loss	dB	_	0.5	0.9
Stopband Isolation	dB	12	18	_
Passband VSWR	_	_	1.5:1	1.8:1

Absolute Maximum Ratings 3,4

Parameter	Absolute Maximum
Input Power	2W CW
Operating Temperature	-40°C to +85°C
Storage Temperature	-65°C to +150°C

- Exceeding any one or combination of these limits may cause permanent damage to this device.
- M/A-COM does not recommend sustained operation near these survivability limits.

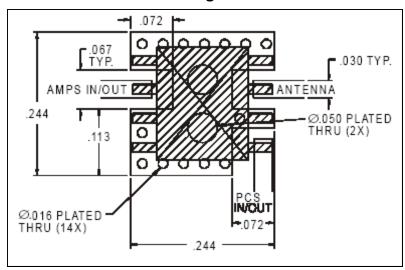
Handling Procedures

Please observe the following precautions to avoid damage:

Static Sensitivity

GMIC Circuits are sensitive to electrostatic discharge (ESD) and can be damaged by static electricity. Proper ESD control techniques should be used when handling these devices.

Recommended PCB Configuration



typical. Mechanical outline has been fixed. Engineering samples and/or test data may be available. Commitment to produce in volume is not guaranteed.

MAFLCC0001

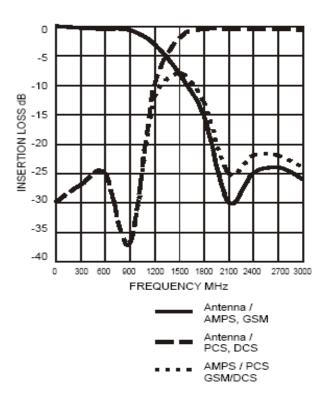


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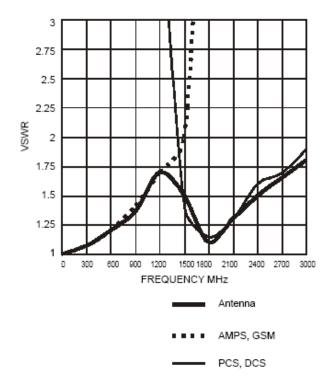
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Typical Performance Curves

Insertion Loss vs. Frequency



VSWR vs. Frequency



[•] India Tel: +91.80.4155721

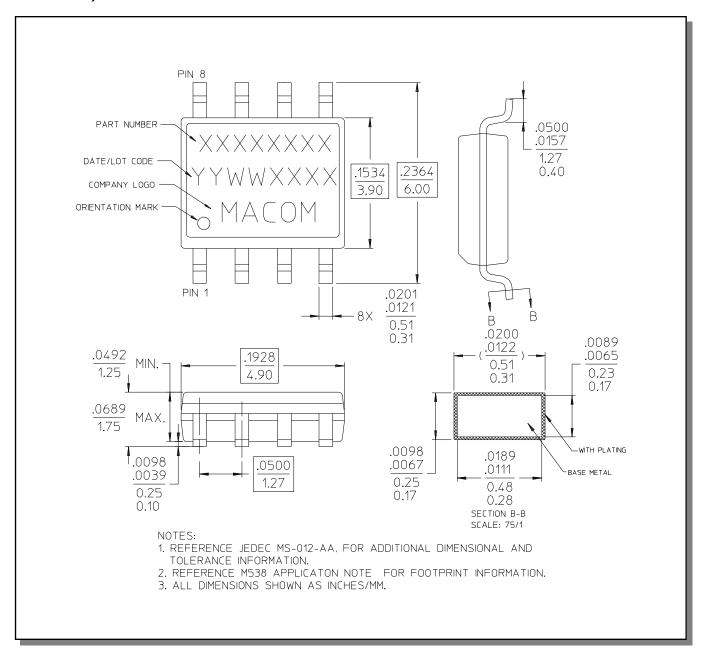
MAFLCC0001



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Lead-Free, SOIC-8[†]



Reference Application Note M538 for lead-free solder reflow recommendations.