## General Description

The AP3031 is an inductor-based DC/DC boost converter designed to drive LED arrays. 1.4A switching current allows AP3031 to be used in different 7' to 10' LCD panel backlights (3*13 LED arrays typically).

A constant frequency 1 MHz PWM control scheme is employed in this IC, which means tiny external components can be used. Specifically, 1 mm tall $4.7 \mu \mathrm{H}$ inductor and $0.47 \mu \mathrm{~F}$ output capacitor for the typical application is sufficient.

The over output voltage protection is equipped in AP3031, which protects the IC under open load condition. The AP3031 includes UVLO, soft-start, current limit and OTSD to protect the circuit.

The AP3031 is available in standard SOT-23-6, TSOT-23-6 and SOIC-8 packages.

## Features

- Up to 92\% Efficiency $\left(\mathrm{V}_{\mathrm{IN}}=9 \mathrm{~V}, \mathrm{I}_{\text {OUT }}=260 \mathrm{~mA}\right)$
- Up to 84\% Efficiency ( $\mathrm{V}_{\mathrm{IN}}=5 \mathrm{~V}, \mathrm{I}_{\mathrm{OUT}}=260 \mathrm{~mA}$ )
- Fast 1 MHz Switching Frequency
- Wide Input Voltage Range: 2.7 V to 16 V
- Low 200 mV Feedback Voltage
- Output Over Voltage Protection
- Cycle by Cycle Current Limit: 1.4A
- Built-in Soft-start
- Built-in Standby Mode to Achieve High Frequency PWM Dimming
- Built-in Thermal Shutdown Function
- Under Voltage Lockout


## Applications

- 7' to 10' LCD Panels
- Digital Photo Frame
- GPS Receiver
- EPC
- PDVD


Figure 1. Package Types of AP3031

## Pin Configuration



Figure 2. Pin Configuration of AP3031 (Top View)

## Pin Description

| Pin Number |  | Function |  |
| :---: | :---: | :---: | :--- |
| 6-Pin | 8-Pin |  |  |
| 1 | 4 | SW | Switch Pin. Connect external inductor and Schottky |
| 2 | $2,3,6$ | GND | Ground Pin |
| 3 | 1 | FB | Voltage Feedback Pin. Reference voltage is 200mV |
| 4 | 8 | CTRL | Enable and Dimming Control Pin. Connect to a high input to enable the IC or a low input to <br> disable the IC. <br> If logic low time is more than about 0.7 ms and then enable the IC, the AP3031 will soft start to <br> protect system departments. If logic low time is less than about 0.7 ms and then enable the IC, <br> the AP3031 will hold on standby mode and start directly to achieve high frequency dimming |
| 5 | 7 | OV | Over-voltage Protection Input Pin. Connect to the output directly. On OVP condition, the out- <br> put voltage will be clamped |
| 6 | 5 | VIN | Input Supply Pin. Must be locally bypassed |

## Functional Block Diagram



Figure 3. Functional Block Diagram of AP3031

## Ordering Information



| Package | Temperature <br> Range | Part Number | Marking ID |  |
| :---: | :---: | :--- | :--- | :---: | Packing Type

BCD Semiconductor's Pb-free products, as designated with "G1" suffix in the part number, are RoHS compliant and green.

WHITE LED STEP-UP CONVERTER

## Absolute Maximum Ratings (Note 1)

| Parameter | Symbol | Value | Unit |
| :--- | :---: | :---: | :---: |
| Input Voltage | $\mathrm{V}_{\text {IN }}$ | 20 | V |
| SW Voltage | $\mathrm{V}_{\text {SW }}$ | 38 | V |
| FB Voltage | $\mathrm{V}_{\text {FB }}$ | 20 | V |
| CTRL Voltage | $\mathrm{V}_{\text {CTRL }}$ | 20 | V |
| Thermal Resistance <br> (Junction to Ambient, No Heat Sink) | $\theta_{\text {JA }}$ | SOT-23-6/TSOT-23-6 | 265 |
|  | ${ }^{\circ}{ }^{\circ} \mathrm{C} / \mathrm{W}$ |  |  |
| Operating Junction Temperature | $\mathrm{T}_{\mathrm{J}}$ | SOIC-8 | 120 |
| Storage Temperature Range | $\mathrm{T}_{\text {STG }}$ | 150 | ${ }^{\circ} \mathrm{C}$ |
| Lead Temperature (Soldering, 10sec) | $\mathrm{T}_{\text {LEAD }}$ | -65 to 150 | ${ }^{\circ} \mathrm{C}$ |
| ESD (Machine Model) |  | 260 | ${ }^{\circ} \mathrm{C}$ |
| ESD (Human Body Model) | 600 | V |  |

Note 1: Stresses greater than those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under "Recommended Operating Conditions" is not implied. Exposure to "Absolute Maximum Ratings" for extended periods may affect device reliability.

## Recommended Operating Conditions

| Parameter | Symbol | Min | Max | Unit |
| :--- | :---: | :---: | :---: | :---: |
| Operating Temperature Range | $\mathrm{T}_{\mathrm{OP}}$ | -40 | 85 | ${ }^{\circ} \mathrm{C}$ |
| Input Voltage | $\mathrm{V}_{\text {IN }}$ | 2.7 | 16 | V |
| CTRL Voltage | $\mathrm{V}_{\text {CTRL }}$ |  | 16 | V |

## Electrical Characteristics

$\left(\mathrm{V}_{\mathrm{IN}}=5.0 \mathrm{~V}, \mathrm{~V}_{\mathrm{CTRL}}=5.0 \mathrm{~V}, \mathrm{~T}_{\mathrm{A}}=25^{\circ} \mathrm{C}\right.$, unless otherwise specified. $)$

| Parameter | Symbol | Conditions | Min | Typ | Max | Unit |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Operating Voltage | $\mathrm{V}_{\text {IN }}$ |  | 2.7 |  | 16 | V |
| Feedback Voltage (Note 2) | $\mathrm{V}_{\mathrm{FB}}$ | $\begin{aligned} & \mathrm{I}_{\mathrm{OUT}}=20 \mathrm{~mA}, 3 \text { LEDs, } \\ & \mathrm{T}_{\mathrm{A}}=-40^{\circ} \mathrm{C} \text { to } 85^{\circ} \mathrm{C} \end{aligned}$ | 188 | 200 | 212 | mV |
| FB Pin Bias Current | $\mathrm{I}_{\mathrm{FB}}$ |  |  | 35 | 100 | nA |
| Quiescent Current | $\mathrm{I}_{\mathrm{Q}}$ | $\mathrm{V}_{\mathrm{FB}}=\mathrm{V}_{\mathrm{IN}}$, no switching | 3.0 | 4.0 | 5.0 | mA |
| Shutdown Quiescent Current | $\mathrm{I}_{\text {SHDN }}$ | $\mathrm{V}_{\text {CTRL }}=0 \mathrm{~V}$ | 20 | 50 | 80 | $\mu \mathrm{A}$ |
| Switching Frequency | f |  | 0.75 | 1 | 1.3 | MHz |
| Maximum Duty Cycle | $\mathrm{D}_{\text {MAX }}$ |  | 90 | 93 |  | \% |
| Switch Current Limit (Note 3) | $\mathrm{I}_{\text {LIMIT }}$ | $\mathrm{D}=60 \%$ | 1.2 | 1.4 |  | A |
| Switch $\mathrm{V}_{\mathrm{CE}}$ Saturation Voltage | $\mathrm{V}_{\text {CESAT }}$ | $\mathrm{I}_{\text {SW }}=0.6 \mathrm{~A}$ |  | 300 |  | mV |
| Switch Leakage Current |  | $\mathrm{V}_{\text {SW }}=16 \mathrm{~V}$ |  | 0.01 | 5 | $\mu \mathrm{A}$ |
| CTRL Pin Voltage | $\mathrm{V}_{\text {CTRL }}$ | Active high | 1.8 |  |  | V |
|  |  | Active low |  |  | 0.5 |  |
| CTRL Pin Bias Current | $\mathrm{I}_{\text {CTRL }}$ |  | 35 | 60 | 85 | $\mu \mathrm{A}$ |
| OVP Voltage | $\mathrm{V}_{\text {OVP }}$ |  |  | 17 |  | V |
| Soft-start Time | $\mathrm{t}_{\text {SS }}$ |  |  | 250 |  | $\mu \mathrm{s}$ |
| Standby Time | $\mathrm{t}_{\text {STB }}$ |  |  | 0.7 |  | ms |
| Thermal Shutdown | $\mathrm{T}_{\text {OTSD }}$ |  |  | 155 |  | ${ }^{\circ} \mathrm{C}$ |
| Thermal Resistance <br> (Junction to Case) | $\theta_{\text {JC }}$ |  |  | 60 |  | ${ }^{\circ} \mathrm{C} / \mathrm{W}$ |

Note 2: The bold type specifications of full temperature range are guaranteed by design (GBD). Note 3: The switch current limit is related to duty cycle. Please refer to Figure 15 for detail.

## Typical Performance Characteristics

(WLED forward voltage $\left(\mathrm{V}_{\mathrm{F}}\right)=3.2 \mathrm{~V}$ at $\mathrm{I}_{\mathrm{F}}=20 \mathrm{~mA}$, unless otherwise noted.)


Figure 4. Efficiency vs. Output Current


Figure 6. Minimum Operating Voltage vs. Temperature


Figure 5. Efficiency vs. Input Voltage


Figure 7. Quiescent Current vs. Input Voltage

## Typical Performance Characteristics (Continued)

(WLED forward voltage $\left(\mathrm{V}_{\mathrm{F}}\right)=3.2 \mathrm{~V}$ at $\mathrm{I}_{\mathrm{F}}=20 \mathrm{~mA}$, unless otherwise noted.)


Figure 8. Shutdown Quiescent Current vs. Input Voltage


Figure 10. CTRL Pin Current vs. CTRL Pin Voltage


Figure 9. CTRL Pin Voltage vs. Temperature

Figure 11. Feedback Voltage vs. Temperature

## Typical Performance Characteristics (Continued)

(WLED forward voltage $\left(\mathrm{V}_{\mathrm{F}}\right)=3.2 \mathrm{~V}$ at $\mathrm{I}_{\mathrm{F}}=20 \mathrm{~mA}$, unless otherwise noted.)


Figure 12. Frequency vs. Temperature


Figure 14. Frequency vs. Input Voltage


Figure 13. OVP Voltage vs. Temperature


Figure 15. Switch Current Limit vs. Duty Cycle

## Typical Performance Characteristics (Continued)

(WLED forward voltage $\left(\mathrm{V}_{\mathrm{F}}\right)=3.2 \mathrm{~V}$ at $\mathrm{I}_{\mathrm{F}}=20 \mathrm{~mA}$, unless otherwise noted.)


Figure 16. Switch Saturation Voltage vs. Switch Current


Figure 17. Case Temperature vs. Output Current

## Application Information

## Operation

The AP3031 is a boost DC-DC converter which uses a constant frequency, current mode control scheme to provide excellent line and load regulation. Operation can be best understood by referring to Figure 3 and Figure 24.

At the start of each oscillator cycle, switch Q1 turns on. The switch current will increase linearly. The voltage on sense resistor is proportional to the switch current. The output of the current sense amplifier is added to a stabilizing ramp and the result is fed into the noninversion input of the PWM comparator A2. When this voltage exceeds the output voltage level of the error amplifier A1, the switch is turned off.

It is clear that the voltage level at inversion input of A2 sets the peak current level to keep the output in regulation. This voltage level is the output signal of error amplifier A1, and is the amplified signal of the voltage difference between feedback voltage and reference voltage of 200 mV . So, a constant output current can be provided by this operation mode.

## LED Current Control

Refer to Figure 24, the LED current is controlled by the feedback resistor $\mathrm{R}_{\text {ISET }}$. LEDs' current accuracy is determined by the feedback voltage and resistor $\mathrm{R}_{\text {ISET }}$, so the precise resistors are preferred. The resistance of $\mathrm{R}_{\text {ISET }}$ is in inverse proportion to the LED current since the feedback reference is fixed at 200 mV . The relation for $\mathrm{R}_{\text {ISET }}$ and LED current ( $\mathrm{I}_{\text {LED }}$ )can be expressed as below:

$$
\mathrm{R}_{\text {ISET }}=\frac{200 \mathrm{mV}}{\mathrm{I}_{\mathrm{LED}}}
$$

## Over Voltage Protection

The AP3031 has an internal open load protection circuit. When the LEDs are disconnected from circuit or fail open, the output voltage is clamped at about 17 V . The AP3031 will switch at a low frequency, and minimize current to avoid input voltage drop.

## Soft Start

The AP3031 has an internal soft start circuit to limit the inrush current during startup. If logic low time on CTRL pin is more than about 0.7 ms and then enable the IC, the AP3031 will start smoothly to protect the supplier. The time of startup is controlled by internal soft-start capacitor. Details please refer to Figure 18.


Figure 18. Soft-start Waveform

$$
\mathrm{V}_{\mathrm{IN}}=5 \mathrm{~V}, 3 \times 13 \text { LEDs, } \mathrm{I}_{\mathrm{LED}}=260 \mathrm{~mA}
$$

## Standby and Dimming

To avoid audio noise and achieve high frequency dimming, AP3031 is equipped with standby function. If logic low time on CTRL pin is less than about 0.7 ms and then enable the IC, the AP3031 will hold on standby mode and start directly to achieve high frequency dimming. Details please refer to Figure 19.


Figure 19. Standby Waveform
Two typical types of dimming control circuit are present as below. First, controlling CTRL Pin voltage to change operation state is a good choice. Second,

## WHITE LED STEP-UP CONVERTER

## Application Information (Continued)

changing the feedback voltage to get appropriate duty and luminous intensity is also useful.

## (1) Adding a Control Signal to CTRL Pin

Adding a PWM signal to CTRL pin directly, the AP3031 is turned on and off by this signal. When the PWM frequency is lower than 1 kHz (Typ.), the IC works in the soft-start mode to dimming the light. On contrary, when the PWM frequency is higher than 1 kHz (Typ.), the IC works in the standby mode: the converter ceaselessly switches off and directly starts to achieve light dimming. This standby function allows AP3031 to support high frequency dimming (up to 25 kHz or higher) to avoid audio noise. More details please refer to Figure 20 and Figure 21.


Figure 20. Dimming Control Using a PWM Signal in CTRL Pin


Figure 21. High Frequency (25kHz) Dimming Waveform

## (2) Changing the Effective Feedback Voltage

There are two popular methods to change the effective feedback voltage.

First, adding a constant DC voltage through a resistor divider to FB pin can control the dimming. Changing the DC voltage or resistor between the FB Pin and the DC voltage can get appropriate luminous intensity.

Comparing with all kinds of PWM signal control, this method features a stable output voltage and LEDs current. Please refer to Figure 22.


Figure 22. Dimming Control Using DC Voltage

Second, using a filtered PWM signal can do it. The filtered PWM signal can be considered as a varying and adjustable DC voltage, please refer to Figure 23.


Figure 23. Dimming Control Using Filtered PWM Voltage

## Typical Application



Figure 24. Typical Application of AP3031 ( $3 \times 13$ WLEDs)

## Mechanical Dimensions

SOT-23-6
Unit: mm(inch)


## Mechanical Dimensions (Continued)

TSOT-23-6


Unit: mm(inch)


## Mechanical Dimensions (Continued)

SOIC-8
Unit: mm(inch)


Note: Eject hole, oriented hole and mold mark is optional.

## BCH A

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## MAIN SITE

## - Headquarters

BCD Semiconductor Manufacturing Limited
No. 1600, Zi Xing Road, Shanghai ZiZhu Science-based Industrial Park, 200241, China
Tel: +86-21-24162266, Fax: +86-21-24162277

## REGIONAL SALES OFFICE

## Shenzhen Office

Shanghai SIM-BCD Semiconductor Manufacturing Co., Ltd., Shenzhen Office
Room E, 5F, Noble Center, No.1006, 3rd Fuzhong Road, Futian District, Shenzhen,
518026, China
Tel: +86-755-8826 7951
Fax: +86-755-8826 7865

- Wafer Fab

Shanghai SIM-BCD Semiconductor Manufacturing Co., Ltd.
800 Yi Shan Road, Shanghai 200233, China
Tel: +86-21-6485 1491, Fax: +86-21-5450 0008

## Taiwan Office

BCD Semiconductor (Taiwan) Company Limited 4F, 298-1, Rui Guang Road, Nei-Hu District, Taipei, Taiwan
Tel: +886-2-2656 2808
Fax: +886-2-2656 2806

