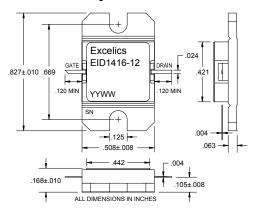


UPDATED 12/06/2006

14.0-16.0 GHz 12-Watt Internally Matched Power FET

FEATURES

- 14.0– 16.0GHz Bandwidth
- Input/Output Impedance Matched to 50 Ohms
- +41.0 dBm Output Power at 1dB Compression
- 5.0 dB Power Gain at 1dB Compression
- 22% Power Added Efficiency
- Hermetic Metal Flange Package



Caution! ESD sensitive device.

EID1416-12

ELECTRICAL CHARACTERISTICS (T_a = 25°C)

SYMBOL	PARAMETERS/TEST CONDITIONS ¹		ТҮР	MAX	UNITS
P _{1dB}	Output Power at 1dB Compression $f = 14.0-16.0$ GHz $V_{DS} = 10 \text{ V}, I_{DSQ} \approx 3200$ mA	40.0	41.0		dBm
G _{1dB}	Gain at 1dB Compression $f = 14.0-16.0$ GHz $V_{DS} = 10 \text{ V}, I_{DSQ} \approx 3200$ mA	4.0	5.0		dB
∆G	Gain Flatnessf = 14.0-16.0GHz V_{DS} = 10 V, $I_{DSQ} \approx 3200$ mA			±1.0	dB
PAE	Power Added Efficiency at 1dB Compression V_{DS} = 10 V, $I_{DSQ} \approx 3200$ mAf = 14.0-16.0GHz		22		%
Id_{1dB}	Drain Current at 1dB Compression f = 14.0-16.0GHz		4000	5000	mA
I _{DSS}	Saturated Drain Current V_{DS} = 3 V, V_{GS} = 0 V		6000	7500	mA
VP	Pinch-off Voltage V_{DS} = 3 V, I_{DS} = 60 mA		-1.0	-2.5	V
R _{TH}	Thermal Resistance ²		2.5	3.0	°C/W

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Note:

1. Tested with 50 Ohm gate resistor.

2. Overall Rth depends on case mounting.

MAXIMUM RATING^{1,2} ($T_a = 25^{\circ}C$)

SYMBOLS	PARAMETERS	ABSOLUTE ¹	CONTINUOUS ²
V _{DS}	Drain-Source Voltage	15V	10V
V _{GS}	Gate-Source Voltage	-5V	-3V
lgsf	Forward Gate Current	135mA	45mA
lgsr	Reverse Gate Current	-21mA	-7mA
Pin	Input Power	40.0dBm	@ 3dB Compression
Tch	Channel Temperature	175 °C	175 °C
Tstg	Storage Temperature	-65 to +175 °C	-65 to +175 °C
Pt	Total Power Dissipation	50W	50W

Note:

1. Exceeding any of the above ratings may result in permanent damage.

2. Exceeding any of the above ratings may reduce MTTF below design goals.

Specifications are subject to change without notice. Excelics Semiconductor, Inc. 310 De Guigne Drive, Sunnyvale, CA 94085 Phone: 408-737-1711 Fax: 408-737-1868 Web: www.excelics.com

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