

100BX Module for Multi-port Application with Enhanced Common Mode Attenuation

EPF8038S



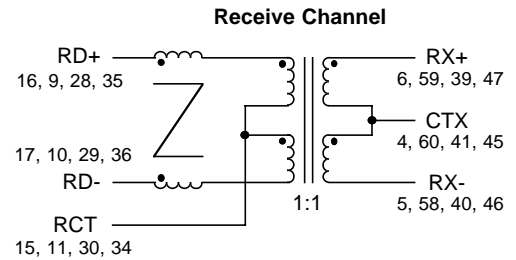
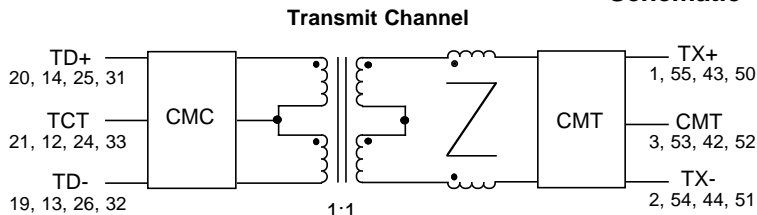
- Optimized for DP83840A/Twister combination •
- Recommended for use with ICS1890 series, SSI TSC78Q2120 chips •
- Guaranteed to operate with 8 mA DC bias at 70°C •
- Complies with or exceeds IEEE 802.3, 10 BT/100 BX Standards •

Electrical Parameters @ 25° C

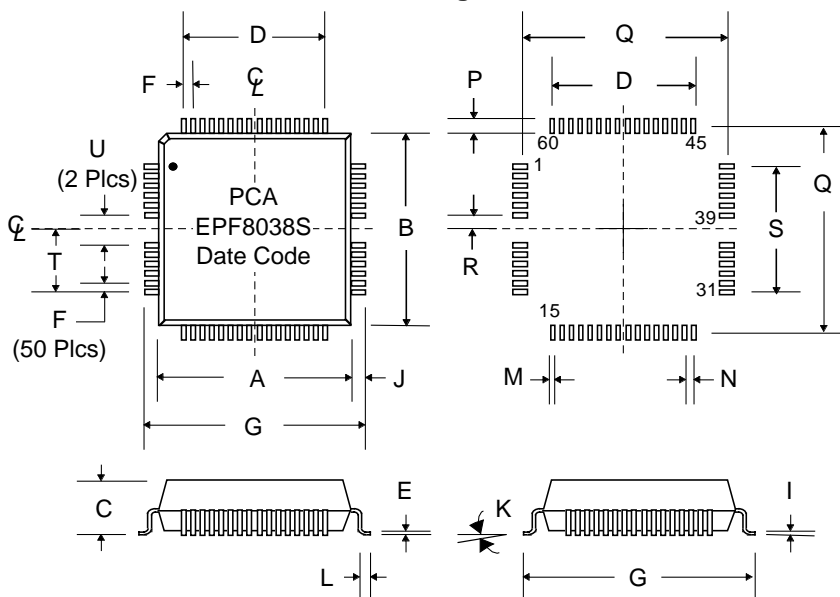
OCL @ 70°C	Insertion Loss (dB Max.)						Return Loss (dB Min.)						Common Mode Rejection (dB Min.)						Crosstalk (dB Min.) [Between Channels]	
	1-80 MHz		80-100 MHz		100-150 MHz		1-30 MHz		30-60 MHz		60-100 MHz		1-30 MHz		30-60 MHz		60-100 MHz		0.1-60 MHz	60-100 MHz
	Xmit	Rcv	Xmit	Rcv	Xmit	Rcv	Xmit	Rcv	Xmit	Rcv	Xmit	Rcv	Xmit	Rcv	Xmit	Rcv	Xmit	Rcv		
100 KHz, 0.1 Vrms 8 mA DC Bias	-1	-1	-1	-1	-3.5	-3	-18	-18	-12	-12	-10	-10	-45	-40	-45	-40	-40	-35	-40	-38

- Isolation : 1500 Vrms • Impedance : 100 Ω • Rise Time : 3.0 nS Max. •

Schematic



Package



Dimensions

Dim.	(Inches)			(Millimeters)		
	Min.	Max.	Nom.	Min.	Max.	Nom.
A	.995	1.005		25.27	25.53	
B	.995	1.005		25.27	25.53	
C	.255	.265		6.48	6.73	
D	.750	Typ.		19.05	Typ.	
E	.003	.020		.076	.508	
F	.050	Typ.		1.27	Typ.	
G	1.150	Typ.		29.21	Typ.	
H	.016	.022		.406	.559	
I	.008	.012		.203	.305	
J	.090	Typ.		2.29	Typ.	
K	0°	8°		0°	8°	
L	.025	.045		.640	1.14	
M			.025			.640
N			.050			1.27
P			.080			2.03
Q			1.100			27.94
R			.075			1.01
S			.650			16.51
T	.325	Typ.		.825	Typ.	
U	.150	Typ.		3.81	Typ.	