

Single P-channel MOSFET

ELM34419AA-N

■ General description

ELM34419AA-N uses advanced trench technology to provide excellent $R_{ds(on)}$, low gate charge and low gate resistance.

■ Features

- $V_{ds} = -30V$
- $I_d = -10A$
- $R_{ds(on)} < 20m\Omega$ ($V_{gs} = -10V$)
- $R_{ds(on)} < 35m\Omega$ ($V_{gs} = -4.5V$)

■ Maximum absolute ratings

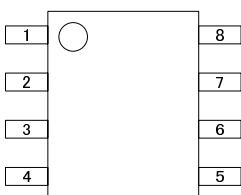
Parameter	Symbol	Limit	Unit	Note
Drain-source voltage	V_{ds}	-30	V	
Gate-source voltage	V_{gs}	± 25	V	
Continuous drain current	I_d	-10	A	
Ta=70°C		-8		
Pulsed drain current	I_{dm}	-55	A	3
Avalanche current	I_{ar}	-29	A	
Avalanche energy	E_{as}	43	mJ	
Power dissipation	P_d	3	W	
Ta=25°C		2		
Junction and storage temperature range	T_j, T_{stg}	-55 to 150	°C	

■ Thermal characteristics

Parameter	Symbol	Typ.	Max.	Unit	Note
Maximum junction-to-case	$R\theta_{jc}$		25	°C/W	
Maximum junction-to-ambient	$R\theta_{ja}$		40	°C/W	

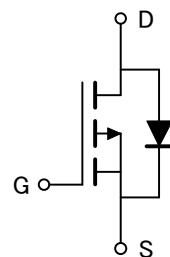
■ Pin configuration

SOP-8 (TOP VIEW)



Pin No.	Pin name
1	SOURCE
2	SOURCE
3	SOURCE
4	GATE
5	DRAIN
6	DRAIN
7	DRAIN
8	DRAIN

■ Circuit



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■ Electrical characteristics

$T_a=25^\circ C$

Parameter	Symbol	Condition	Min.	Typ.	Max.	Unit	Note
STATIC PARAMETERS							
Drain-source breakdown voltage	BVDSS	$I_d=-250\ \mu A, V_{GS}=0V$	-30			V	
Zero gate voltage drain current	IDSS	$V_{DS}=-24V, V_{GS}=0V$ $V_{DS}=-20V, V_{GS}=0V, T_j=125^\circ C$			-1 -10	μA	
Gate-body leakage current	IGSS	$V_{DS}=0V, V_{GS}=\pm 25V$			± 100	nA	
Gate threshold voltage	VGS(th)	$V_{DS}=V_{GS}, I_d=-250\ \mu A$	-1.0	-1.5	-3.0	V	
Static drain-source on-resistance	RDS(on)	$V_{GS}=-10V, I_d=-10A$ $V_{GS}=-4.5V, I_d=-7A$		15 25	20 35	$m\Omega$ $m\Omega$	1
Forward transconductance	GFS	$V_{DS}=-10V, I_d=-10A$		24		S	1
Diode forward voltage	VSD	$I_S=-1A, V_{GS}=0V$			-1.2	V	1
Max. body-diode continuous current	IS				-2.5	A	
DYNAMIC PARAMETERS							
Input capacitance	CISS	$V_{GS}=0V, V_{DS}=-15V, f=1MHz$		1490		pF	
Output capacitance	Coss			301		pF	
Reverse transfer capacitance	CRSS			190		pF	
Gate resistance	RG	$V_{GS}=15mV, V_{DS}=0V, f=1MHz$		7.8	9.0	Ω	
SWITCHING PARAMETERS							
Total gate charge	QG	$V_{GS}=-10V, V_{DS}=-15V$ $I_d=-10A$		26		nC	2
Gate-source charge	QGS			4		nC	2
Gate-drain charge	QGD			5		nC	2
Turn-on delay time	TD(on)	$V_{GS}=-10V, V_{DS}=-15V$ $I_d \approx -1A, R_{gen}=6\ \Omega$		5.7		ns	2
Turn-on rise time	TR			10.0		ns	2
Turn-off delay time	TD(off)			18.0		ns	2
Turn-off fall time	TF			5.0		ns	2

NOTE :

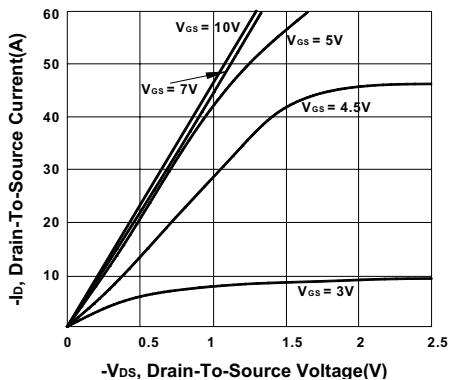
1. Pulsed width $\leq 300\ \mu sec$ and Duty cycle $\leq 2\%$.
2. Independent of operating temperature.
3. Pulsed width limited by maximum junction temperature.
4. Duty cycle $\leq 1\%$.

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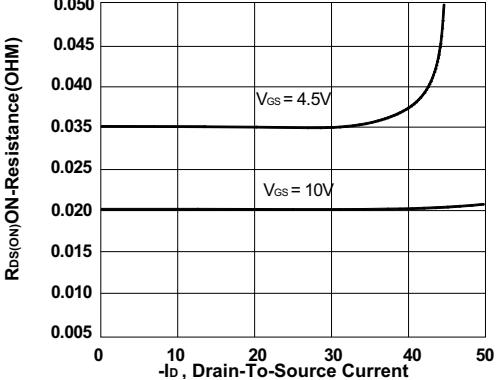
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■ Typical electrical and thermal characteristics

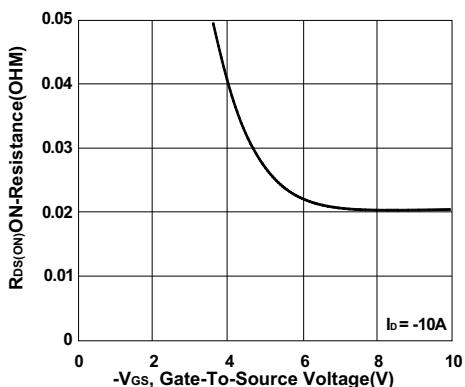
Output Characteristics



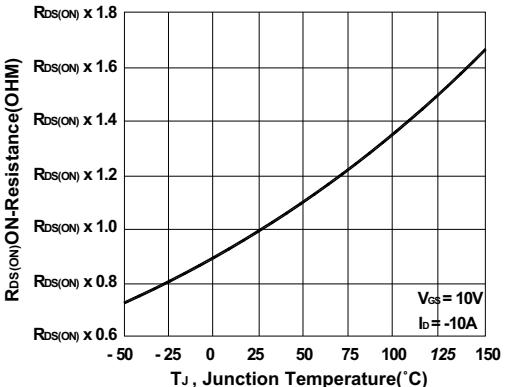
On-Resistance VS Drain Current



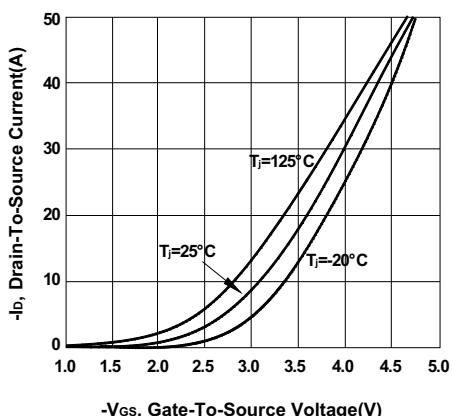
On-Resistance VS Gate-To-Source



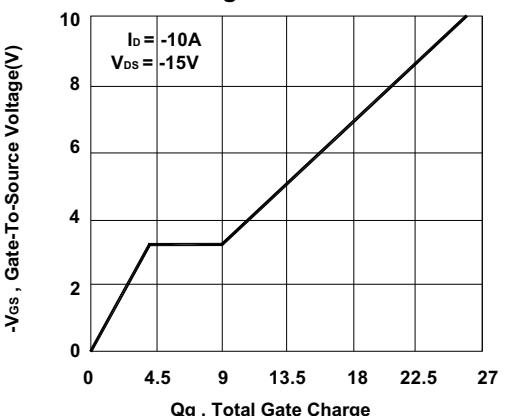
On-Resistance VS Drain Current



Transfer Characteristics



Gate charge Characteristics



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