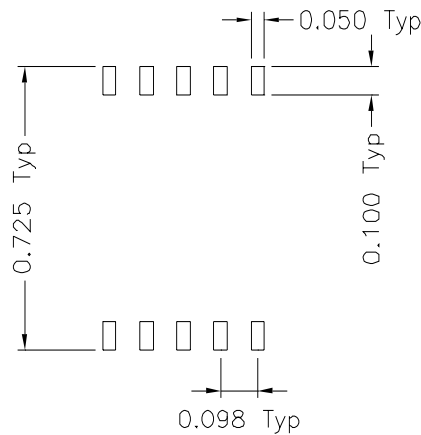
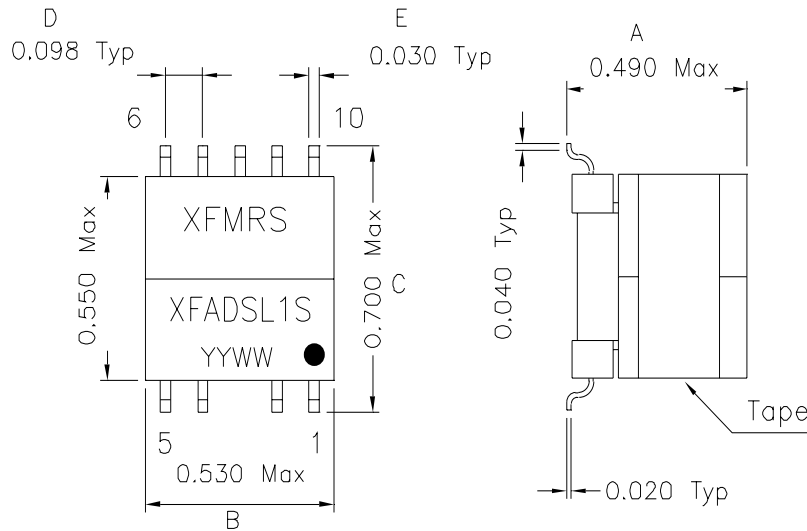


### 1. Dimensions:

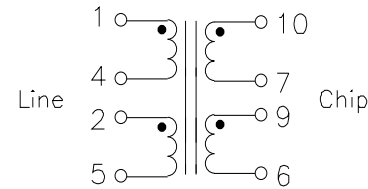


Suggested PCB Layout

Note: Designed to meet the requirements of UL1950 for Supplementary Insulation with 250 Working volts.

DOC. REV A/1

### 2.Schematic:



### 3.Electrical Specifications: @25°C

OCL: Pins 1-5 408uH±10% @10KHz, 0.1V

Leakage L: Pins 1-5 12uH Max @100KHz, 0.1V Short 9-7

Cw/w: Pins 1-10 25pF Max @100KHz, 0.1V

DC Res.: Pins 1-4, 2-5 0.390 Ohms±15%  
9-6, 10-7 0.330 Ohms±15%

Turns Ratio: (1-5) : (10-6) 1CS:1CS±1%

Isolation: 2000Vrms Line to Chip

THD: -85dB Min @ 100KHz, 5.3Vrms 100 Ohm load

Long Bal: 55dB Min 20KHz - 1MHz

Insertion Loss: 0.5dB Max @ 772KHz 100 Ohm load

Freq. Resp.: ±3dB 20KHz - 2MHz

Operating Temp.: -40 to +85°C

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	UNLESS OTHERWISE SPECIFIED TOLERANCES: .xxx ±0.010	P/N: XFADSL1S	REV. A
Dimensions in INCH	DWN.	廖玉坤	Apr-24-99
	CHK.	李清儿	Apr-24-99
SHEET 1 OF 1	APP.	Joe Huff	Apr-24-99