

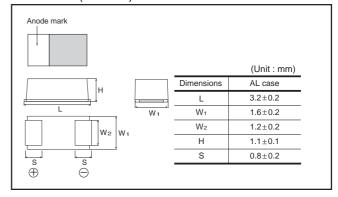
# Chip tantalum capacitors

## **TCT Series AL Case**

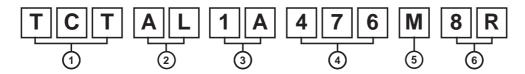
#### ●Features (AL)

- 1) Vital for all hybrid integrated circuits board application.
- 2) Wide capacitance range.
- 3) Screening by thermal shock.

#### ●Dimensions (Unit: mm)



## ●Part No. Explanation



1)Series name

TCT

2 Case style

(3)Rated voltage

4 Nominal capacitance

Nominal capacitance in pF in 3 digits: 2 significant figures followed by the figure representing the number of 0's.

(5) Capacitance tolerance

M: ±20%

							25	
CODE	0E	0G	0J	1A	1C	1D	1E	1V

6 Taping

a · Tane widt

R : Positive electrode on the side opposite to sprocket hole

#### Rated table

				Rated vo	ltage (V	)		
(μF)	2.5	4	6.3	10	16	20	25	35
	0E	0G	0J	1A	1C	1D	1E	1V
1.0 (105)								*AL
2.2 (225)								AL
3.3 (335)								AL
4.7 (475)							AL	
6.8 (685)							AL	
10 (106)						AL		
15 (156)					AL			
22 (226)					AL			
33 (336)				AL				
47 (476)				AL				
68 (686)			AL					
100 (107)		AL	AL					
150 (157)		AL	AL					
220 (227)	AL	AL						
330 (337)	*AL							

Remark) Case size codes (AL) in the above show products line-up.

#### Marking

The indications listed below should be given on the surface of a capacitor.

- (1) Polarity : The polarity should be shown by □ bar. (on the anode side)
  (2) Rated DC voltage : Due to the small size of AL case, a voltage code is used as shown below.
- (3) Visual typical example

(1) voltage code	(2) capacitance code
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Voltage Code	Rated DC Voltage (V)
е	2.5
g	4
j	6.3
А	10
С	16
D	20
E	25
V	35

Capacitance Code	Nominal Capacitance (μF)
Α	1.0
J	2.2
Ν	3.3
S	4.7
W	6.8
а	10
е	15
j	22
n	33
S	47
W	68
ā	100
ē	150
j	220
n	330

[AL case] note 1)





\ manufacture code

note 2) voltage code and capacitance code are variable with parts number

2/6

<sup>\*</sup> Under development

## Characteristics

Iter	n	Performance				Test	con	ditic	ons (based	on JIS C 5101–	1 and JIS C 5101–3)						
Operating Temp	perature	-5	5°C	to -	⊦125	°C						Voltage reduction when temperature exceeds +85°C			ceeds +85°C		
Maximum operat temperature with derating	ing no voltage	+85°C															
Rated voltage (	VDC)	2.5	2.5 4 6.3 10 16 20 25 35						at 85°C								
Category voltag	e (VDC)	1.6	2.5	4	6.3	10	13	16	2	22		at 12	25°C	;			
Surge voltage (	VDC)	3.2	5.0	8	13	20	26	32	4	14		at 85	5°C				
DC Leakage cu	rrent				atisfi list		he v	oltaç	ge	on		As p	er 4.	.5.1	IS C 5101-1 JIS C 5101 ted voltage	-3	
Capacitance tol	erance	Sha ±20		e sa	atisfi	ed a	allow	ance	e r	ran		As p Mea Mea	er 4. surin surin	.5.2 ng fi ng v		-3	
Tangent of loss (Df, tan δ)	angle				atisfic list		he v	oltaç	ge	on		As per 4.8 JIS C 5101-1 As per 4.5.3 JIS C 5101-3 Measuring frequency : 120±12Hz Measuring voltage : 0.5Vrms +1.5 to 2V.DC Measuring circuit : DC Equivalent series circuit					
Impedance		Shall be satisfied the voltage on " Standard list "								As per 4.10 JIS C 5101-1 As per 4.5.4 JIS C 5101-3 Measuring frequency: 100±10kHz Measuring voltage : 0.5Vrms or less Measuring circuit : DC Equivalent series circuit			eries circuit				
Resistance to Soldering heat	Appearance	There should be no significant abnormality. The indications should be clear.					normality.	As per 4.14 JIS C 5101-1 As per 4.6 JIS C 5101-3									
	L.C.	Le	ss th	nan	initia	ıl lir	nit					Dip in the solder bath Solder temp : 260±5°C					
	ΔC / C	Within ±20% of initial value						Duration : 5±0.5s  Repetition : 1									
	Df (tan δ)	Le	ss th	nan	200	% o	f initi	al liı	mi	t		Afte	the	spe	specimens, leave it at room temperature for and then measure the sample.		
Temperature cycle	Appearance	There should be no significant abnormality. The indications should be clear.				normality.	As p	er 4.	.10	JIS C 5101-							
	L.C.	Le	ss th	nan	200°	% o	f initi	al liı	mi	t					5 cycles eps 1 to 4) v	vithout discontin	uation.
	ΔC / C	Wi	thin	±20	)% o	f ini	tial v	alue	9			] ` ´			Temp.	Time	
	Df (tan δ)	Le	ss th	nan	2009	% o	f initi	al liı	mi	t			1		-55±3°C	30±3min.	
													2	R	oom temp.	3min. or less	
													3	-	125±2°C	30±3min.	
													4	_	oom temp.		
												After the specimens, leave it at room temperature f over 24h and then measure the sample.					
Moisture resistance	Appearance						o sig				normality.	As p	er 4.	.12	JIS C 5101- JIS C 5101-	-3	
	L.C.	Le	ss th	nan	200°	% o	f initi	al liı	mi	t						e under such atnerature and hum	
	ΔC / C	Wi	thin	±20	)% o	f ini	tial v	alue	9			60±2	2°C a	and	90 to 95%	RH, respectively	
	Df (tan δ)	Le	ss th	nan	200	% o	f initi	al lii	mi	t		<ul> <li>leave it at room temperature for over 24h and then measure the sample.</li> </ul>				sure the	

Iter	n	Performance	Test conditions (based on JIS C 5101–1 and JIS C 5101–3)				
Temperature Stability Temp.		−55°C	As per 4.29 JIS C 5101-1 As per 4.13 JIS C 5101-3				
Stability	ΔC / C	Within 0/–15% of initial value	As per 4.13 JIS C 5101-3				
	Df (tan δ)	Shall be satisfied the voltage on " Standard list "					
	L.C.	-					
	Temp.	+85°C					
	ΔC / C	Within +15/0% of initial value					
	Df (tan δ)	Shall be satisfied the voltage on " Standard list "					
	L.C.	5μA or 0.1CV whichever is greater					
	Temp.	+125°C					
	ΔC / C	Within +20/0% of initial value					
	Df (tan δ)	Shall be satisfied the voltage on " Standard list "					
	L.C.	6.3μA or 0.125CV whichever is greater					
Surge voltage	Appearance	There should be no significant abnormality.	As per 4.26JIS C 5101-1				
	L.C.	Less than 200% of initial value	As per 4.14JIS C 5101-3 Apply the specified surge voltage every 5±0.5 min.				
	ΔC / C	Within ±20% of initial value	for 30±5 s. each time in the atmospheric condition of 85±2°C. Repeat this procedure 1,000 times.				
	Df (tan δ)	Less than 200% of initial limit	After the specimens, leave it at room temperature for over 24h and then measure the sample.				
Loading at	Appearance	There should be no significant abnormality.	As per 4.23 JIS C 5101-1 As per 4.15 JIS C 5101-3 After applying the rated voltage for 2000+72/0 h without				
High temperature	L.C.	Less than 200% of initial limit					
	ΔC / C	Within ±20% of initial value	discontinuation via the serial resistance of $3\Omega$ or less at a temperature of $85\pm2^{\circ}$ C, leave the sample at room				
	Df (tan δ)	Less than 200% of initial limit	temperature / humidity for over 24h and measure the value.				
Terminal	Capacitance	The measured value should be stable.	As per 4.35 JIS C 5101-1				
strength	Appearance	There should be no significant abnormality.	As per 4.9 JIS C 5101-3 A force is applied to the terminal until it bends to 1mm and by a prescribed tool maintain the condition for 5s.  (See the figure below)  (Unit: mm)  F (Apply force)  thickness=1.6mm				

It	em	Performance	Test conditions (JIS C 5101-1 and JIS C 5101-3)			
Adhesiveness		The terminal should not come off.	As per 4.34 JIS C 5101-1 As per 4.8 JIS C 5101-3 Apply force of 5N in the two directions shown in the figure below for 10±1s after mounting the terminal on a circuit board.			
			Apply force a circuit board			
Dimension	S	Refer to "External dimensions"	Measure using a caliper of JIS B 7507 Class 2 or higher grade.			
Resistance to solvents		The indication should be clear	As per 4.32 JIS C 5101-1 As per 4.18 JIS C 5101-3 Dip in the isopropyl alcohol for 30±5s, at room temperature.			
Solderability		3/4 or more surface area of the solder coated terminal dipped in the soldering bath should be covered with the new solder.	As per 4.15.2 JIS C 5101-1 As per 4.7 JIS C 5101-3 Dip speed=25±2.5mm / s Pre-treatment (accelerated aging): Leave the sample on the boiling distilled water for 1 h. Solder temp.: 245±5°C Duration: 3±0.5s Solder: M705 Flux: Rosin 25% IPA 75%			
Vibration	Capacitance	Measure value should not fluctuate during the measurement.	As per 4.17 JIS C 5101-1 Frequency : 10 to 55 to 10Hz/min. Amplitude : 1.5mm			
	Appearance	There should be no significant abnormality.	Time: 2h each in X and Y directions  Mounting: The terminal is soldered on a print circuit boar			

## • Standard products list, TCT series

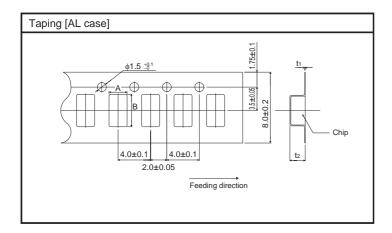
Part No.	Rated voltage 85°C	voltage voltage voltage Tolerance c		Leakage current 25°C		Df 120Hz (%)	Impedance 100kHz			
	(V)	(V)	(V)	(μF)	(μF) (%)		–55°C	25°C 85°C	125°C	(Ω)
TCT AL 0E 227□	2.5	1.6	3.3	220	±20	5.5	35	20	25	2.5
*TCT AL 0E 337□	2.5	1.6	3.3	330	±20	16.5	80	30	40	2.5
TCT AL 0G 107□	4	2.5	5.2	100	±20	4.0	35	20	25	3.0
TCT AL 0G 157 □	4	2.5	5.2	150	±20	6.0	35	20	25	2.7
TCT AL 0G 227 □	4	2.5	5.2	220	±20	20	35	20	25	2.5
TCT AL 0J 686 □	6.3	4	8.0	68	±20	4.3	35	20	25	4.0
TCT AL 0J 107 □	6.3	4	8.0	100	±20	6.3	34	18	24	3.0
TCT AL 1A 336 □	10	6.3	13	33	±20	3.3	30	15	20	4.0
TCT AL 1A 476 □	10	6.3	13	47	±20	4.7	35	20	25	4.0
TCT AL 1C 156□	16	10	20	15	±20	2.4	30	15	20	4.0
TCT AL 1C 226□	16	10	20	22	±20	3.6	35	20	25	4.0
TCT AL 1D 106□	20	13	26	10	±20	2.0	30	15	20	8.0
TCT AL 1E 475 □	25	16	32	4.7	±20	1.2	30	15	20	8.0
TCT AL 1E 685 □	25	16	32	6.8	±20	1.7	30	15	20	8.0
TCT AL 1V 225 □	35	22	45	2.2	±20	0.8	30	15	20	8.0
TCT AL 1V 335 □	35	22	45	3.3	±20	1.2	30	15	20	8.0

□=Tolerance (M : ±20%)

\*: Under development

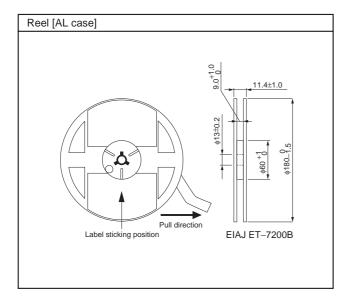
## Packaging specifications

Case code	A±0.1	B±0.1	t1±0.05	t2±0.1
AL	1.9	3.5	0.25	1.3



## Packaging style

Case code	Packaging	Packag	ging style	Symbol	Basic ordering units
AL case	Taping	plastic taping	∮180mm Reel	R	3,000pcs



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