

200mA / 30V Low V_{CE} (sat) Digital transistors

(with built-in resistors)

DTD713ZE / DTD713ZM

Applications

Inverter, Interface, Driver

Feature

- 1) VCE (sat) is lower than conventional products.
- Built-in bias resistors enable the configuration of an inverter circuit without connecting external input resistors (see equivalent circuit).
- 3) The bias resistors consist of thin-film resistors with complete isolation to allow negative biasing of the input. They also have the advantage of almost completely eliminating parasitic effects.
- 4) Only the on / off conditions need to be set for operation, making the device design easy.

Structure

NPN epitaxial plannar silicon transistor (Resistor built-in type)

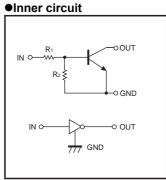
Packaging specifications

	Package	EMT3	VMT3
	Packaging type	Taping	Taping
	Code	TL	T2L
Part No.	Basic ordering unit (pieces)	3000	8000
DTD713ZE		0	-
DTD713ZM		-	0

•Absolute maximum ratings (Ta=25°C

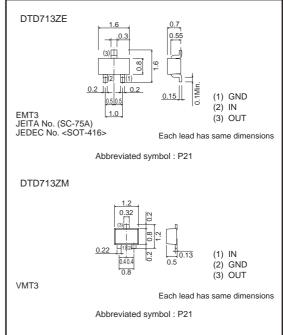
Parameter	Symbol	Limits	Unit
Farameter	Symbol	DTD713ZE DTD713ZN	
Supply voltage	Vcc	30	V
Input voltage	Vin	-5 to +10	V
Collector current *1	IC (max)	200	mA
Power dissipation *2	PD	150	mW
Junction temperature	Tj	150	°C
Storage temperature	Tstg	-55 to +150	°C

*1 Characteristics of built-in transistor.*2 Each terminal mounted on a recommended land.







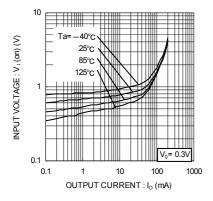


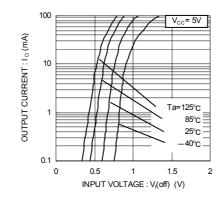
•Electrical characteristics (Ta=25°C)

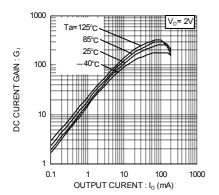
Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Innutualtara	VI(off)	-	-	0.3	N	Vcc=5V, Io=100µA
Input voltage	VI(on)	2.5	-	-	V Vo=0.3V, Io=20mA	
Output voltage	VO(on)	-	70	300	mV	lo/l=50mA / 2.5mA
Input current	h	-	-	6.4	mA	Vi= 5V
Output current	IO(off)	-	-	0.5	μΑ	Vcc=30V, VI=0V
DC current gain	Gı	140	-	-	-	Vo=2V, Io=100mA
Transition frequency *	fт	-	260	_	MHz	Vce=10V, Ie= -5mA, f=100MHz
Input resistance	R1	0.7	1.0	1.3	kΩ	-
Resistance ratio	R2/R1	8.0	10	12	-	_

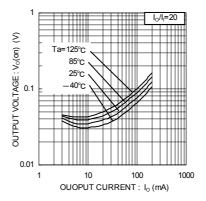
* Characteristics of built-in transistor.

•Electrical characteristics curves









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