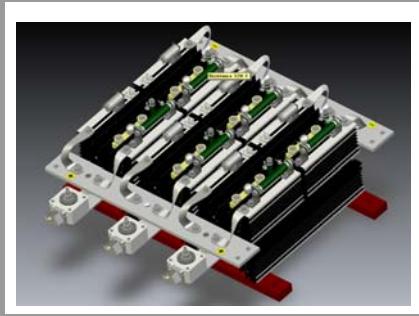


SKS 700N B6C 470 V16



SEMISTACK Thyristor

Three-phase controlled bridge rectifier

SKS 700N B6C 470 V16

Preliminary Data

Features

- P11 heatsink
- Presspack stack with double side cooling
- Natural convection cooled
- Black anodized heatsink for maximum radiation

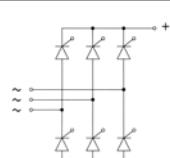
Typical Applications

- Industrial rectifiers
- Softstarters
- DC motor control
- AC controller

Options

No. 08755015

Characteristics		Symbol	Conditions	min.	typ.	max.	Unit
Electrical Data							
I _d	T _{amb} = 45°C		no overload			700	A
			150 % overload, 60s every 10 min.		604	907	A
			200 % overload, 60s every 10 min.		549	1099	A
V _{CES}					1600		V
V _{DC}			DC voltage applied to the capacitor bank			670	V
V _{AC}			network voltage (line side), -20% / +15%			500	V
V _{isol}			50 Hz / 1 min.		2500		V
i ² t	T _{vj} = 25°C					1125	kA ² s
	T _{vj} = 125°C					845	kA ² s
(di/dt) _{cr}	T _{vj} = 125°C					125	A/μs
(dv/dt) _{cr}						1000	V/μs
V _{GT}				3			V
I _{GT}				200			mA
P _{tot}	T _{amb} = 45°C				1787		W
R _{thja}	per component				0.30		K/W
T _{vj}				-40		125	°C
Fuse	Type SU (690V) Size 32				630		A
RC	Resistance (80W)				33		Ω
RC	Capacitance (900V _{dc} / 630V _{ac})				0.47		μF
Thermal trip	normally closed				95		°C
Mechanical Data							
dv/dt _{AIR}							m ³ /h
W	approx. total weight			56			kg
Size	Width x Depth x Height (with fan)			700	650	311	mm
T _{stg}				5		60	°C
T _{amb}				5		60	°C
Altitude	Installation height w/o derating					1000	m
Protection						IP00	
Pollution	EN 50178				2		
Fan Data							
Fan	included in the stack (NO)						
Type							
V _{FAN}	Fan voltage						V
f _{FAN}	Fan frequency						Hz
I _{FAN}	Fan current						A
P _{FAN}	Fan power						W



B6C

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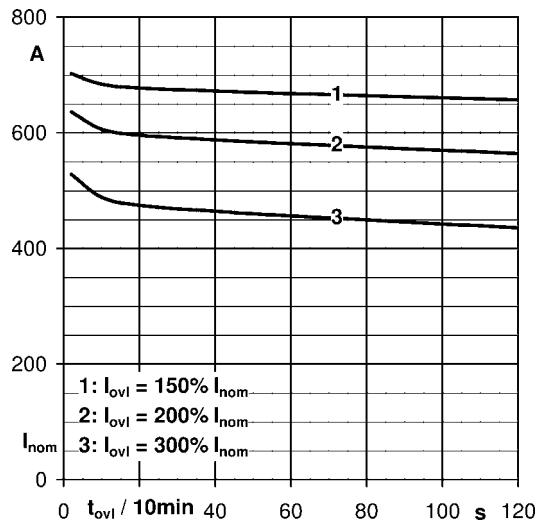


Fig. 1 Maximal overload factor vs nom current and ovl duration, Tamb = 35°C

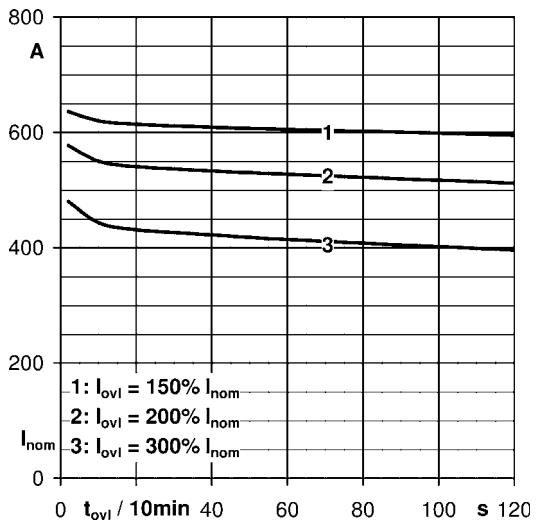


Fig. 2 Maximal overload factor vs nom current and ovl duration, Tamb = 45°C

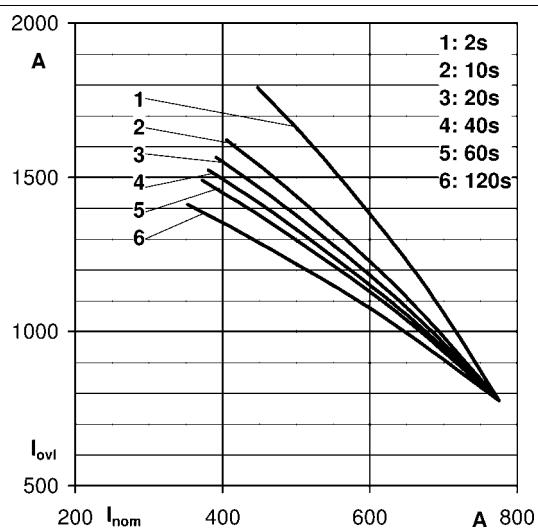


Fig. 3 Maximal overload duration vs nom current and ovl factor, Tamb = 35°C

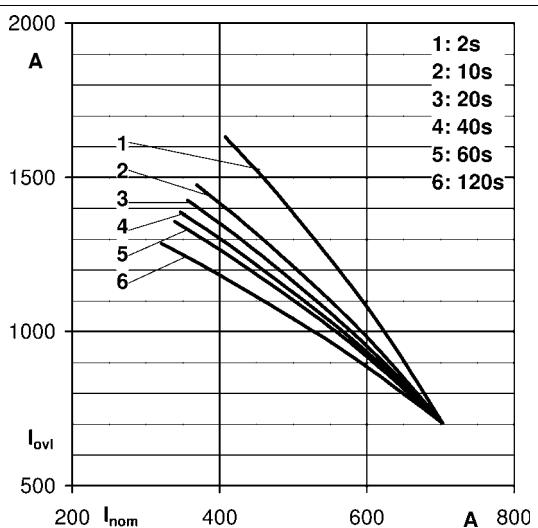


Fig. 4 Maximal overload duration vs nom current and ovl factor, Tamb = 45°C

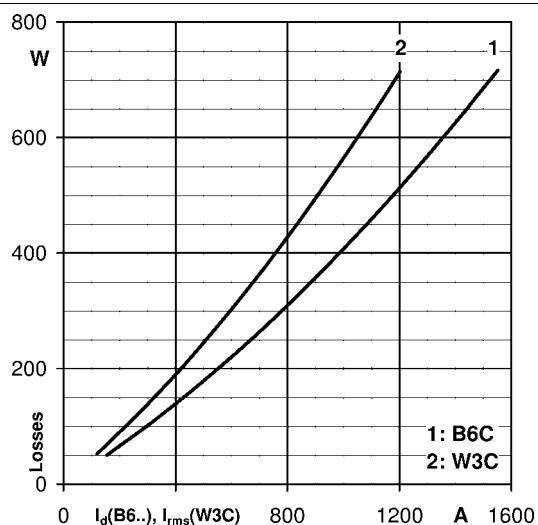


Fig. 5 Power losses

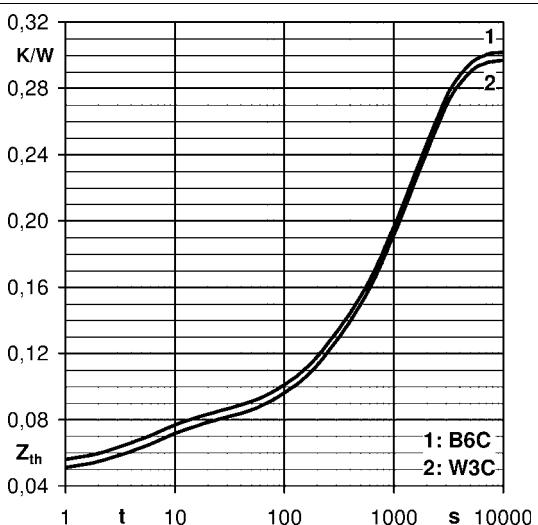
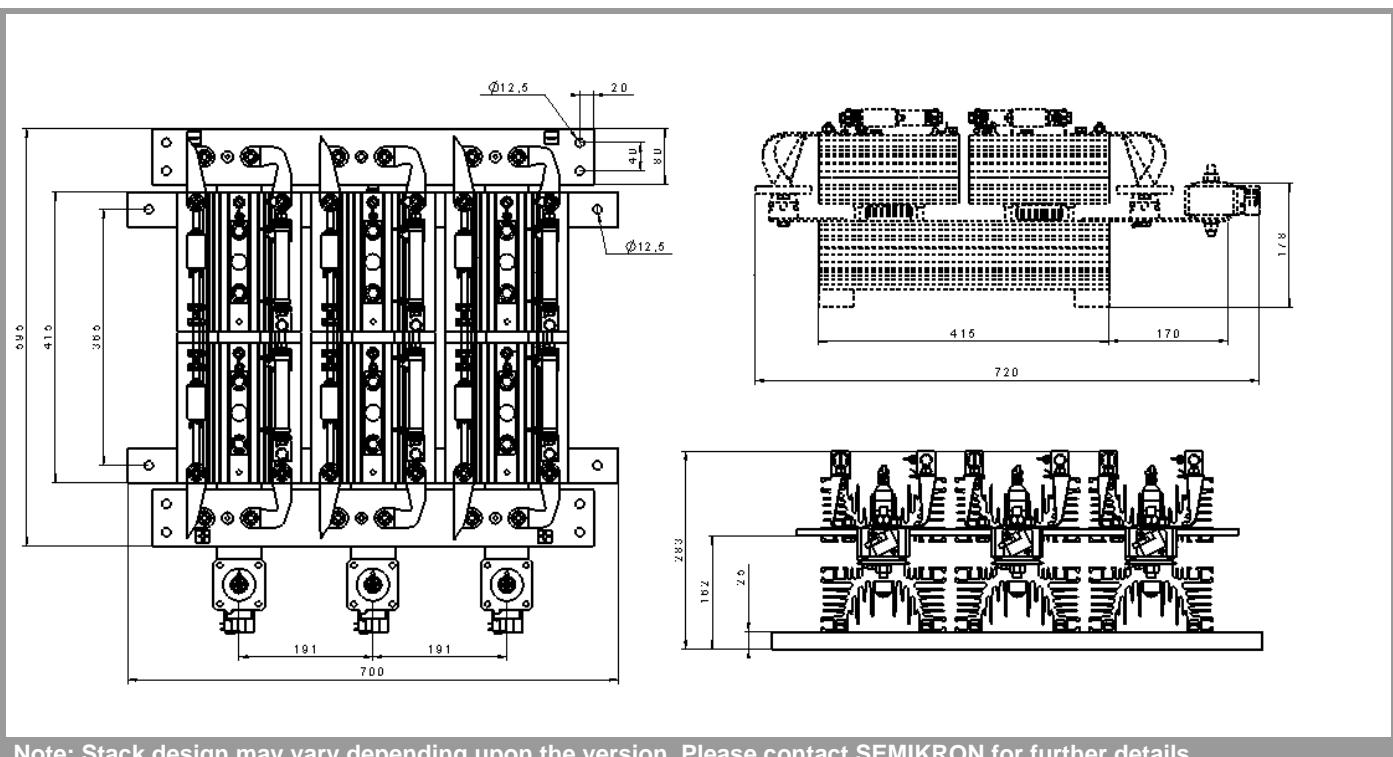


Fig. 6 Thermal impedance $Z_{th}(j-a)$

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Note: Stack design may vary depending upon the version. Please contact SEMIKRON for further details.

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