

Single P-channel MOSFET

ELM37401FA-S

■ General description

ELM37401FA-S uses advanced trench technology to provide excellent $R_{ds(on)}$, low gate charge and low gate resistance.

■ Features

- $V_{ds} = -20V$
- $I_d = -1.4A$
- $R_{ds(on)} < 115m\Omega$ ($V_{gs} = -4.5V$)
- $R_{ds(on)} < 215m\Omega$ ($V_{gs} = -2.5V$)
- $R_{ds(on)} < 350m\Omega$ ($V_{gs} = -1.8V$)

■ Maximum absolute ratings

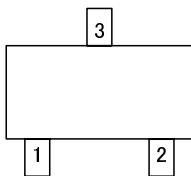
Parameter	Symbol	Limit	Unit	Note
Drain-source voltage	V_{ds}	-20	V	
Gate-source voltage	V_{gs}	± 12	V	
Continuous drain current Ta=25°C	I_d	-1.4	A	
Ta=70°C		-1.1		
Pulsed drain current	I_{dm}	-10	A	3
Power dissipation Ta=25°C	P_d	0.35	W	
Ta=70°C		0.22		
Junction and storage temperature range	T_j, T_{stg}	-55 to 150	°C	

■ Thermal characteristics

Parameter		Symbol	Typ.	Max.	Unit	Note
Maximum junction-to-ambient	t≤5s	$R_{\theta ja}$		360	°C/W	
Maximum junction-to-ambient	Steady-state			425	°C/W	
Maximum junction-to-lead	Steady-state	$R_{\theta jl}$		320	°C/W	

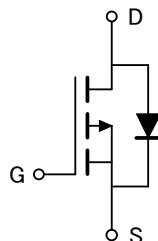
■ Pin configuration

SC-70 (TOP VIEW)



Pin No.	Pin name
1	GATE
2	SOURCE
3	DRAIN

■ Circuit



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■ Electrical characteristics

$T_a=25^\circ C$

Parameter	Symbol	Condition	Min.	Typ.	Max.	Unit	Note
STATIC PARAMETERS							
Drain-source breakdown voltage	BVdss	Vgs=0V, Id=-250 μA	-20			V	
Zero gate voltage drain current	Idss	Vds=-16V, Vgs=0V Vds=-16V, Vgs=0V, Tj=125°C			-1 -10	μA	
Gate-body leakage current	Igss	Vds=0V, Vgs=±12V			±100	nA	
Gate threshold voltage	Vgs(th)	Vds=Vgs, Id=-250 μA	-0.4	-0.8	-1.2	V	
On state drain current	Id(on)	Vgs=-4.5V, Vds=-5V	-10			A	1
Static drain-source on-resistance	Rds(on)	Vgs=-4.5V, Id=-1.4A		98	115	mΩ	1
		Vgs=-2.5V, Id=-1.2A		150	215	mΩ	
		Vgs=-1.8V, Id=-1A		250	350	mΩ	
Forward transconductance	Gfs	Vds=-5V, Id=-1.4A		7		S	1
Diode forward voltage	Vsd	Is=-1A, Vgs=0V			-1.0	V	1
Max. body-diode continuous current	Is				-0.7	A	
Pulsed body-diode current	Ism				-1.4	A	3
DYNAMIC PARAMETERS							
Input capacitance	Ciss	Vgs=0V, Vds=-10V, f=1MHz		476		pF	
Output capacitance	Coss			260		pF	
Reverse transfer capacitance	Crss			105		pF	
SWITCHING PARAMETERS							
Total gate charge	Qg	Vgs=-4.5V, Vds=-10V Id=-1.4A		5.63	8.45	nC	2
Gate-source charge	Qgs			2.35		nC	2
Gate-drain charge	Qgd			1.47		nC	2
Turn-on delay time	td(on)	Vgs=-4.5V, Vds=-10V Id ≈ -1A, Rgen=6Ω		11	22	ns	2
Turn-on rise time	tr			32	55	ns	2
Turn-off delay time	td(off)			38	68	ns	2
Turn-off fall time	tf			32	55	ns	2

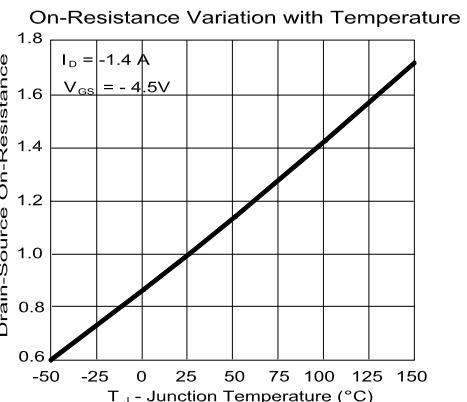
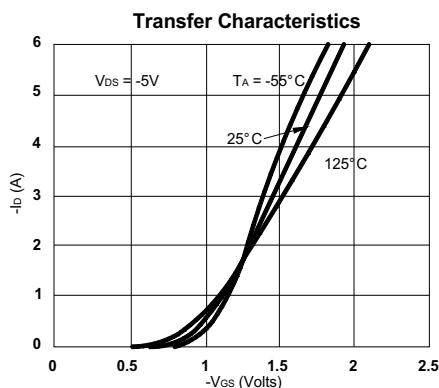
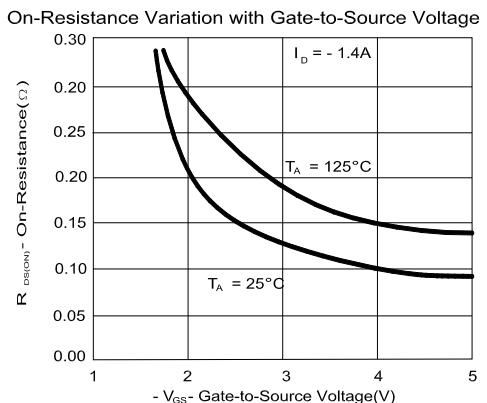
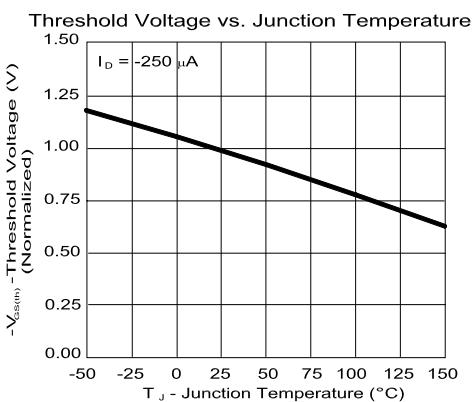
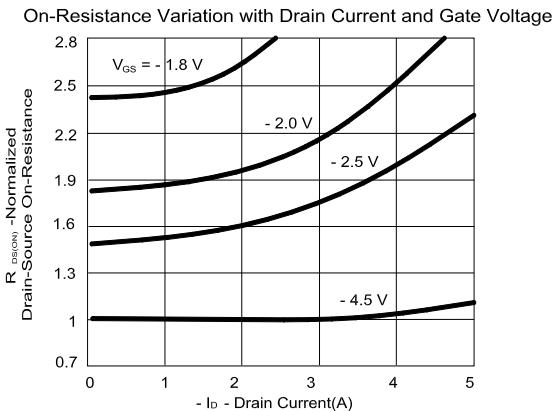
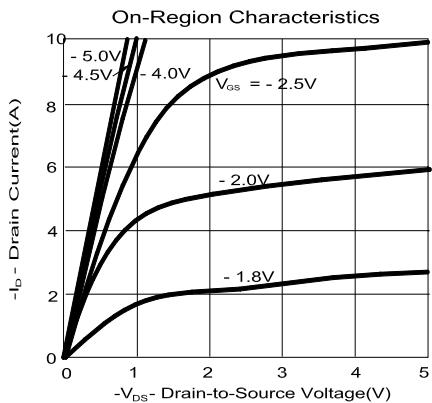
NOTE :

1. Pulsed width $\leq 300 \mu sec$ and Duty cycle $\leq 2\%$.
2. Independent of operating temperature.
3. Pulsed width limited by maximum junction temperature.
4. Duty cycle $\leq 1\%$.

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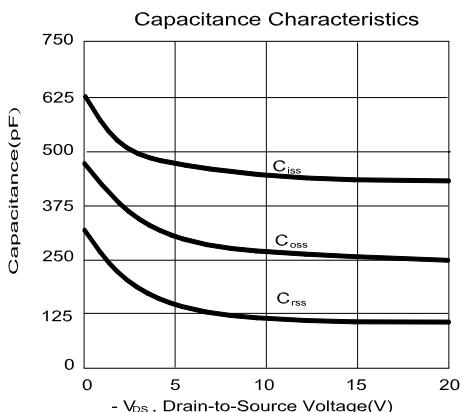
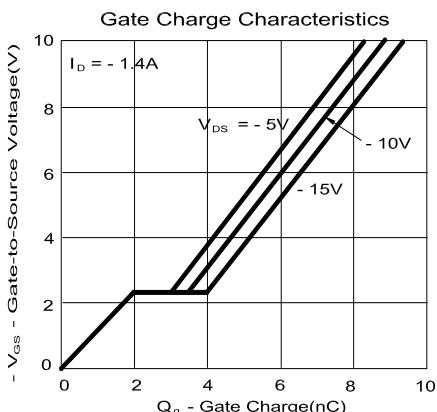
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■ Typical electrical and thermal characteristics

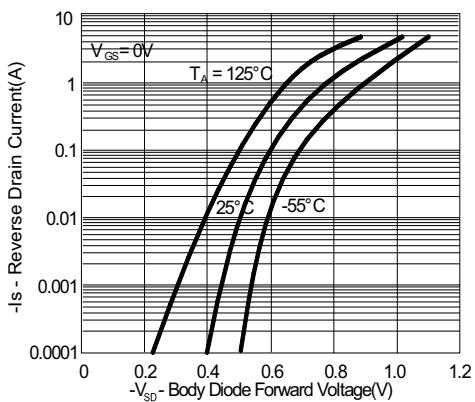


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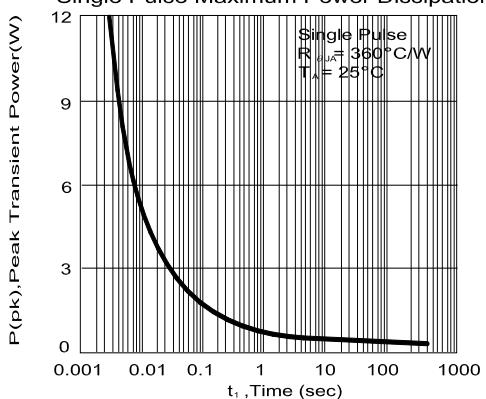
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Body Diode Forward Voltage Variation with Source Current and Temperature



Single Pulse Maximum Power Dissipation



Transient Thermal Response Curve

