

**KKA1519B****2 x 6 Watt Stereo Power Amplifier**

The KKA1519B is an integrated class-B dual output amplifier in a 9-lead single in-line (SIL) plastic medium power package. The device is primarily developed for car radio applications.

**FEATURES**

- Requires very few external components
- Thermally protected
- High output power
- Reverse polarity safe
- Fixed gain
- Compatible with TDA1517 (except gain)
- Good ripple rejection
- No switch-on/switch-off plop
- Mute/stand-by switch
- Protected against electrostatic discharge
- Load dump protection
- AC and DC short-circuit-safe to ground and V<sub>p</sub>
- Capability to handle high energy on outputs (V<sub>p</sub> = 0 V)

**QUICK REFERENCE DATA**

PARAMETER	SYMBOL	CONDITIONS	MIN	TYP.	MAX.	UNIT
Supply voltage range						
operating	V <sub>p</sub>		6,0	14,4	18,0	V
non-operating	V <sub>p</sub>		-	-	30	V
load dump protected	V <sub>p</sub>		-	-	45	V
Repetitive peak output current	I <sub>ORM</sub>		-	-	2,5	A
Total quiescent current	I <sub>tot</sub>			40	80	mA
Stand-by current	I <sub>sb</sub>			0,1	100	mA
Switch-on current	I <sub>sw</sub>				40	mA
Input impedance	/Z <sub>1</sub> /		50			kΩ
Output power		THD= 0,5%; 4 Ω		5		W
		THD= 10%; 4 Ω		6		W
Channel separation	α		40			dB
Noise output voltage	V <sub>no(rms)</sub>				150	µV
Supply voltage ripple rejection	SVRR	f=100Hz	40			dB
	SVRR	f=1kHz to 10 kHz	48			dB
Crystal temperature	T <sub>c</sub>				150	°C

**PACKAGE OUTLINE:** 9-lead SIL-bent-to-DIL; plastic (SOT110B).

**PAD DESCRIPTION**

1	INV1	non-inverting input 1
2	GND1	ground (signal)
3	SVRR	supply voltage ripple rejection
4	OUT1	output 1
5	GND2	ground (substrate)
6	OUT2	output 2
7	V <sub>p</sub>	supply voltage
8	M/SS	mute/stand-by switch
9	INV2	non-inverting input 2

**DC ELECTRICAL CHARACTERISTICS** (note 1)

V<sub>p</sub> = 14,4 V; Tamb = 25 °C; unless otherwise specified

PARAMETER	CONDITIONS	SYMBOL	MIN	TYP.	MAX.	UNIT
<b>Supply</b>						
Supply voltage range	note 2	V <sub>p</sub>	6,0	14,4 40 6,95	18,0	V
Quiescent current		I <sub>P</sub>	-	80	-	mA
DC output voltage	note 3	V <sub>O</sub>	-	-	-	V
<b>Mute/stand-by switch</b>						
Switch-on voltage level	see Fig.3	V <sub>ON</sub>	8,5	-	-	V
<b>Mute condition</b>						
Output signal in mute position	V <sub>I</sub> = 1 V (max.); f = 20 Hz to 15 kHz	V <sub>mute</sub>	3,3	-	6,4	V
		V <sub>O</sub>	-	-	20	mV
<b>Stand-by condition</b>						
DC current in stand-by condition		V <sub>sb</sub>	0	12	2 100 40	V
Switch-on current		I <sub>sb</sub>	-	-	-	μA
		I <sub>sw</sub>	-	-	-	μA

**AC CHARACTERISTICS** (note 1)

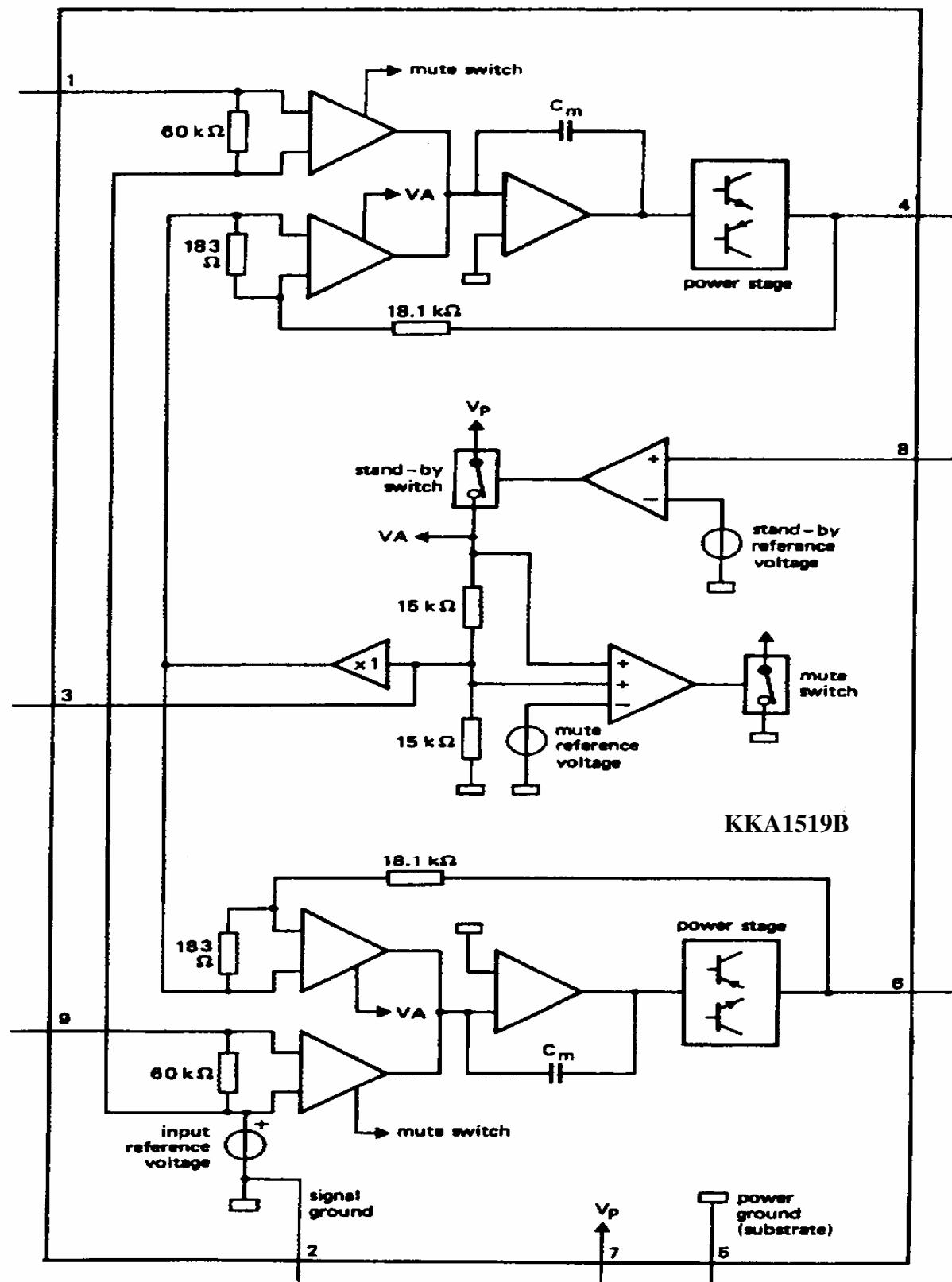
V<sub>p</sub>=14,4V; R<sub>L</sub>=4Ω; f=1kHz; Tamb=25°C unless otherwise specified

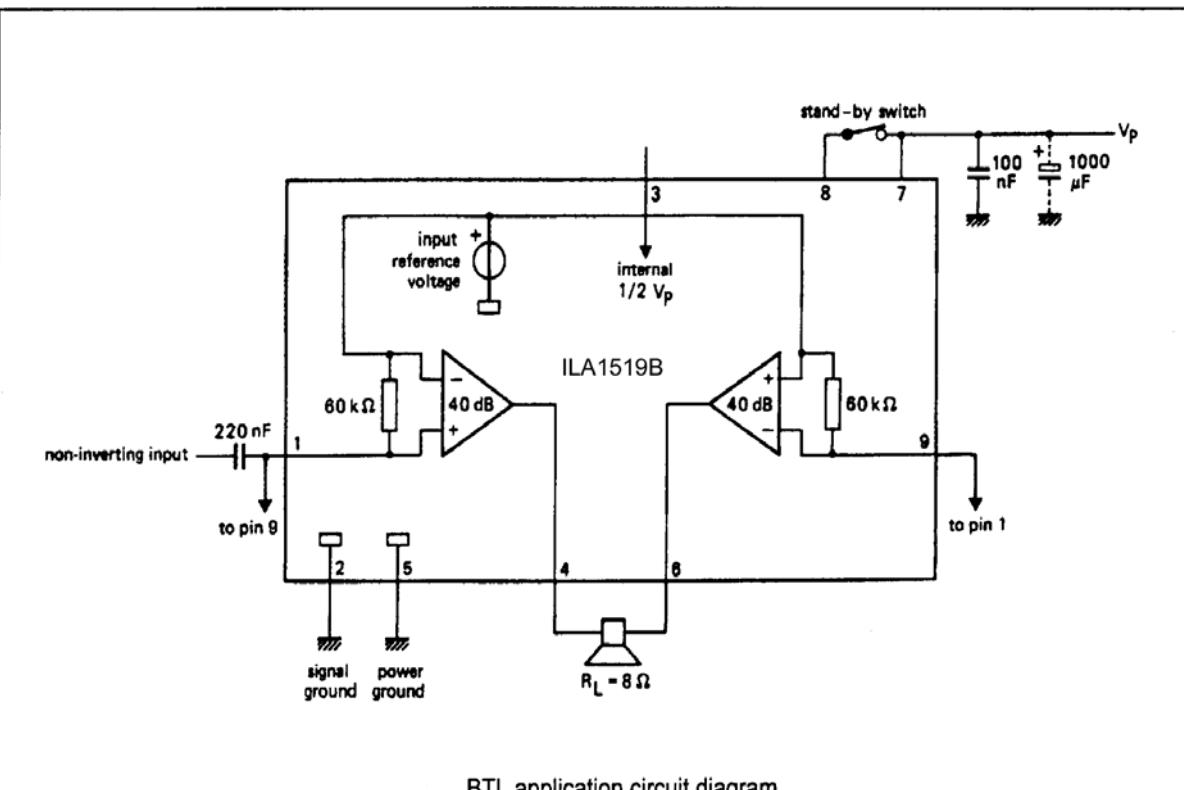
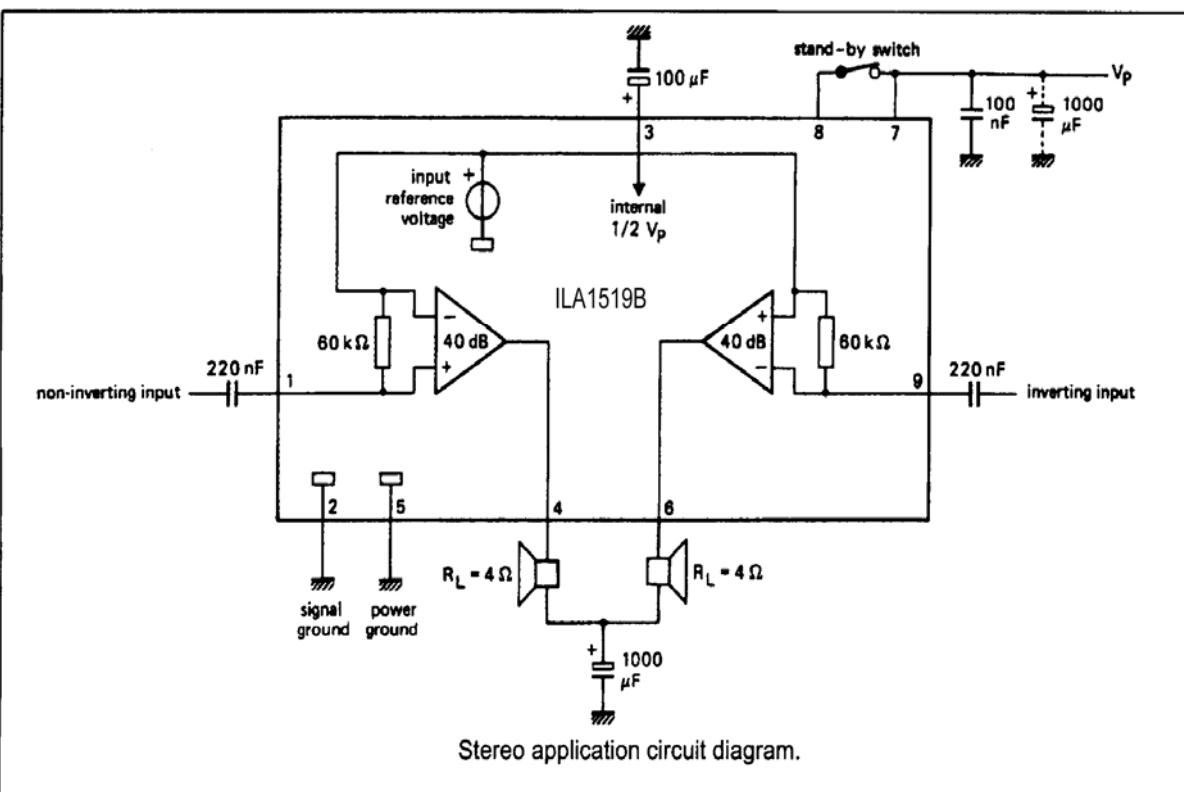
PARAMETER	CONDITIONS	SYMBOL	MIN.	TYP.	MAX.	UNIT
Output power	note 4; THD = 0,5% THD = 10%	P <sub>O</sub>	4	5	-	W
Total harmonic distortion	P <sub>O</sub> =1W	THD	5,5	6,0 0,1	-	W
Low frequency roll-off	note 5; -3 dB	f <sub>L</sub>	-	45	-	Hz
High frequency roll-off	-1 dB	f <sub>H</sub>	20	40	-	kHz
Closed loop voltage gain		G <sub>V</sub>	39	-	41	dB
Supply voltage ripple rejection	note 6					
ON						
ON	f = 100 Hz	SVRR	40	-	-	dB
	f = 10 Hz to 10 kHz	SVRR	48	-	-	dB
mute		SVRR	48	-	-	dB
stand-by		SVRR	80	60	-	dB
Input impedance		I <sub>Zil</sub>	50	-	75	kΩ
Noise output voltage	note 7;			150	-	
ON	R <sub>S</sub> =0Ω	V <sub>no(rms)</sub>	-	250	-	mV
ON	R <sub>S</sub> = 10 kΩ	V <sub>no(rms)</sub>	-	120	500	mV
mute	note 8	V <sub>no(rms)</sub> a	-	-	-	mV
Channel separation	R <sub>S</sub> = 10 kΩ	I <sub>DGvl</sub>	40	0,1	-	dB
Channel balance			-	-	1	dB

**Notes to the characteristics**

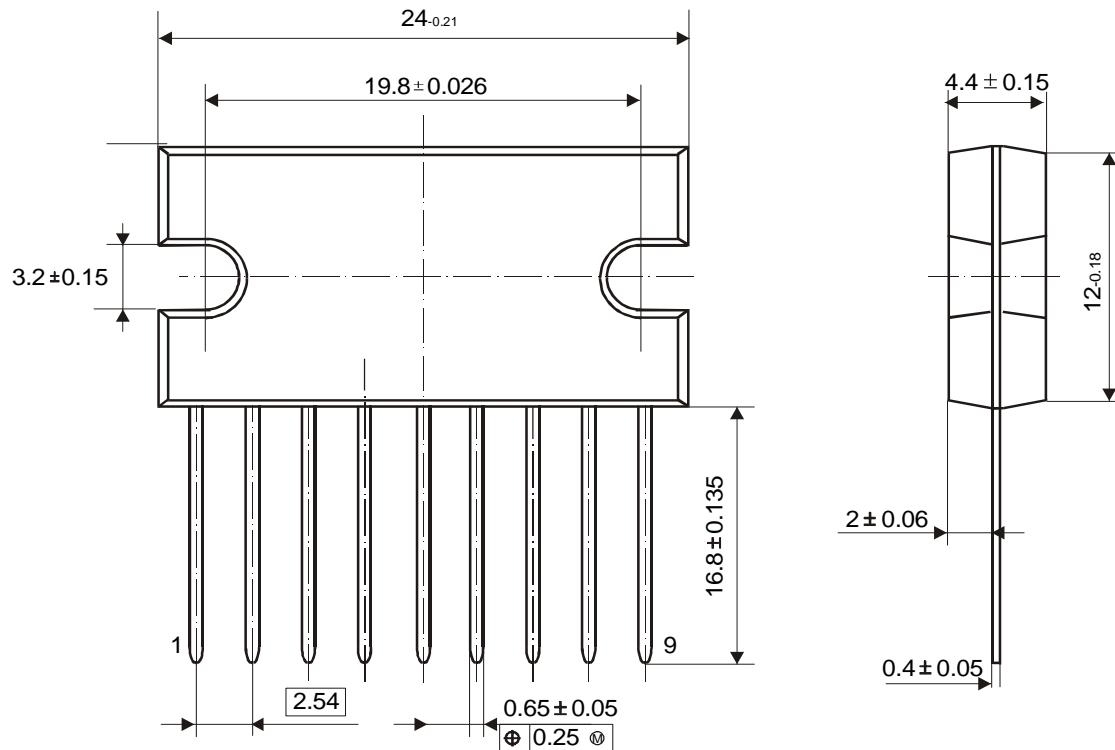
1. All characteristics are measured using the circuit shown in Fig. 4.
2. The circuit is DC adjusted at V<sub>p</sub>= 6V to 18V and AC operating at V<sub>p</sub>= 8,5V to 18 V.
3. At 18 V < V<sub>p</sub> < 30 V the DC output voltage < V<sub>p</sub>/2.
4. Output power is measured directly at the output pins of the IC.
5. Frequency response externally fixed.
6. Ripple rejection measured at the output with a source impedance of 0 ^ (maximum ripple amplitude of 2 V) and a frequency between 100 Hz and 10 kHz.
7. Noise voltage measured in a bandwidth of 20 Hz to 20 kHz.
8. Noise output voltage independent of R^ (V<sub>j</sub> = 0 V).

**BLOCK DIAGRAM**

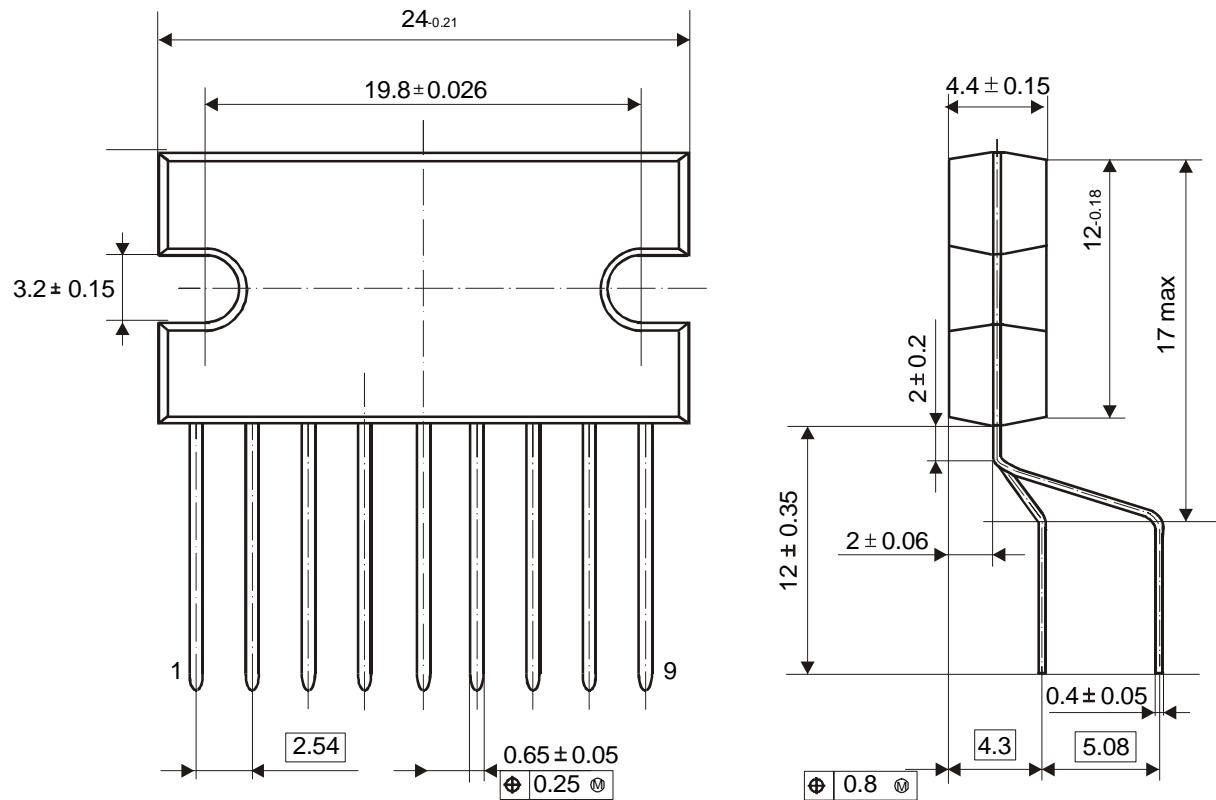




- 9-Pin Plastic Power Single-in-Line (SIL-9MPF, SOT 131-2)



- 9-Pin Plas (SIL-9P, SOT 157-2)



- 9-Pin Plastic Power Single-in-Line (SIL-9MPF, SOT 110-1)

