

# **HD74LV139A**

# Dual 2-to-4-line Decoders / Demultiplexers

REJ03D0385-0100 Rev.1.00 Aug. 24, 2004

### **Description**

The HD74LV139A has two independent two-to-four-line decoders each with a single active low enable input in a 16 pin package. Data on the select inputs cause one of the four normally high outputs to go low. Low voltage and high-speed operation is suitable at the battery drive product (note type personal computer) and low power consumption extends the life of a battery for long time operation.

### **Features**

- $V_{CC} = 2.0 \text{ V to } 5.5 \text{ V}$
- All inputs  $V_{IH}$  (Max.) = 5.5 V (@ $V_{CC}$  = 0 V to 5.5 V)
- All outputs  $V_0$  (Max.) = 5.5 V (@ $V_{CC}$  = 0 V)
- Typical  $V_{OL}$  ground bounce < 0.8 V (@ $V_{CC}$  = 3.3 V, Ta = 25°C)
- Typical  $V_{OH}$  undershoot > 2.3 V (@ $V_{CC}$  = 3.3 V, Ta = 25°C)
- High output current  $\pm 6$  mA (@V<sub>CC</sub> = 3.0 V to 3.6 V),  $\pm 12$  mA (@V<sub>CC</sub> = 4.5 V to 5.5 V)
- Ordering Information

Part Name	Package Type	Package Code	Package Abbreviation	Taping Abbreviation (Quantity)
HD74LV139AFPEL	SOP-16 pin (JEITA)	FP-16DAV	FP	EL (2,000 pcs/reel)
HD74LV139ATELL	TSSOP-16 pin	TTP-16DAV	Т	ELL (2,000 pcs/reel)

Note: Please consult the sales office for the above package availability.

### **Function Table**

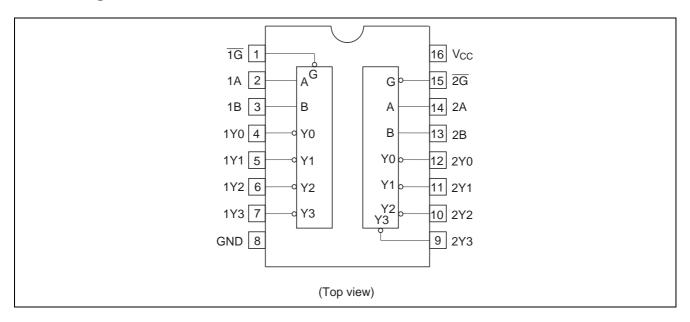
	Input								
Enable Select			Outputs						
G	В	А	Y0	Y1	Y2	Y3			
Н	X	X	Н	Н	Н	Н			
L	L	L	L	Н	Н	Н			
L	L	Н	Н	L	Н	Н			
L	Н	L	Н	Н	L	Н			
L	Н	Н	Н	Н	Н	L			

H: High level

L: Low level

X: Immaterial

### **Pin Arrangement**



### **Absolute Maximum Ratings**

Item	Symbol	Ratings	Unit	Conditions
Supply voltage range	V <sub>CC</sub>	-0.5 to 7.0	V	
Input voltage range*1	VI	-0.5 to 7.0	V	
Output voltage range*1, 2	Vo	$-0.5$ to $V_{CC} + 0.5$	V	Output: H or L
		-0.5 to 7.0		V <sub>CC</sub> : OFF
Input clamp current	I <sub>IK</sub>	-20	mA	V <sub>I</sub> < 0
Output clamp current	I <sub>OK</sub>	±50	mA	$V_O < 0$ or $V_O > V_{CC}$
Continuous output current	Io	±25	mA	$V_O = 0$ to $V_{CC}$
Continuous current through V <sub>CC</sub> or GND	I <sub>CC</sub> or I <sub>GND</sub>	±50	mA	
Maximum power dissipation at	P <sub>T</sub>	785	mW	SOP
Ta = $25^{\circ}$ C (in still air)* <sup>3</sup>		500		TSSOP
Storage temperature	Tstg	-65 to 150	°C	

Notes: The absolute maximum ratings are values, which must not individually be exceeded, and furthermore, no two of which may be realized at the same time.

- 1. The input and output voltage ratings may be exceeded if the input and output clamp-current ratings are observed.
- 2. This value is limited to 5.5 V maximum.
- 3. The maximum package power dissipation was calculated using a junction temperature of 150°C.

## **Recommended Operating Conditions**

Item	Symbol	Min	Max	Unit	Conditions
Supply voltage range	V <sub>CC</sub>	2.0	5.5	V	
Input voltage range	VI	0	5.5	V	
Output voltage range	Vo	0	Vcc	V	
Output current	I <sub>OH</sub>	_	-50	μА	V <sub>CC</sub> = 2.0 V
		_	-2	mA	$V_{CC} = 2.3 \text{ to } 2.7 \text{ V}$
		_	-6		$V_{CC} = 3.0 \text{ to } 3.6 \text{ V}$
		_	-12		V <sub>CC</sub> = 4.5 to 5.5 V
	I <sub>OL</sub>	_	50	μА	V <sub>CC</sub> = 2.0 V
		_	2	mA	V <sub>CC</sub> = 2.3 to 2.7 V
		_	6		$V_{CC} = 3.0 \text{ to } 3.6 \text{ V}$
		_	12		V <sub>CC</sub> = 4.5 to 5.5 V
Input transition rise or fall rate	Δt /Δν	0	200	ns/V	V <sub>CC</sub> = 2.3 to 2.7 V
		0	100		V <sub>CC</sub> = 3.0 to 3.6 V
		0	20	1	V <sub>CC</sub> = 4.5 to 5.5 V
Operating free-air temperature	Та	-40	85	°C	

Note: Unused or floating inputs must be held high or low.

### **DC Electrical Characteristics**

Ta = -40 to  $85^{\circ}C$ 

Item	Symbol	V <sub>CC</sub> (V)*	Min	Тур	Max	Unit	Test Conditions
Input voltage	V <sub>IH</sub>	2.0	1.5	_	_	V	
		2.3 to 2.7	V <sub>CC</sub> × 0.7	_	_		
		3.0 to 3.6	V <sub>CC</sub> × 0.7	_	_		
		4.5 to 5.5	V <sub>CC</sub> × 0.7	_	_		
	V <sub>IL</sub>	2.0	_	_	0.5		
		2.3 to 2.7	_	_	V <sub>CC</sub> × 0.3		
		3.0 to 3.6	_	_	V <sub>CC</sub> × 0.3		
		4.5 to 5.5	_	_	V <sub>CC</sub> × 0.3		
Output voltage	V <sub>OH</sub>	Min to Max	V <sub>CC</sub> - 0.1	_	_	V	I <sub>OH</sub> = -50 μA
		2.3	2.0	_	_		I <sub>OH</sub> = -2 mA
		3.0	2.48	_	_		I <sub>OH</sub> = -6 mA
		4.5	3.8	_	_		I <sub>OH</sub> = -12 mA
	V <sub>OL</sub>	Min to Max	_	_	0.1		I <sub>OL</sub> = 50 μA
		2.3	_	_	0.4		I <sub>OL</sub> = 2 mA
		3.0	_	_	0.44		I <sub>OL</sub> = 6 mA
		4.5	_	_	0.55		I <sub>OL</sub> = 12 mA
Input current	I <sub>IN</sub>	0 to 5.5	_	_	±1	μΑ	V <sub>IN</sub> = 5.5 V or GND
Quiescent supply current	Icc	5.5	_		20	μА	$V_{IN} = V_{CC}$ or GND, $I_O = 0$
Output leakage current	loff	0	_	-	5	μА	$V_1 \text{ or } V_0 = 0 \text{ V to } 5.5 \text{ V}$
Input capacitance	C <sub>IN</sub>	3.3	_	3.3	_	pF	$V_I = V_{CC}$ or GND

Note: For conditions shown as Min or Max, use the appropriate values under recommended operating conditions.

# **Switching Characteristics**

 $V_{CC}=2.5\pm0.2~V$ 

		Ta = 25°C		Ta = -40	Ta = -40 to 85°C		Test	FROM	то	
Item	Symbol	Min	Тур	Max	Min	Max	Unit	Conditions	(Input)	(Output)
Propagation	t <sub>PLH</sub> /t <sub>PHL</sub>	_	7.5	17.6	1.0	21.0	ns	$C_L = 15 pF$	A or B	Υ
delay time		_	10.5	22.5	1.0	26.5		$C_L = 50 pF$		
		_	7.5	15.8	1.0	19.0		C <sub>L</sub> = 15 pF	G	]
		_	10.0	20.2	1.0	24.0		C <sub>L</sub> = 50 pF		

 $V_{CC} = 3.3 \pm 0.3 \text{ V}$ 

		Т	a = 25°	С	Ta = -40	Ta = -40 to 85°C		Test	FROM	то
Item	Symbol	Min	Тур	Max	Min	Max	Unit	Conditions	(Input)	(Output)
Propagation	t <sub>PLH</sub> /t <sub>PHL</sub>	_	5.5	11.0	1.0	13.0	ns	C <sub>L</sub> = 15 pF	A or B	Υ
delay time		_	7.5	14.5	1.0	16.5		$C_L = 50 pF$		
		_	5.5	9.2	1.0	11.0		$C_L = 15 pF$	G	
		_	7.0	12.7	1.0	14.5		C <sub>L</sub> = 50 pF		

 $V_{CC} = 5.0 \pm 0.5~V$ 

		Т	a = 25°	С	Ta = -40 to 85°C			Test	FROM	то
Item	Symbol	Min	Тур	Max	Min	Max	Unit	Conditions	(Input)	(Output)
Propagation	t <sub>PLH</sub> /t <sub>PHL</sub>	_	4.0	7.2	1.0	8.5	ns	$C_L = 15 pF$	A or B	Υ
delay time		_	5.5	9.2	1.0	10.5		C <sub>L</sub> = 50 pF		
		_	4.0	6.3	1.0	7.5		C <sub>L</sub> = 15 pF	G	
		_	5.5	8.3	1.0	9.5		C <sub>L</sub> = 50 pF		

# **Operating Characteristics**

 $C_L = 50 \ pF$ 

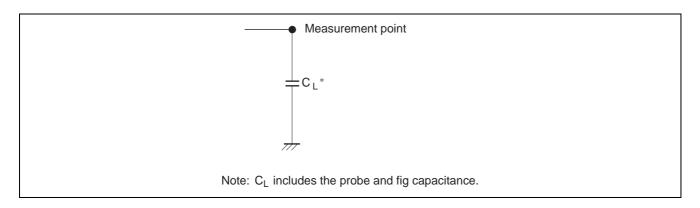
				Ta = 25°C			
Item	Symbol	V <sub>CC</sub> (V)	Min	Тур	Max	Unit	Test Conditions
Power dissipation capacitance	C <sub>PD</sub>	3.3	_	17.3	_	pF	f = 10 MHz
		5.0	_	18.2	_		

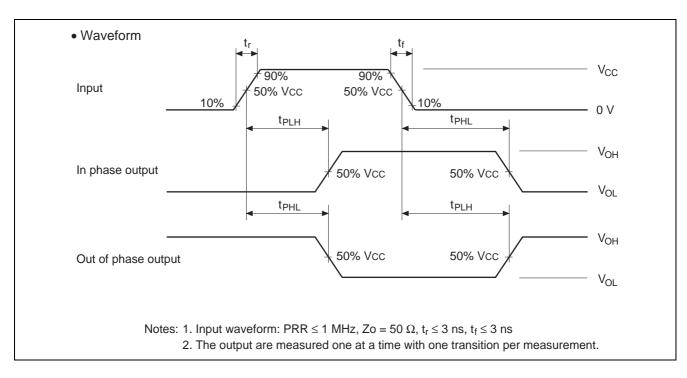
### **Noise Characteristics**

 $C_L = 50 \text{ pF}$ 

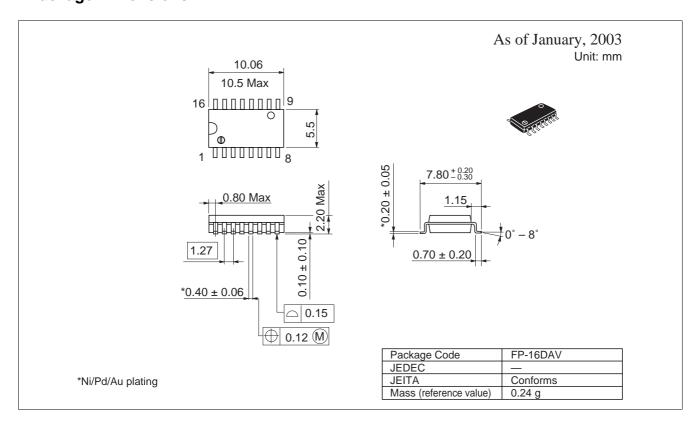
			Ta = 25°C				
Item	Symbol	V <sub>CC</sub> (V)	Min	Тур	Max	Unit	Test Conditions
Quiet output, maximum dynamic V <sub>OL</sub>	V <sub>OL (P)</sub>	3.3	_	0.3	0.8	٧	
Quiet output, minimum dynamic V <sub>OL</sub>	V <sub>OL (V)</sub>	3.3	_	-0.2	-0.8	V	
Quiet output, minimum dynamic V <sub>OH</sub>	V <sub>OH (V)</sub>	3.3	_	3.0	_	٧	
High-level dynamic input voltage	V <sub>IH (D)</sub>	3.3	2.31	_	_	V	
Low-level dynamic input voltage	V <sub>IL (D)</sub>	3.3	_	_	0.99	V	

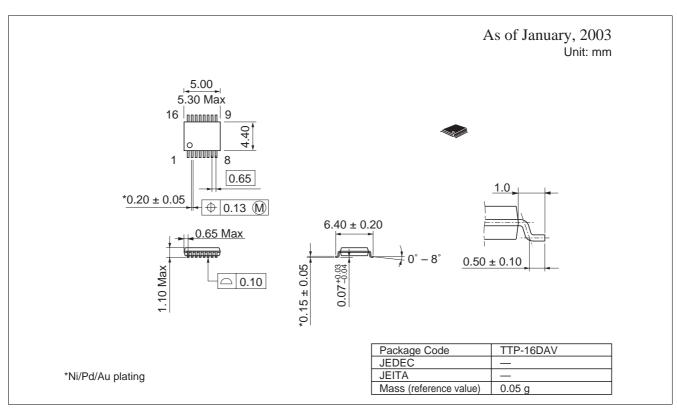
### **Test Circuit**





### **Package Dimensions**





Renesas Technology Corp. Sales Strategic Planning Div. Nippon Bldg., 2-6-2, Ohte-machi, Chiyoda-ku, Tokyo 100-0004, Japan

Keep safety first in your circuit designs!

1. Renesas Technology Corp. puts the maximum effort into making semiconductor products better and more reliable, but there is always the possibility that trouble may occur with them. Trouble with semiconductors may lead to personal injury, fire or property damage.

Remember to give due consideration to safety when making your circuit designs, with appropriate measures such as (i) placement of substitutive, auxiliary circuits, (ii) use of nonflammable material or (iii) prevention against any malfunction or mishap.

- Notes regarding these materials

  1. These materials are intended as a reference to assist our customers in the selection of the Renesas Technology Corp. product best suited to the customer's application; they do not convey any license under any intellectual property rights, or any other rights, belonging to Renesas Technology Corp. a third party.

  2. Renesas Technology Corp. assumes no responsibility for any damage, or infringement of any third-party's rights, originating in the use of any product data, diagrams, charts, programs, algorithms, or circuit application examples contained in these materials, and are subject to change by Renesas Technology Corp. without notice due to product improvements or other reasons. It is therefore recommended that customers contact Renesas Technology Corp. or an authorized Renesas Technology Corp. product distributor for the latest product information before purchasing a product listed herein.

  The information described here may contain technical inaccuracies or typographical errors.

  Renesas Technology Corp. assumes no responsibility for any damage, liability, or other loss rising from these inaccuracies or errors.

  Please also pay attention to information published by Renesas Technology Corp. by various means, including the Renesas Technology Corp. Semiconductor home page (http://www.renesas.com).

  4. When using any or all of the information contained in these materials, including product data, diagrams, charts, programs, and algorithms, please be sure to evaluate all information as a total system before making a final decision on the applicability of the information and products. Renesas Technology Corp. assumes no responsibility for any damage, liability or other loss resulting from the information contained herein.

  5. Renesas Technology Corp. semiconductors are not designed or manufactured for use in a device or system that is used under circumstances in which human life is potentially at stake. Please contact Renesas Technology Corp. or an authorized Renesas Technology

- use.

  6. The prior written approval of Renesas Technology Corp. is necessary to reprint or reproduce in whole or in part these materials.

  7. If these products or technologies are subject to the Japanese export control restrictions, they must be exported under a license from the Japanese government and cannot be imported into a country other than the approved destination.

  Any diversion or reexport contrary to the export control laws and regulations of Japan and/or the country of destination is prohibited.

  8. Please contact Renesas Technology Corp. for further details on these materials or the products contained therein.



**RENESAS SALES OFFICES** 

http://www.renesas.com

Refer to "http://www.renesas.com/en/network" for the latest and detailed information.

**Renesas Technology America, Inc.** 450 Holger Way, San Jose, CA 95134-1368, U.S.A Tel: <1> (408) 382-7500, Fax: <1> (408) 382-7501

# Renesas Technology Europe Limited Dukes Meadow, Millboard Road, Bourne End, Buckinghamshire, SL8 5FH, U.K.

Tel: <44> (1628) 585-100, Fax: <44> (1628) 585-900

Renesas Technology Hong Kong Ltd.
7th Floor, North Tower, World Finance Centre, Harbour City, 1 Canton Road, Tsimshatsui, Kowloon, Hong Kong Tel: <852> 2265-6688, Fax: <852> 2730-6071

**Renesas Technology Taiwan Co., Ltd.** 10th Floor, No.99, Fushing North Road, Taipei, Taiwan Tel: <886> (2) 2715-2888, Fax: <886> (2) 2713-2999

Renesas Technology (Shanghai) Co., Ltd. Unit2607 Ruijing Building, No.205 Maoming Road (S), Shanghai 200020, China Tel: <86> (21) 6472-1001, Fax: <86> (21) 6415-2952

Renesas Technology Singapore Pte. Ltd.
1 Harbour Front Avenue, #06-10, Keppel Bay Tower, Singapore 098632 Tel: <65> 6213-0200, Fax: <65> 6278-8001