Thyristor Module
Thyristor/Diode Modules

PSKT 72 /PSKH 72

I_{TRMS} = 2x 180 A
I_{TAVM} = 2x 115 A
V_{RRM} = 800 - 1800 V

Preliminary Data Sheet

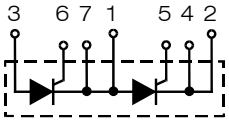
V_{RSM} V_{DSM}	V_{RRM} V_{DRM}	Type			
V	V	Version 1		Version 8	
900	800	PSKT 72/08io1	--	PSKT 72/08io8	PSKH 72/08io8
1300	1200	PSKT 72/12io1	PSKH 72/12io1	PSKT 72/12io8	PSKH 72/12io8
1500	1400	PSKT 72/14io1	--	PSKT 72/14io8	PSKH 72/14io8
1700	1600	PSKT 72/16io1	PSKH 72/16io1	PSKT 72/16io8	PSKH 72/16io8
1900	1800	PSKT 72/18io1	--	PSKT 72/18io8	PSKH 72/18io8

Symbol	Test Conditions		Maximum Ratings	
I_{TRMS}, I_{FRMS}	$T_{VJ} = T_{VJM}$		180	A
I_{TAVM}, I_{FAVM}	T_C = 63°C; 180° sine		115	A
	T_C = 85°C; 180° sine		85	A
I_{TSM}, I_{FSM}	T_{VJ} = 45°C;	t = 10 ms (50 Hz), sine	1700	A
	V_R = 0	t = 8.3 ms (60 Hz), sine	1800	A
	$T_{VJ} = T_{VJM}$	t = 10 ms (50 Hz), sine	1540	A
	V_R = 0	t = 8.3 ms (60 Hz), sine	1640	A
$\int i^2dt$	T_{VJ} = 45°C	t = 10 ms (50 Hz), sine	14 450	A^2s
	V_R = 0	t = 8.3 ms (60 Hz), sine	13 500	A^2s
	$T_{VJ} = T_{VJM}$	t = 10 ms (50 Hz), sine	11 850	A^2s
	V_R = 0	t = 8.3 ms (60 Hz), sine	11 300	A^2s
$(di/dt)_{cr}$	$T_{VJ} = T_{VJM}$	repetitive, I_T = 250 A	150	A/µs
	f =50 Hz, t_P =200 µs			
	$V_D = 2/3\ V_{DRM}$			
	I_G = 0.45 A	non repetitive, $I_T = I_{TAVM}$	500	A/µs
	di_G/dt = 0.45 A/µs			
$(dv/dt)_{cr}$	$T_{VJ} = T_{VJM}$;	$V_{DR} = 2/3\ V_{DRM}$	1000	V/µs
	$R_{GK} = \infty$; method 1 (linear voltage rise)			
P_{GM}	$T_{VJ} = T_{VJM}$	t_P = 30 µs	10	W
	$I_T = I_{TAVM}$	t_P = 300 µs	5	W
P_{GAV}			0.5	W
V_{RGM}			10	V
T_{VJ}			-40...+125	°C
T_{VJM}			125	°C
T_{stg}			-40...+125	°C
V_{ISOL}	50/60 Hz, RMS	t = 1 min	3000	V~
	$I_{ISOL} \leq$ 1 mA	t = 1 s	3600	V~
M_d	Mounting torque (M5)		2.5-4.0/22-35	Nm/lb.in.
	Terminal connection torque (M5)		2.5-4.0/22-35	Nm/lb.in.
Weight	Typical including screws		90	g

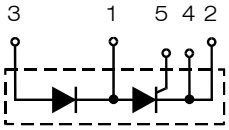
Data according to IEC 60747 and refer to a single thyristor/diode unless otherwise stated.

TO-240 AA

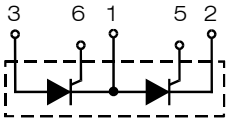
PSKT Version 1



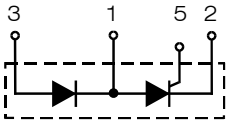
PSKH Version 1



PSKT Version 8



PSKH Version 8



Features

- International standard package, JEDEC TO-240 AA
- **D**irect **C**opper **B**onded Al_2O_3 -ceramic base plate
- Planar passivated chips
- Isolation voltage 3600 V~
- UL registered, E 148688
- Gate-cathode twin pins for version 1

Applications

- DC motor control
- Softstart AC motor controller
- Light, heat and temperature control

Advantages

- Space and weight savings
- Simple mounting with two screws
- Improved temperature and power cycling capability
- Reduced protection circuits

POWERSEM GmbH, Walpersdorfer Str. 53
D - 91126 Schwabach
Phone: 09122 - 9764 - 0 FAX: 09122 - 9764 - 20

Symbol	Test Conditions		Characteristic Values	
I_{RRM}, I_{DRM}	$T_{VJ} = T_{VJM}$; $V_R = V_{RRM}$; $V_D = V_{DRM}$		5	mA
V_T, V_F	I_T, I_F = 300 A; T_{VJ} = 25°C		1.74	V
V_{T0}	For power-loss calculations only (T_{VJ} = 125°C)		0.85	V
r_T			3.2	mΩ
V_{GT}	V_D = 6 V;	T_{VJ} = 25°C	2.5	V
		T_{VJ} = -40°C	2.6	V
I_{GT}	V_D = 6 V;	T_{VJ} = 25°C	150	mA
		T_{VJ} = -40°C	200	mA
V_{GD}	$T_{VJ} = T_{VJM}$;	V_D = 2/3 V_{DRM}	0.2	V
I_{GD}			10	mA
I_L	T_{VJ} = 25°C; t_P = 10 µs; V_D = 6 V I_G = 0.45 A; di_G/dt = 0.45 A/µs		450	mA
I_H	T_{VJ} = 25°C; V_D = 6 V; R_{GK} = ∞		200	mA
t_{gd}	T_{VJ} = 25°C; V_D = 1/2 V_{DRM} I_G = 0.45 A; di_G/dt = 0.45 A/µs		2	µs
t_q	$T_{VJ} = T_{VJM}$; I_T = 150 A, t_P = 200 µs; -di/dt = 10 A/µs V_R = 100 V; dv/dt = 20 V/µs; V_D = 2/3 V_{DRM}	typ.	185	µs
Q_S	$T_{VJ} = T_{VJM}$; I_T, I_F = 50 A, -di/dt = 6 A/µs		170	µC
I_{RM}			45	A
R_{thJC}	per thyristor/diode; DC current	other values see Fig. 8/9	0.3	K/W
	per module		0.15	K/W
R_{thJK}	per thyristor/diode; DC current		0.5	K/W
	per module		0.25	K/W
d_S	Creepage distance on surface		12.7	mm
d_A	Strike distance through air		9.6	mm
a	Maximum allowable acceleration		50	m/s^2

Optional accessories for module-type PSKT 72 version 1
Keyed gate/cathode twin plugs with wire length = 350 mm, gate = yellow, cathode = red
Type **ZY 200L** (L = Left for pin pair 4/5) } UL 758, style 1385,
Type **ZY 200R** (R = right for pin pair 6/7) } CSA class 5851, guide 460-1-1

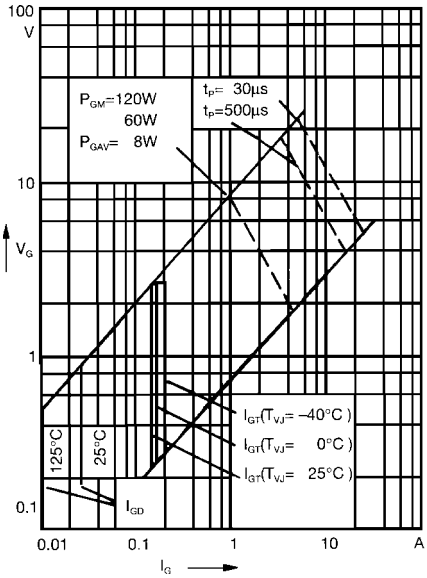


Fig. 1 Gate trigger characteristics

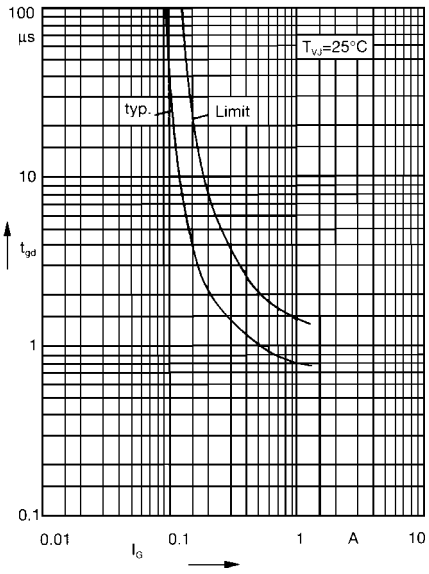
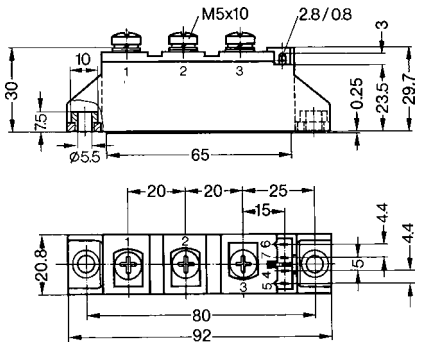


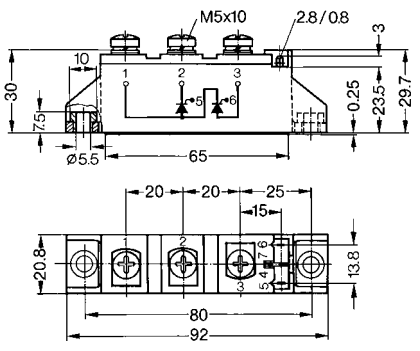
Fig. 2 Gate trigger delay time

Dimensions in mm (1 mm = 0.0394")

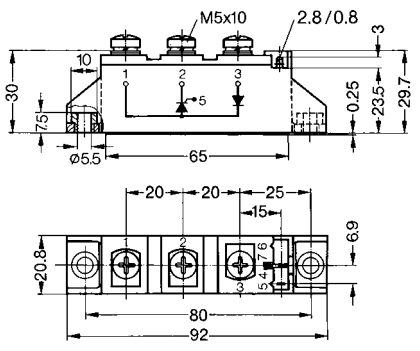
PSKT/PSKH Version 1



PSKT Version 8



PSKH Version 8



POWERSEM GmbH, Walpersdorfer Str. 53
D - 91126 Schwabach
Phone: 09122 - 9764 - 0 FAX: 09122 - 9764 - 20